



McIDAS

Man computer Interactive Data Access System

WIDE WORD Workstation Hardware Manual

Appendix A Schematic Drawings (serial numbers 032 and later)

Issued March 1991

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Revised 1992



Space Science and Engineering Center
University of Wisconsin - Madison
1225 West Dayton Street
Madison, WI 53706
Telephone 608-262-2455
TWX 608-263-6738

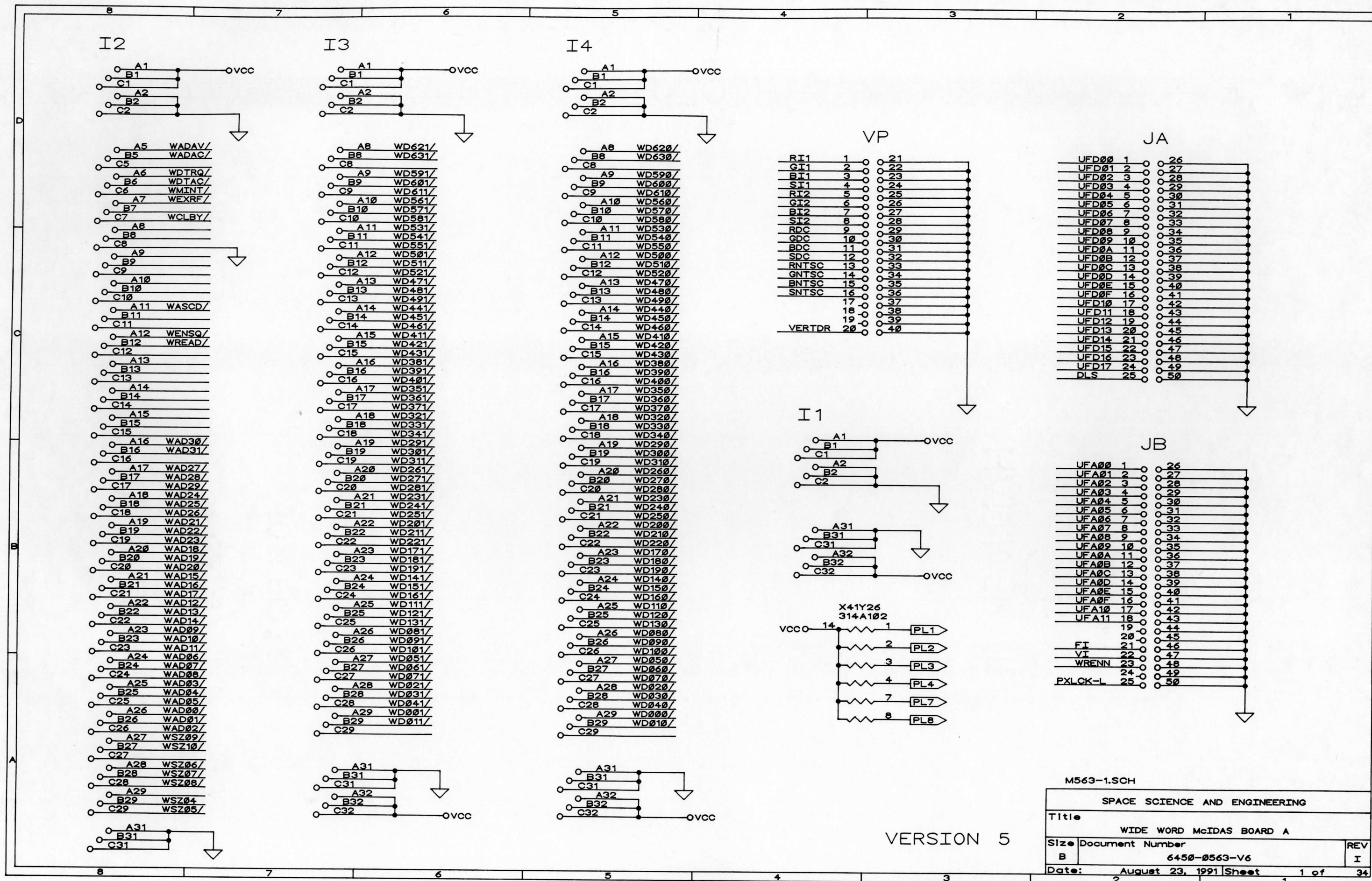
WIDE WORD McIDAS

BOARD A

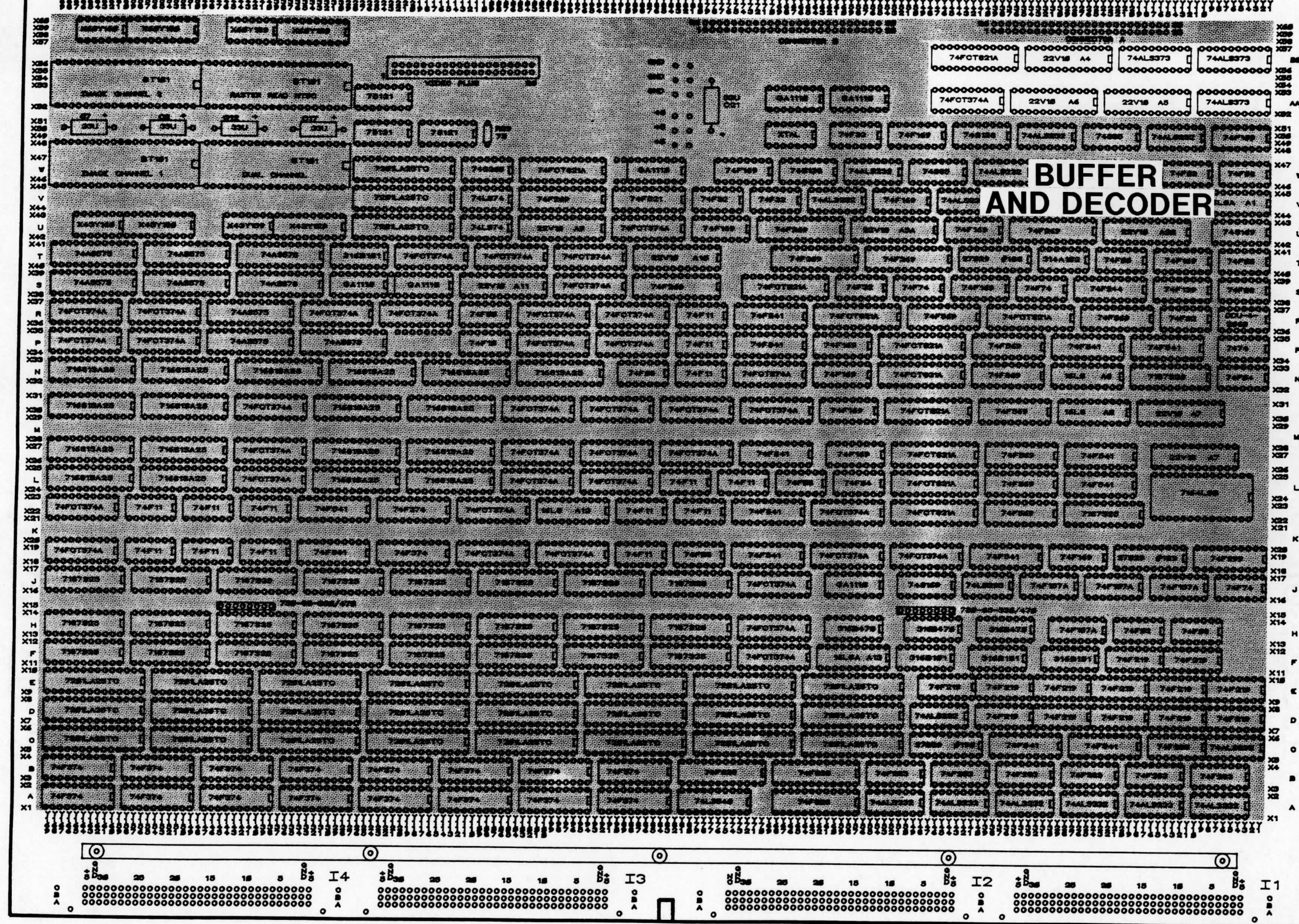
VERSION 6.0

**SPACE SCIENCE AND ENGINEERING
UNIVERSITY OF WISCONSIN**

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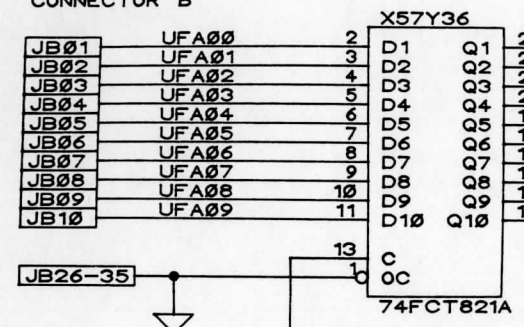


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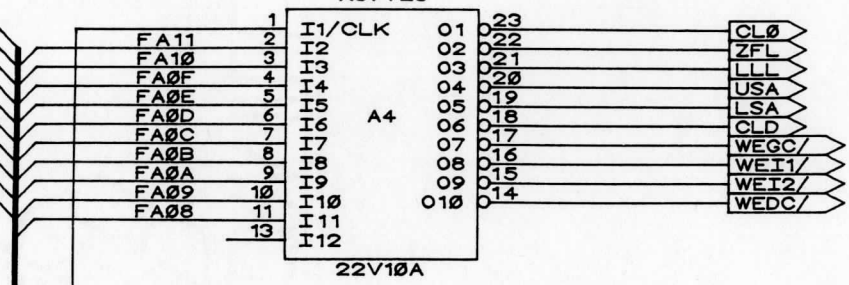
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M564ASS.SCH

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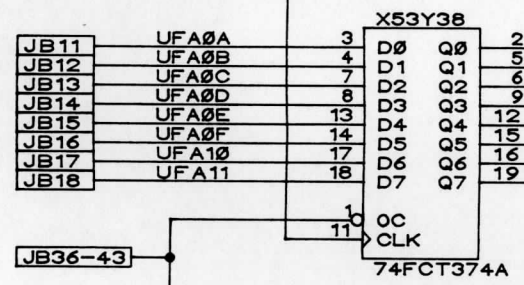
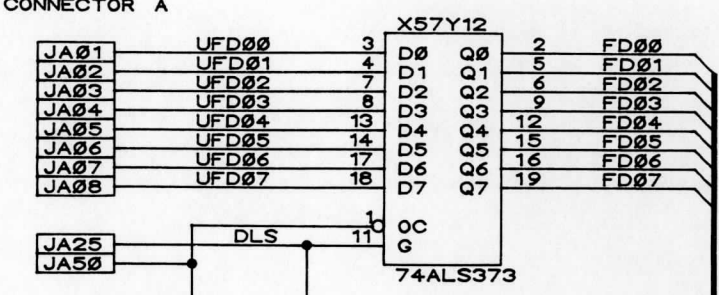
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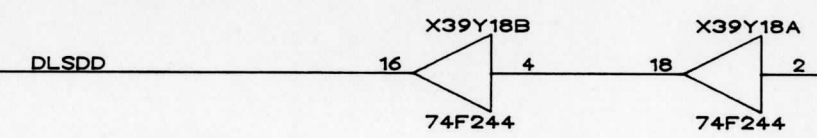
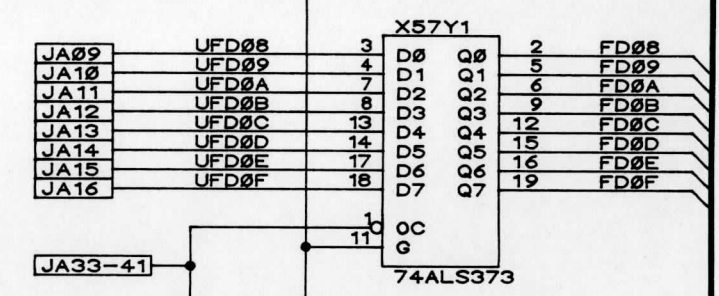
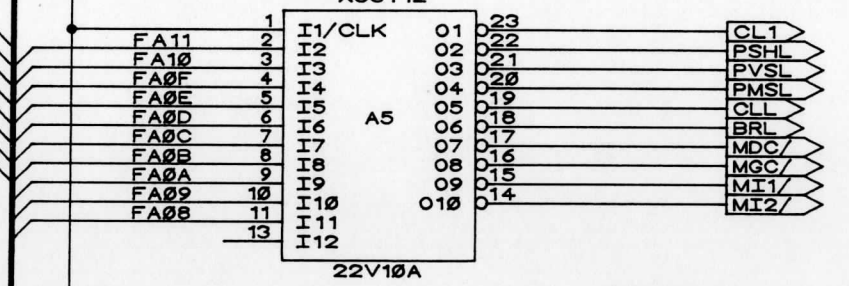
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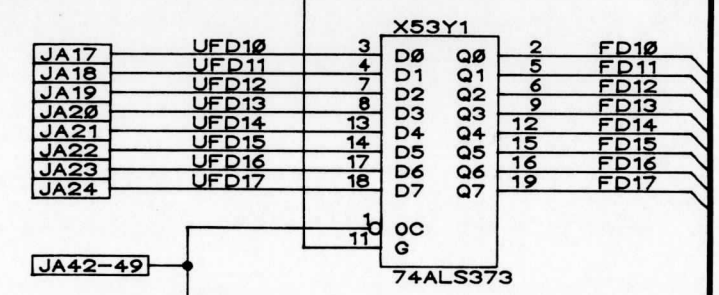
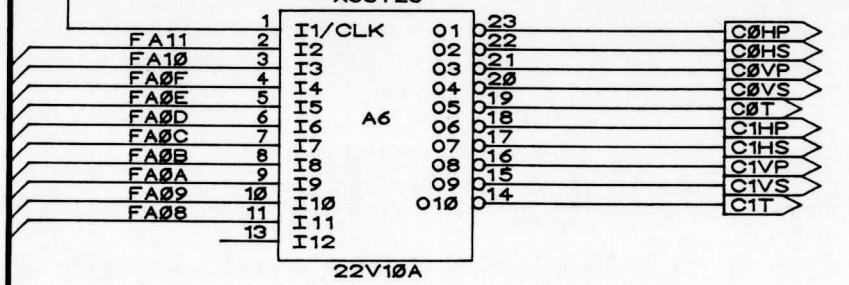
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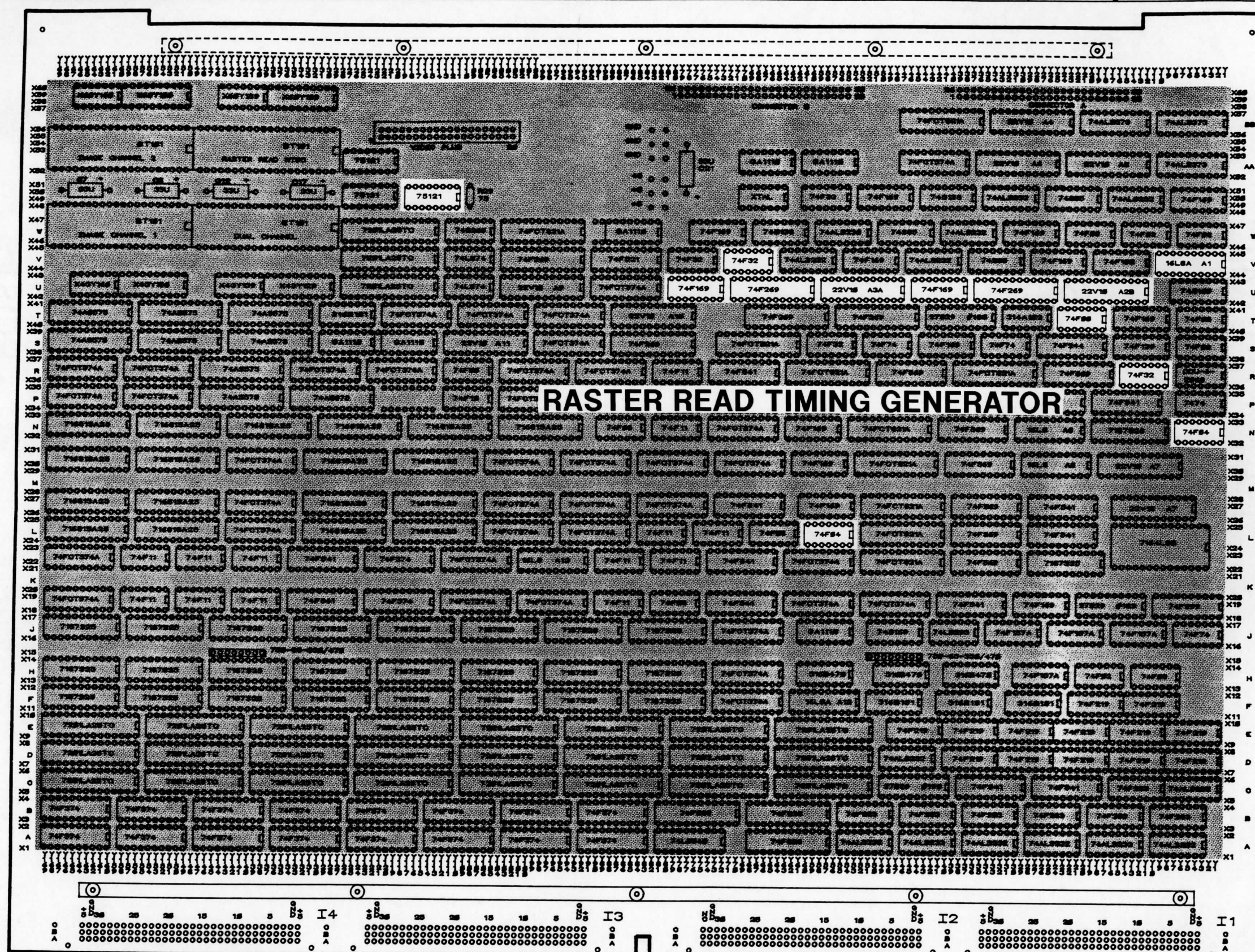


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VERSION 6

M563-2.SCH

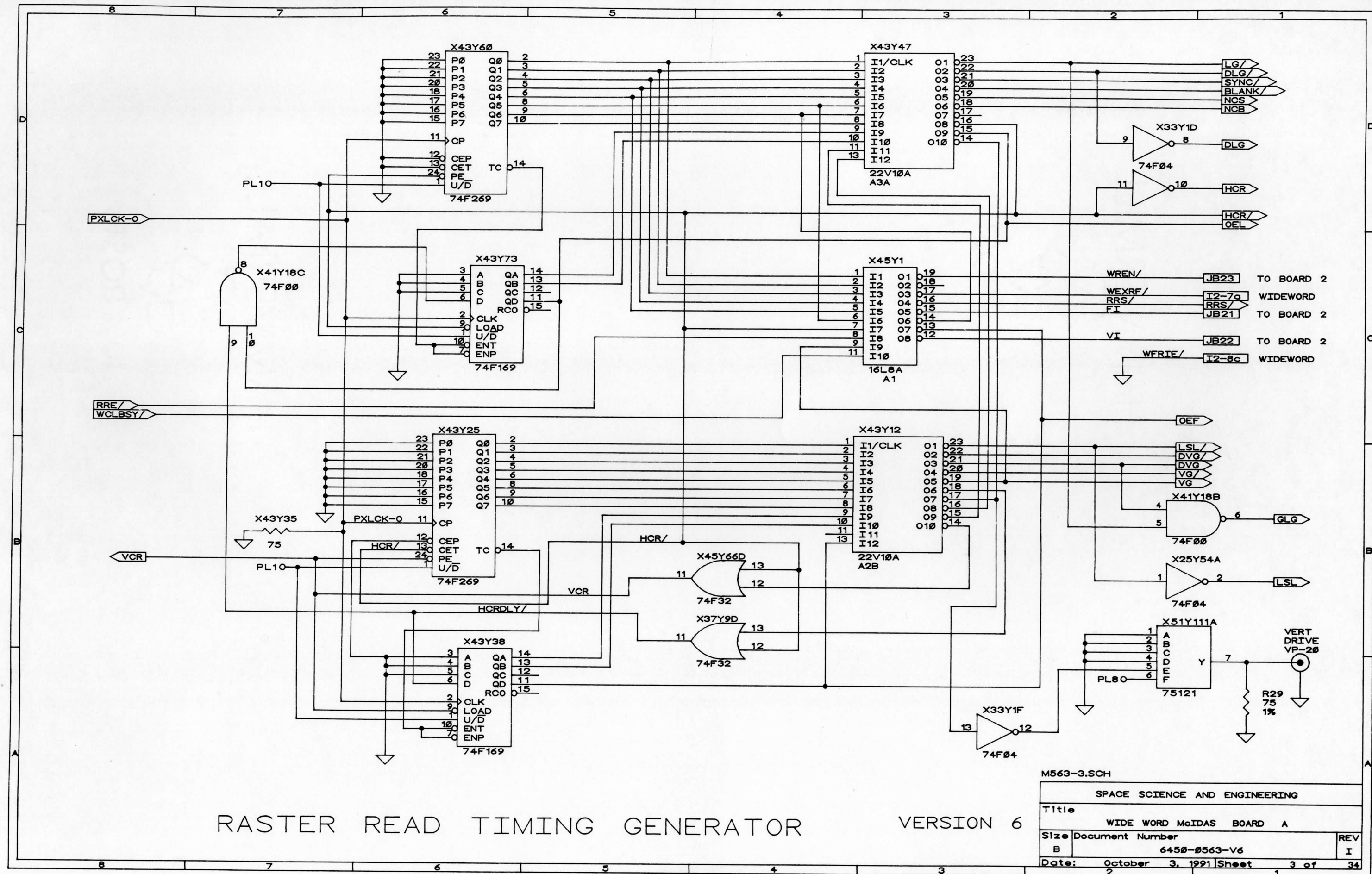
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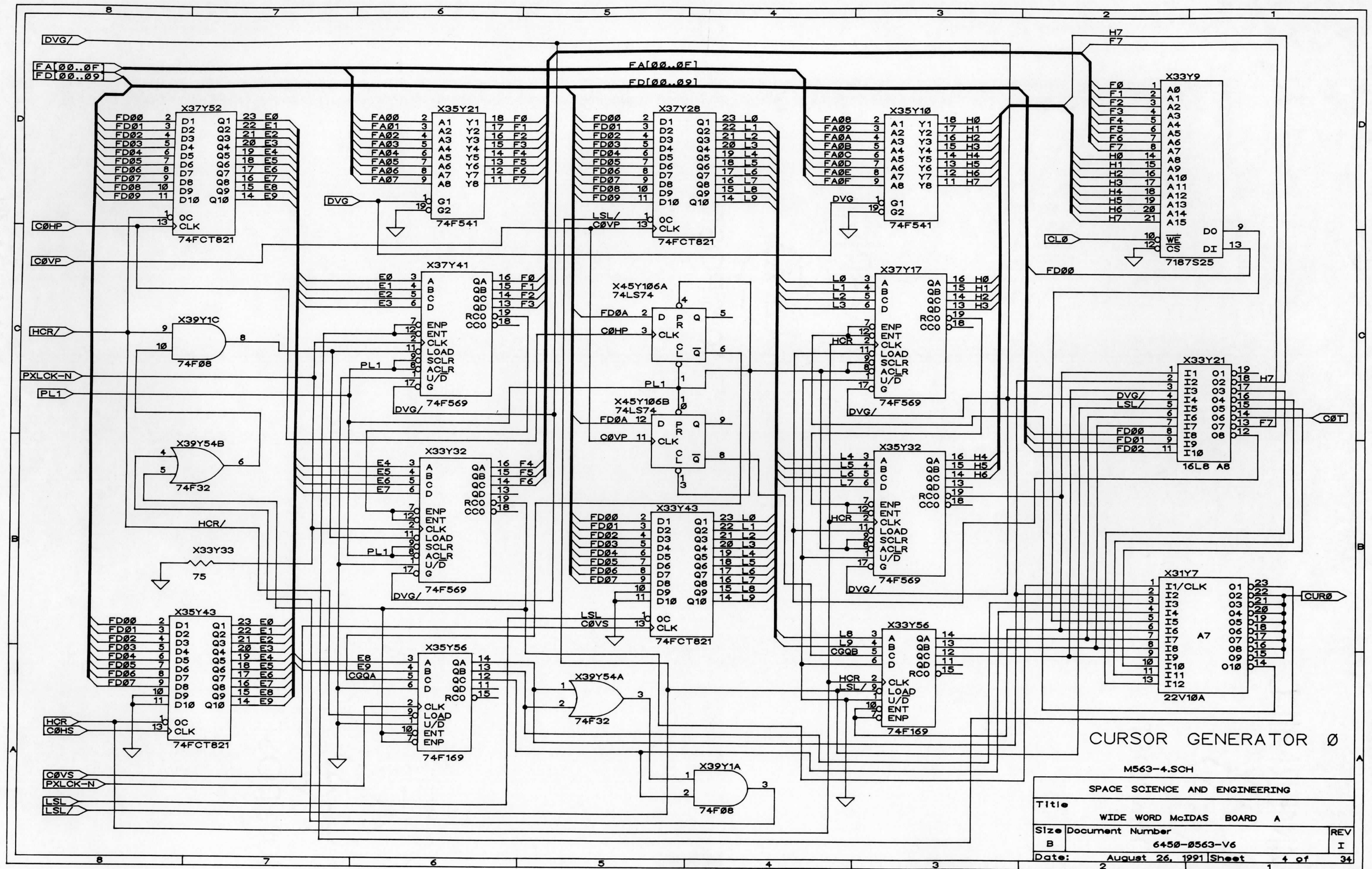
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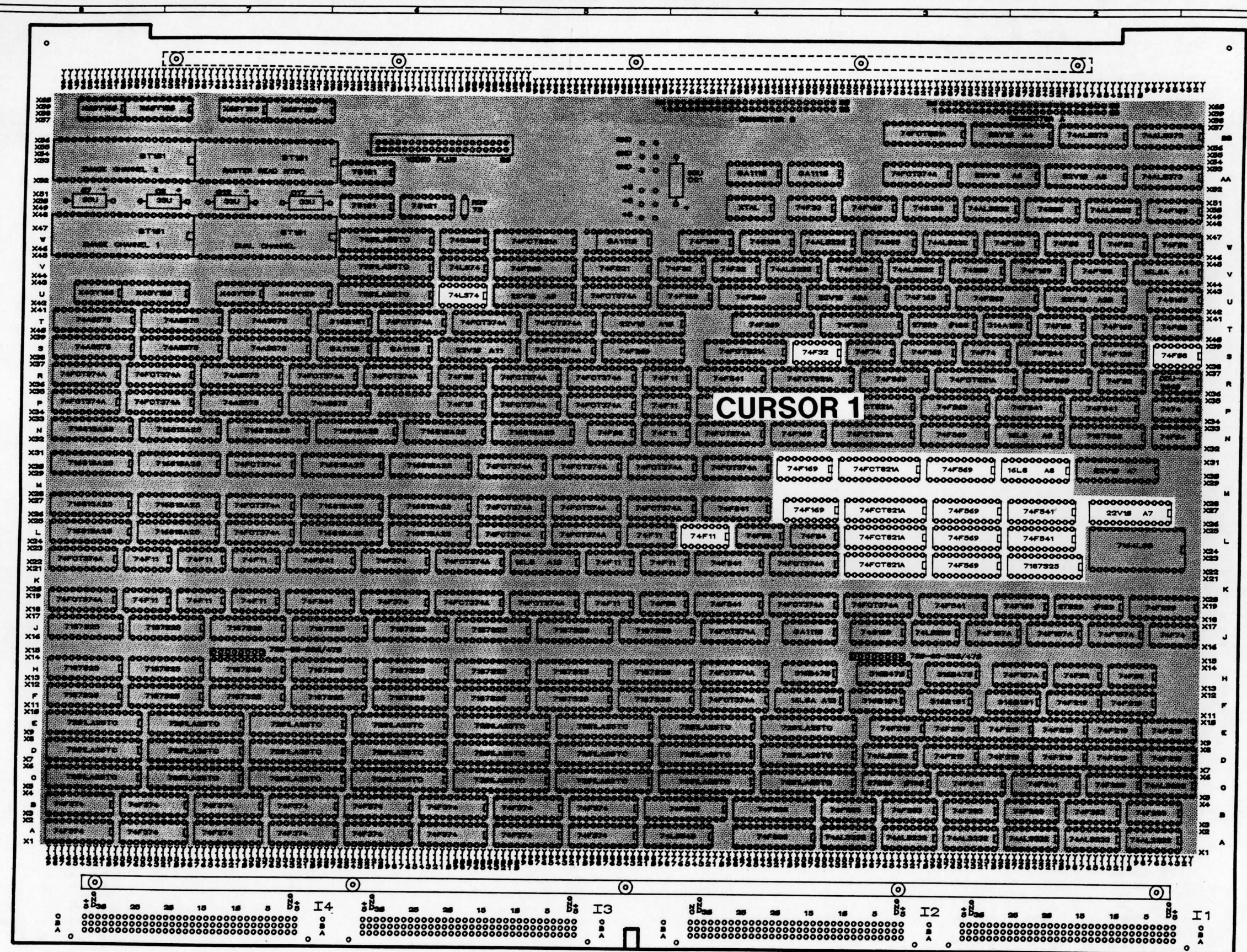
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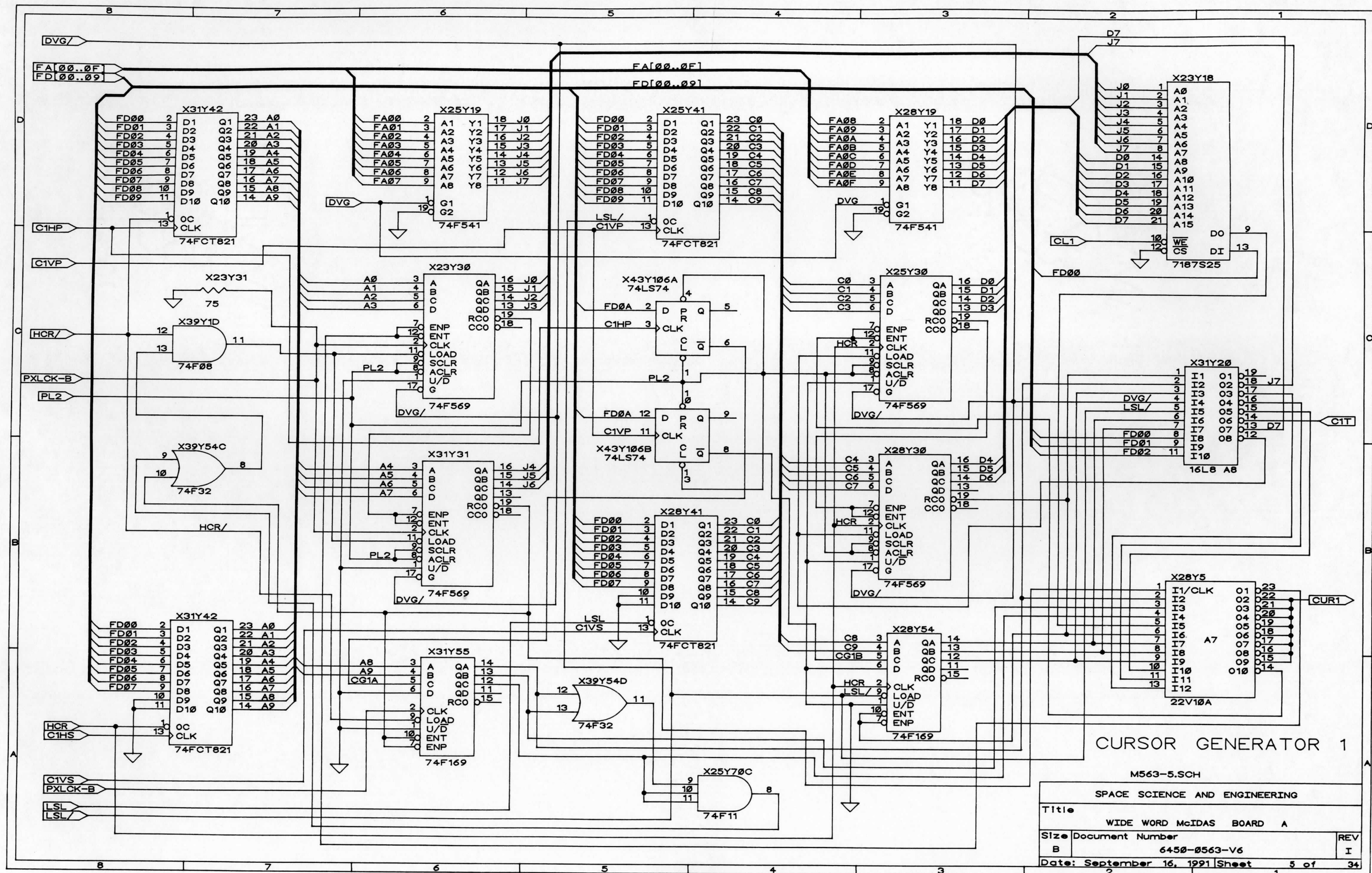


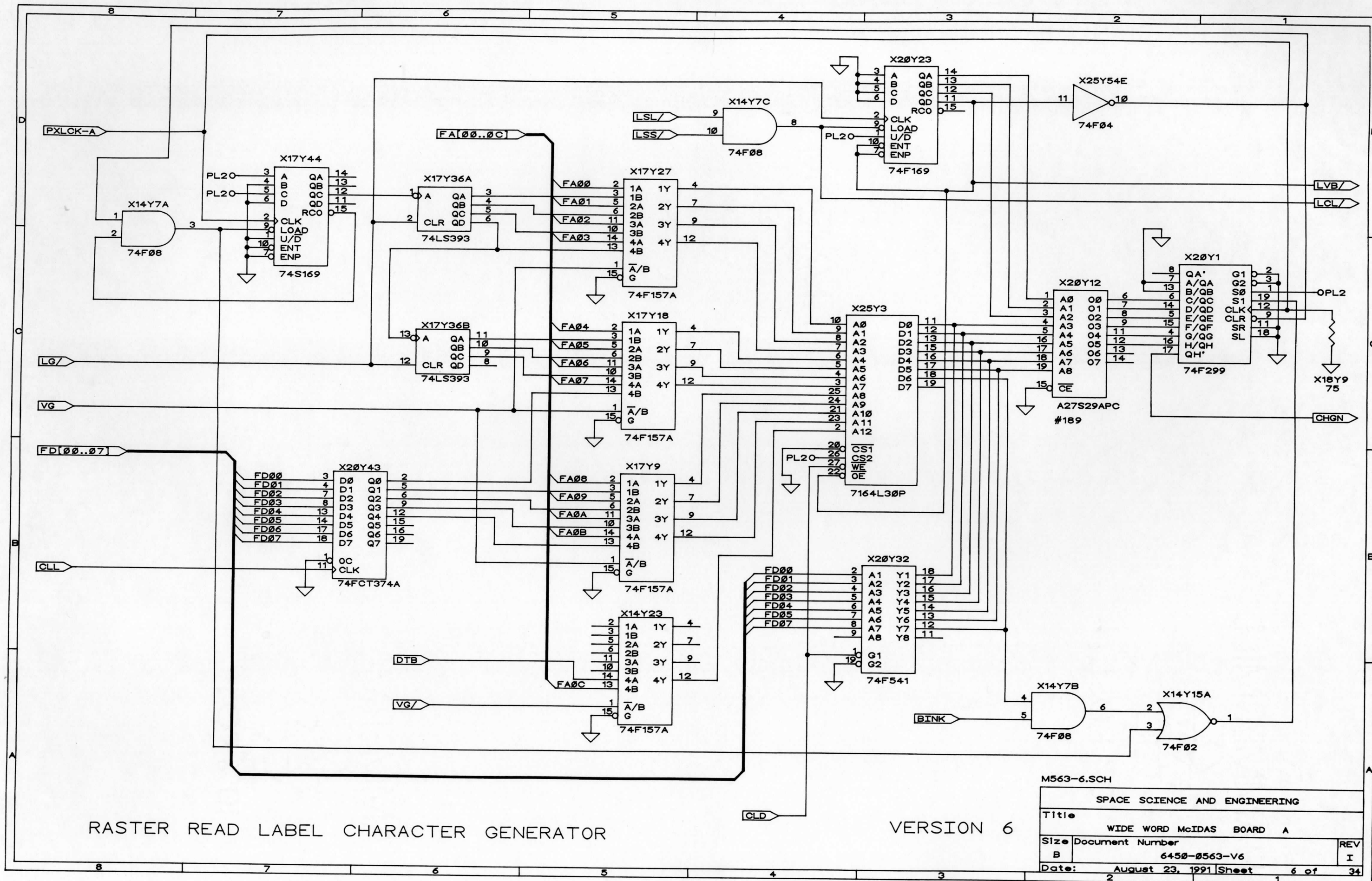


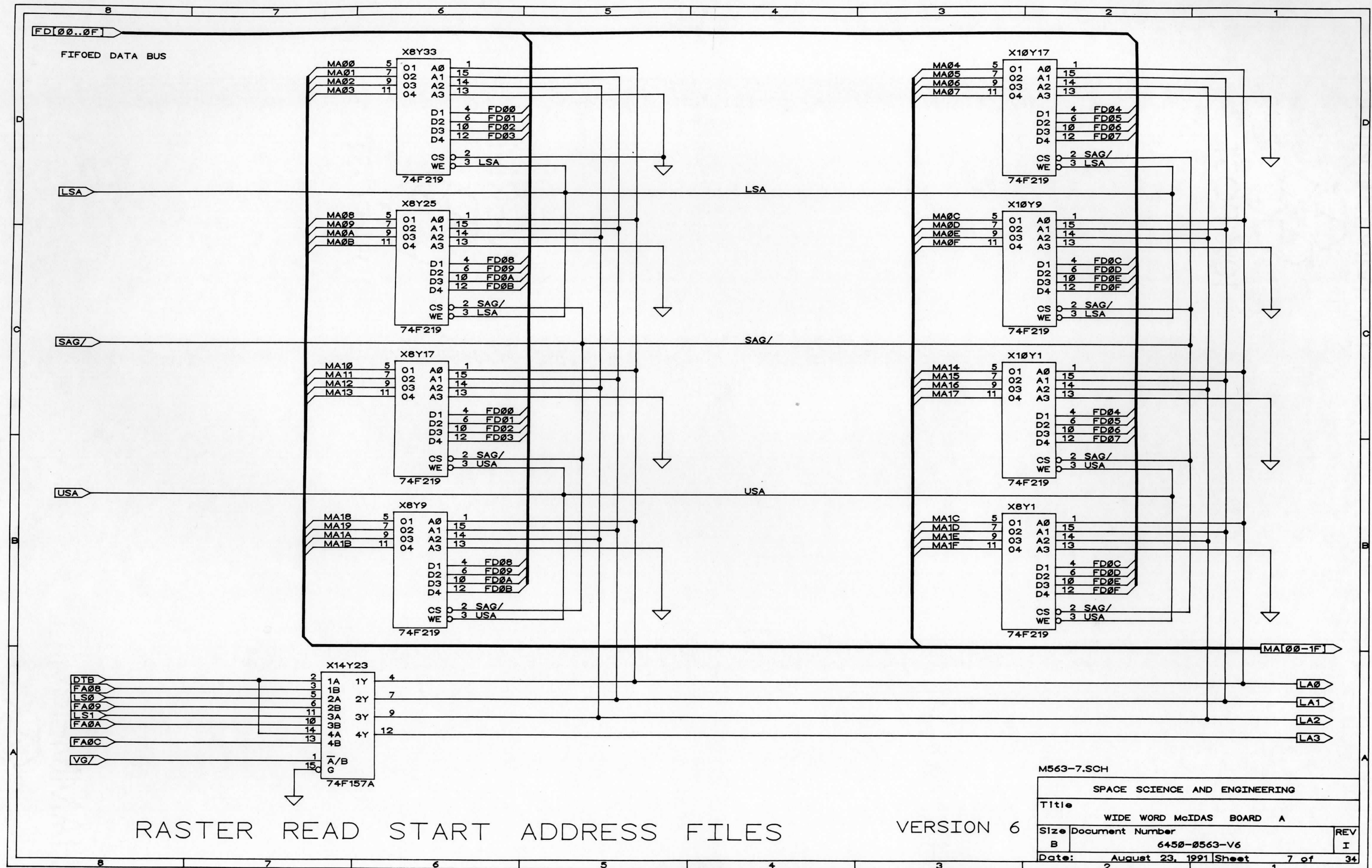
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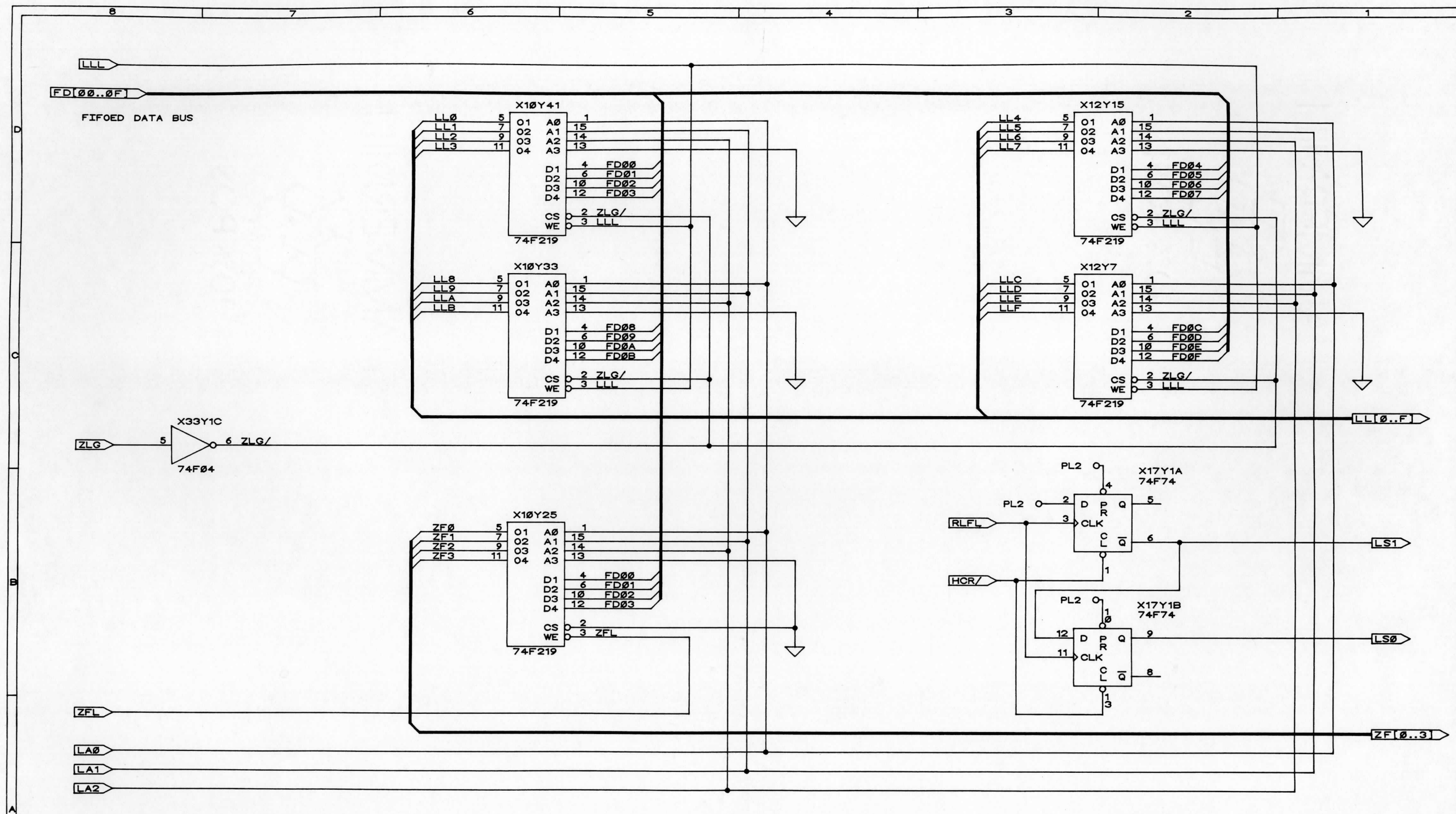
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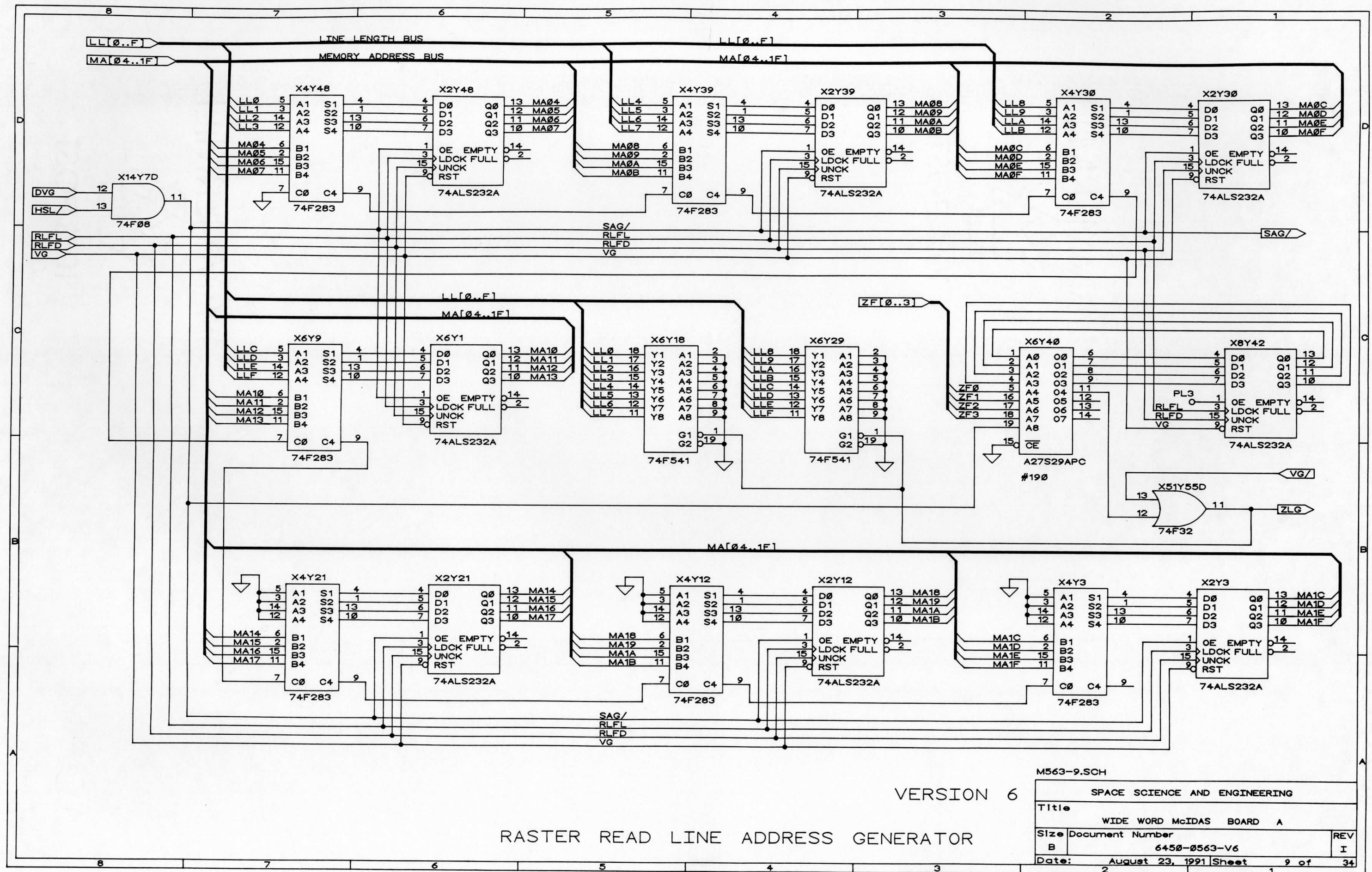


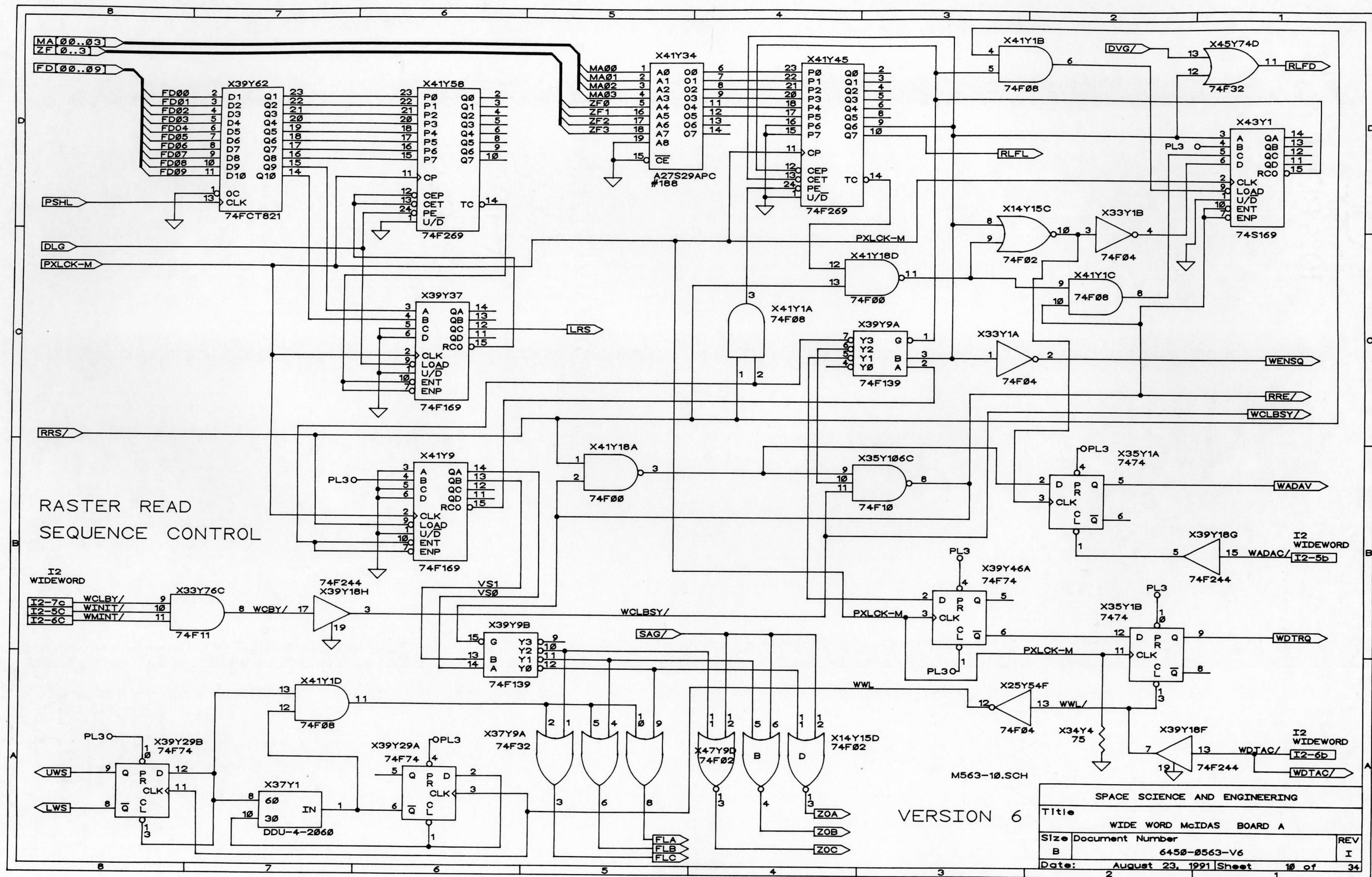


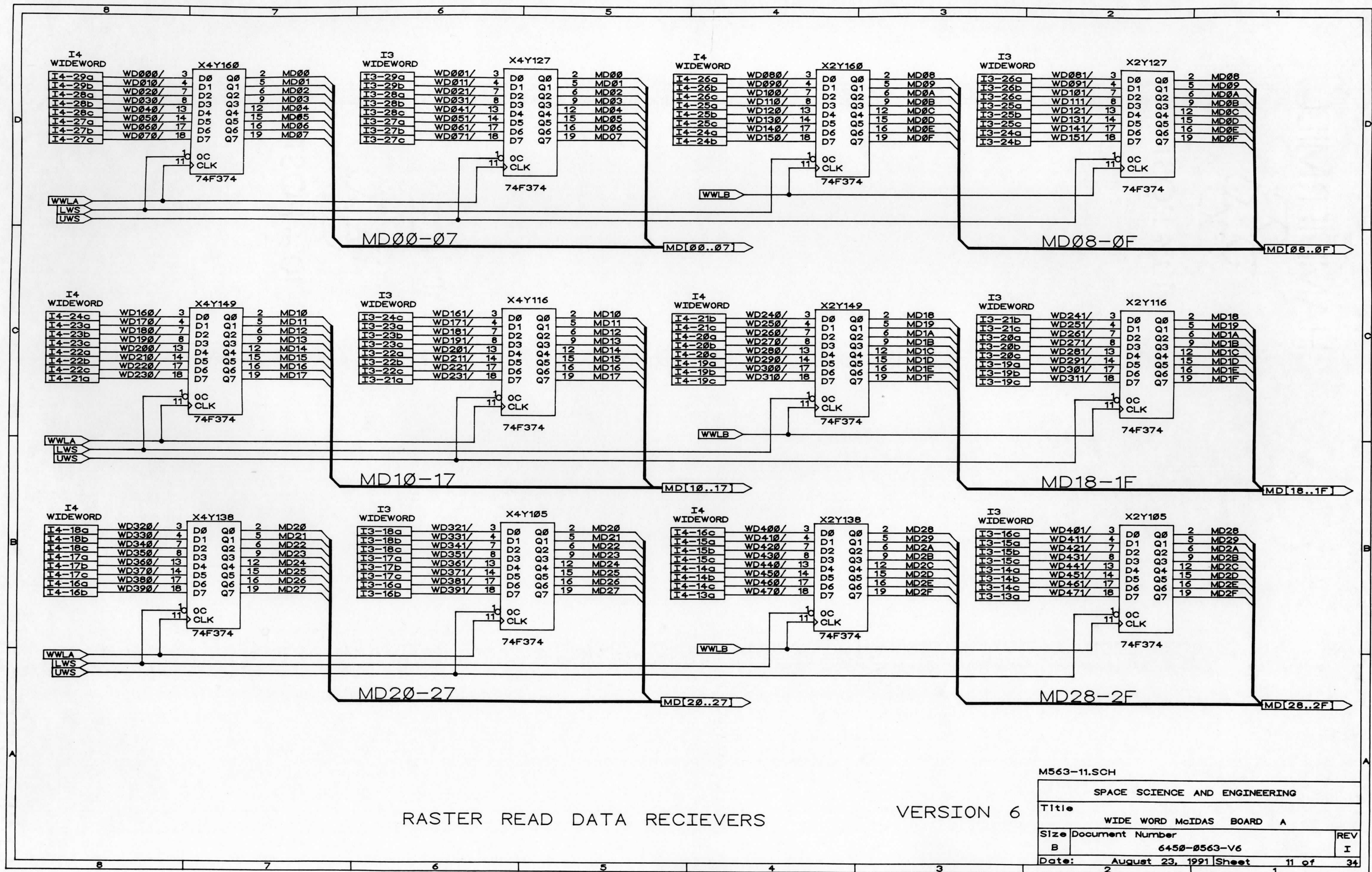
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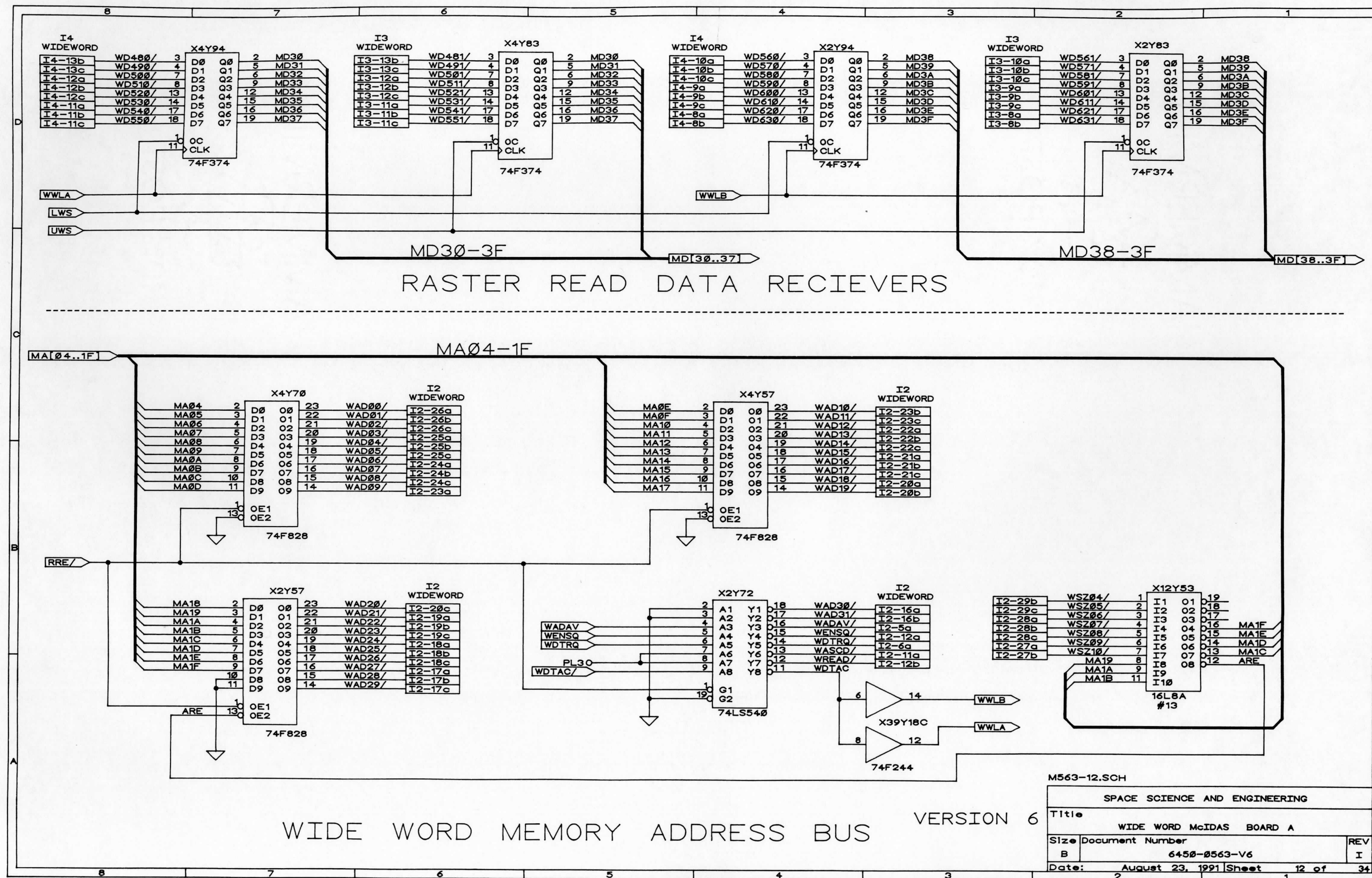
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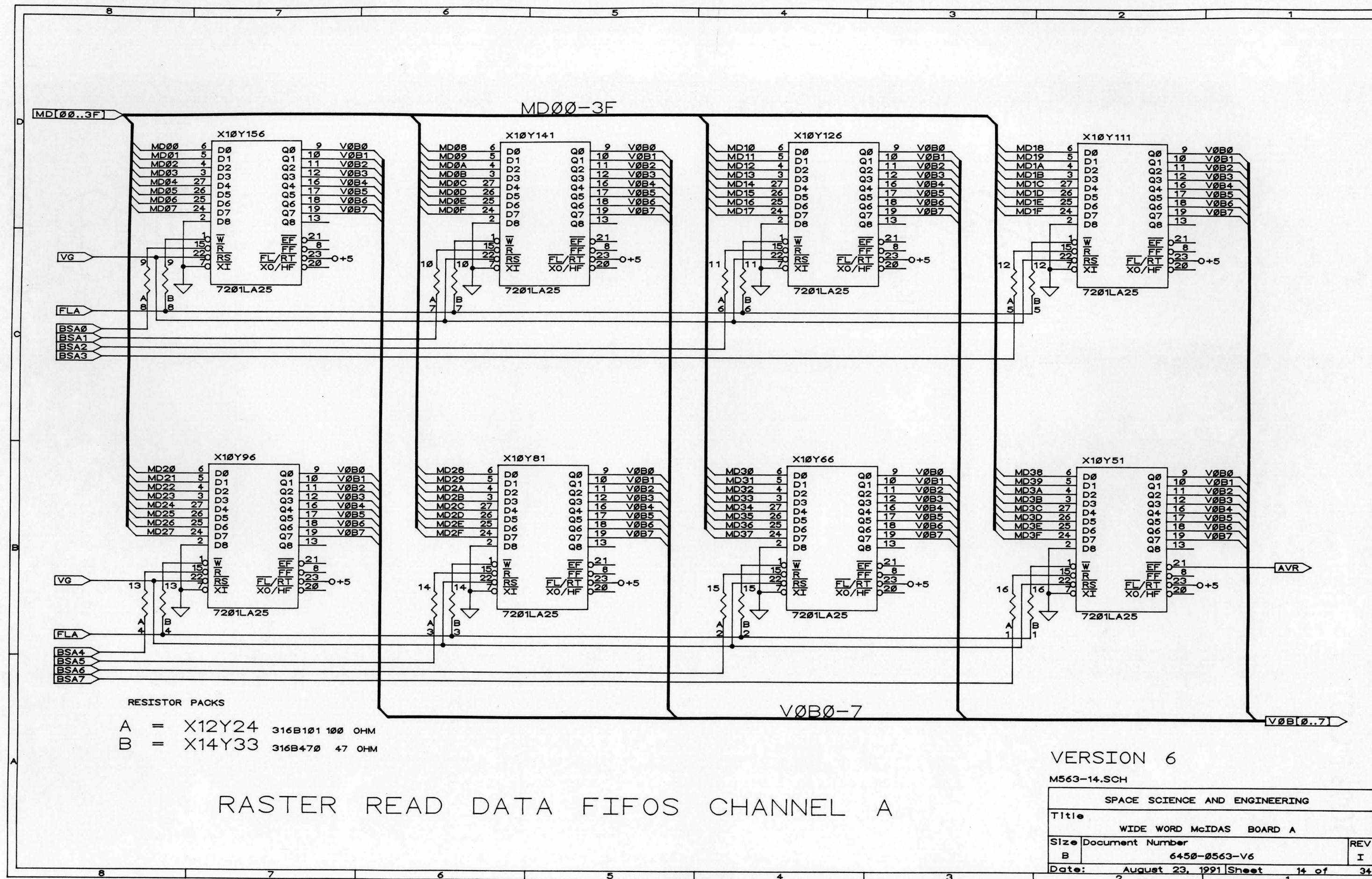
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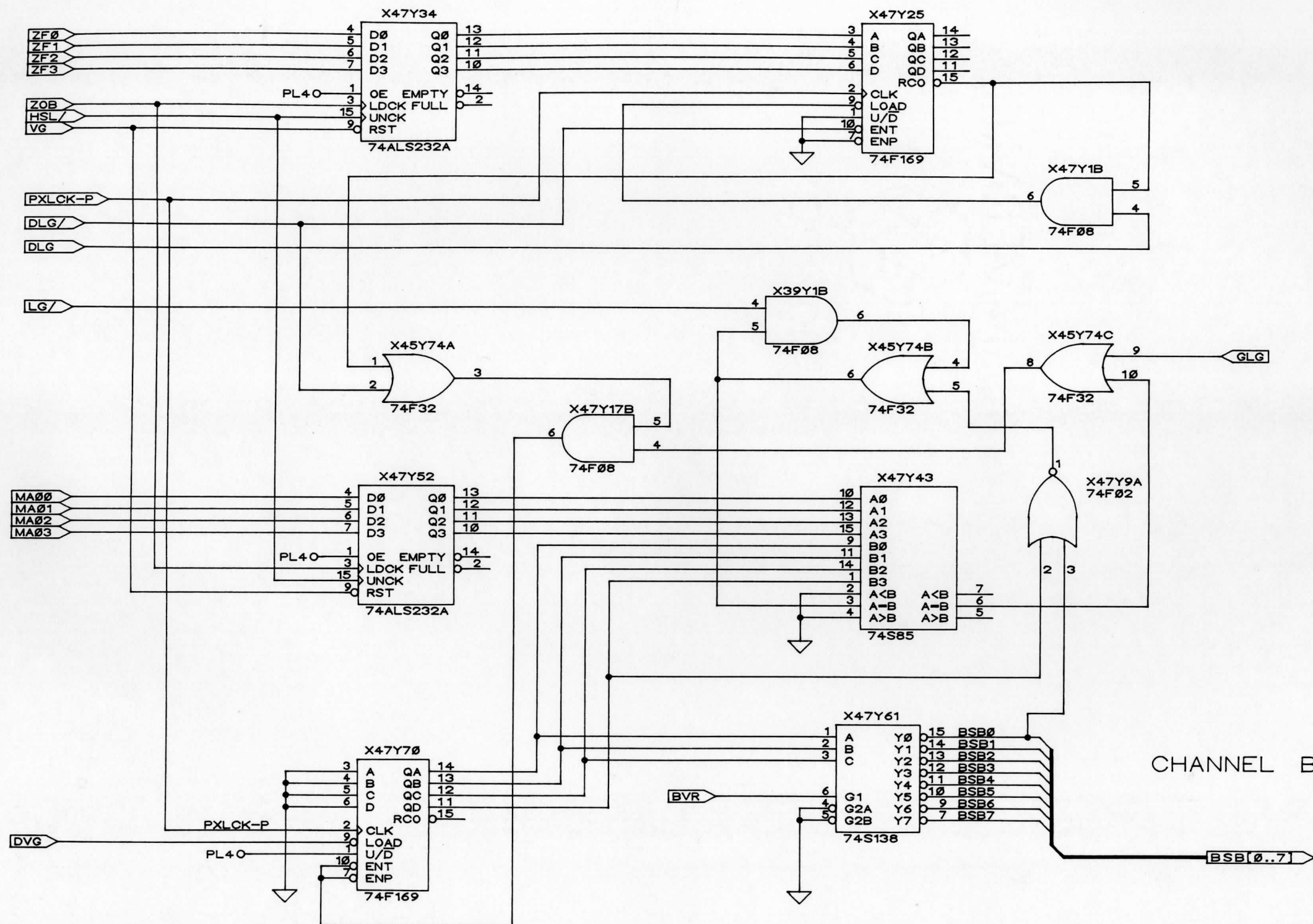












VERT INTERVAL ----> INDEX TO ZERO (MOD 16)

HORIZ INTERVAL ----> INDEX TO START PIXEL OFFSET

ACTIVE LINE ----> COUNT ON ZOOM CARRY

RASTER READ FIFO PIXEL SELECTOR

VERSION 6

M563-15.SCH

SPACE SCIENCE AND ENGINEERING

Title

WIDE WORD McIDAS BOARD A

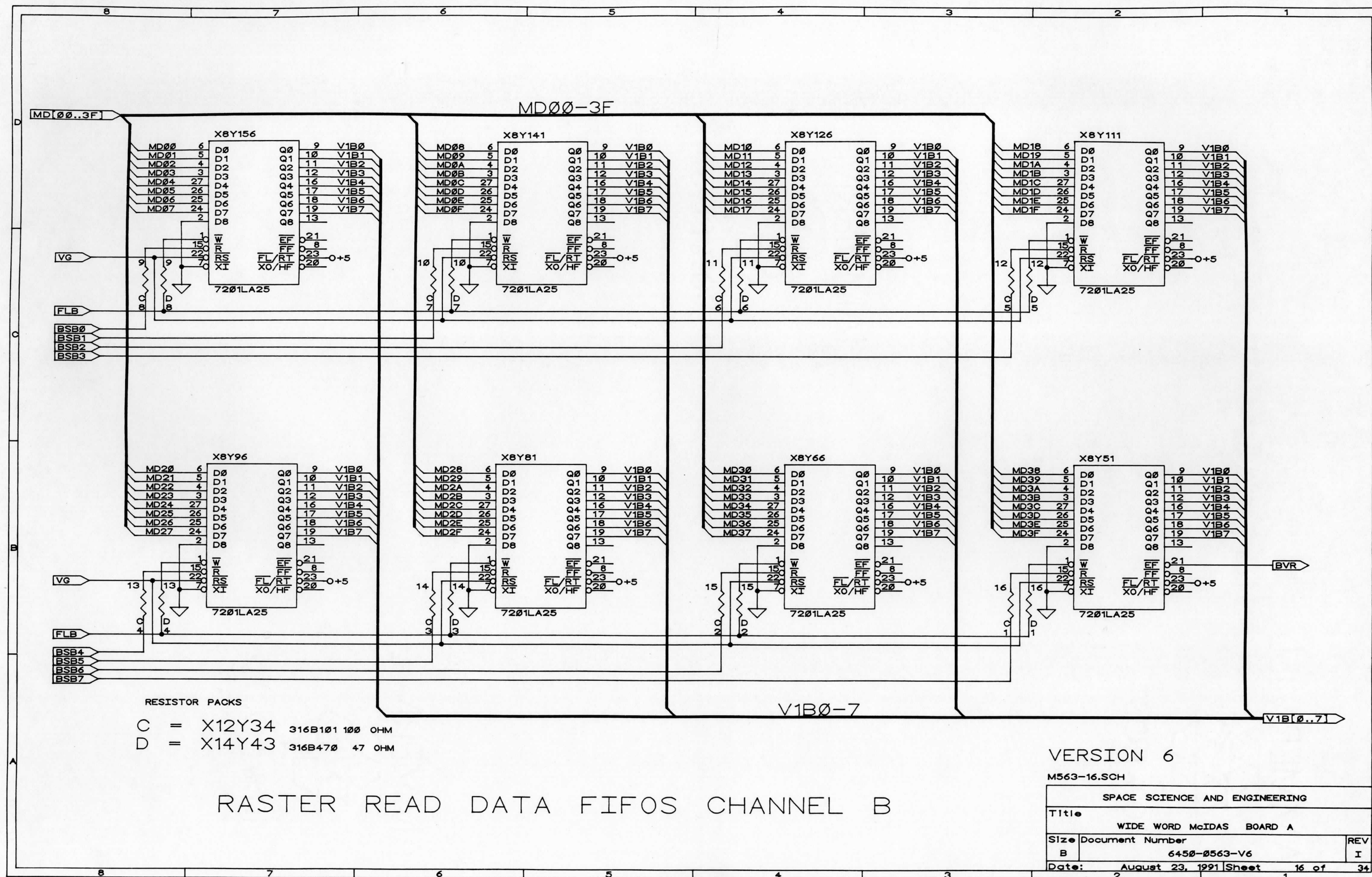
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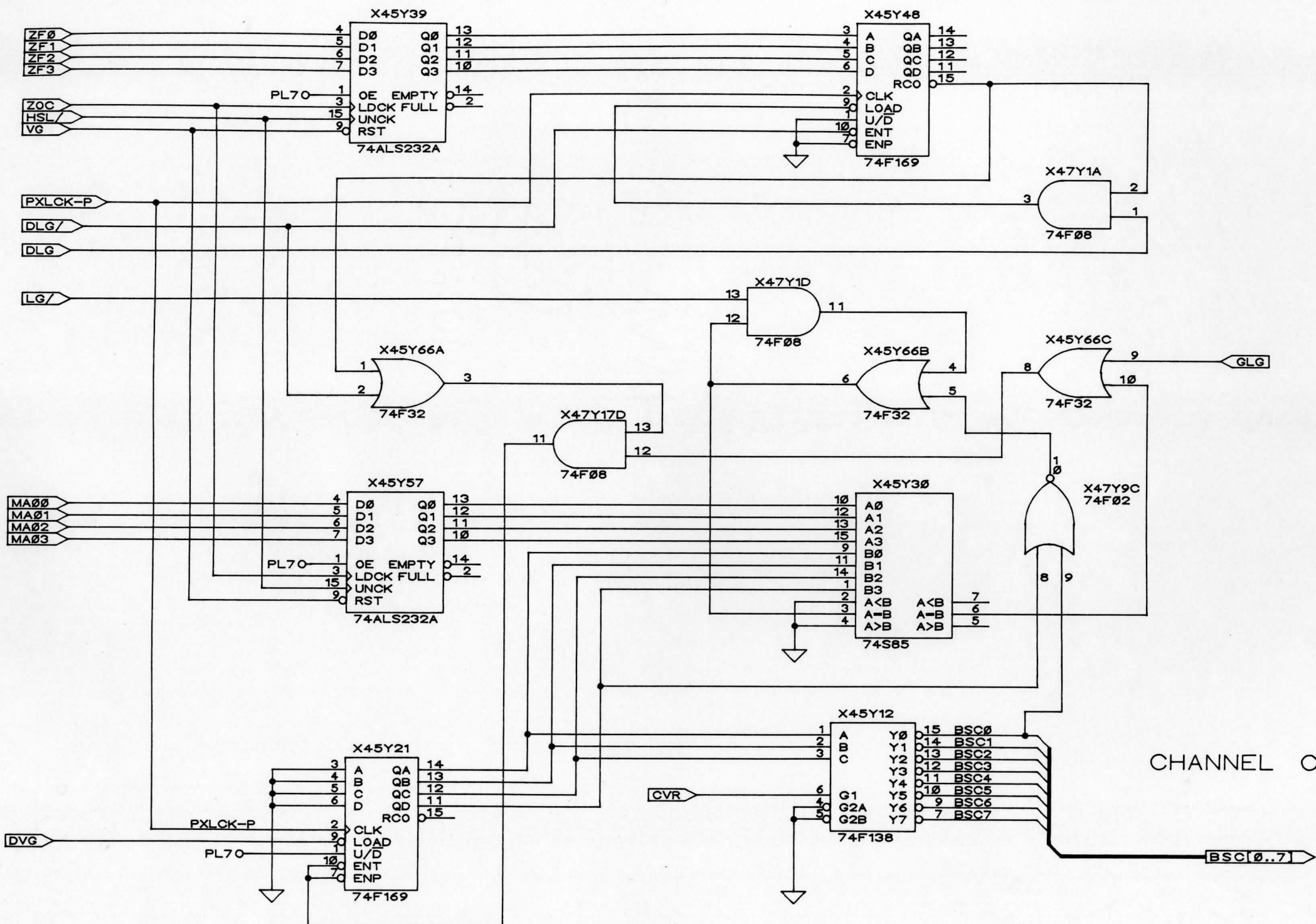
6450-0563-V6

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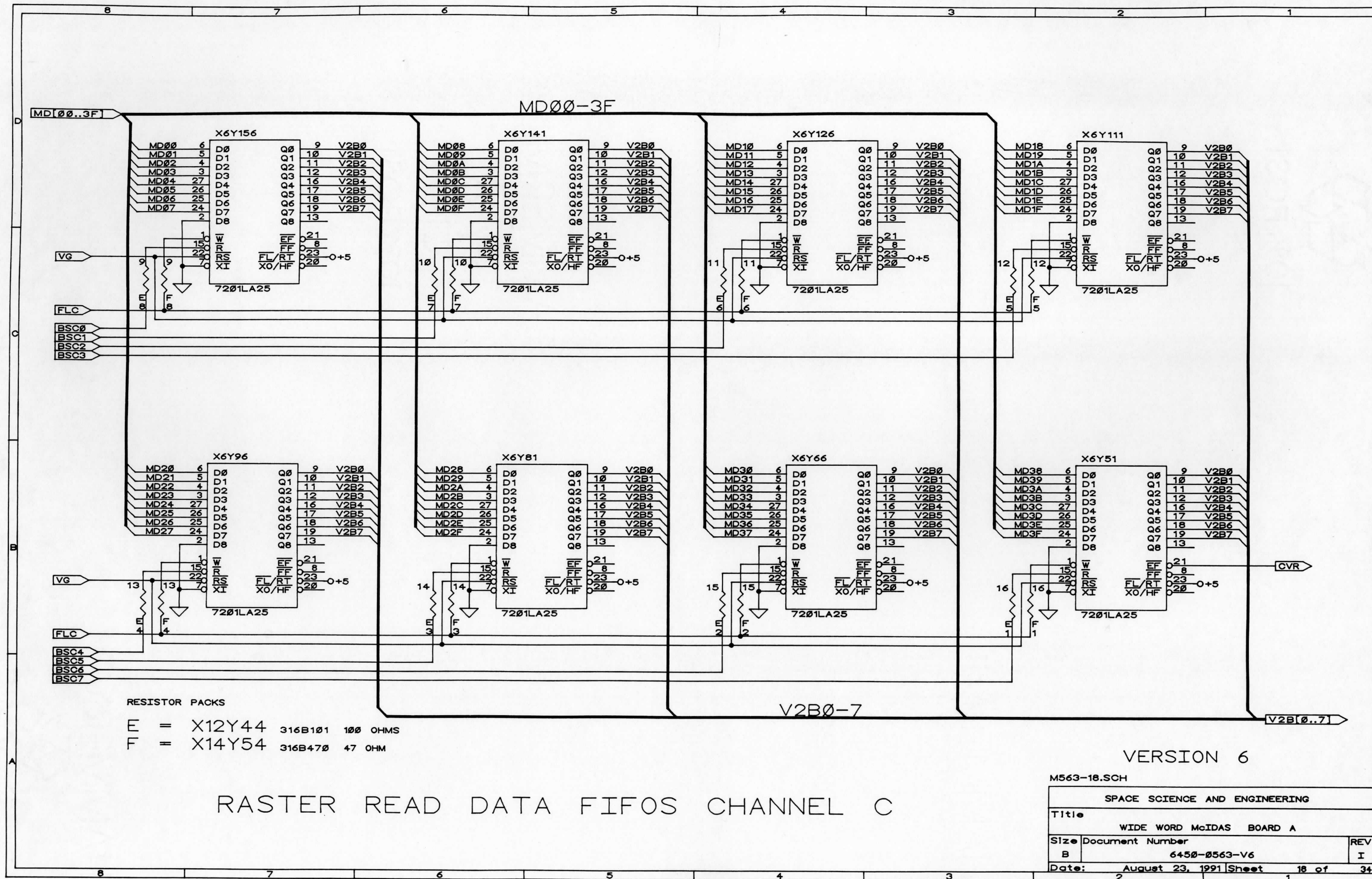
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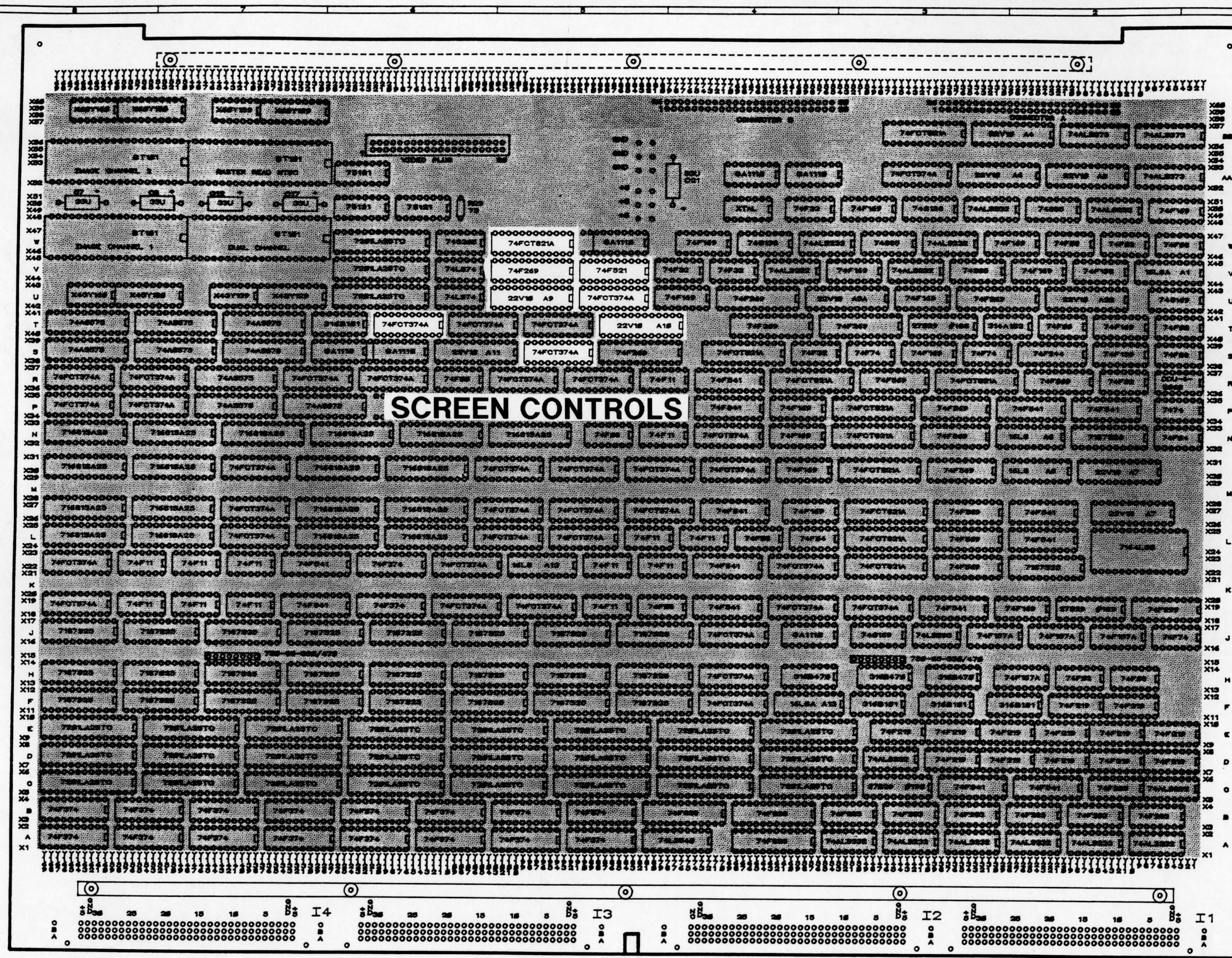
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M563-17.SCH

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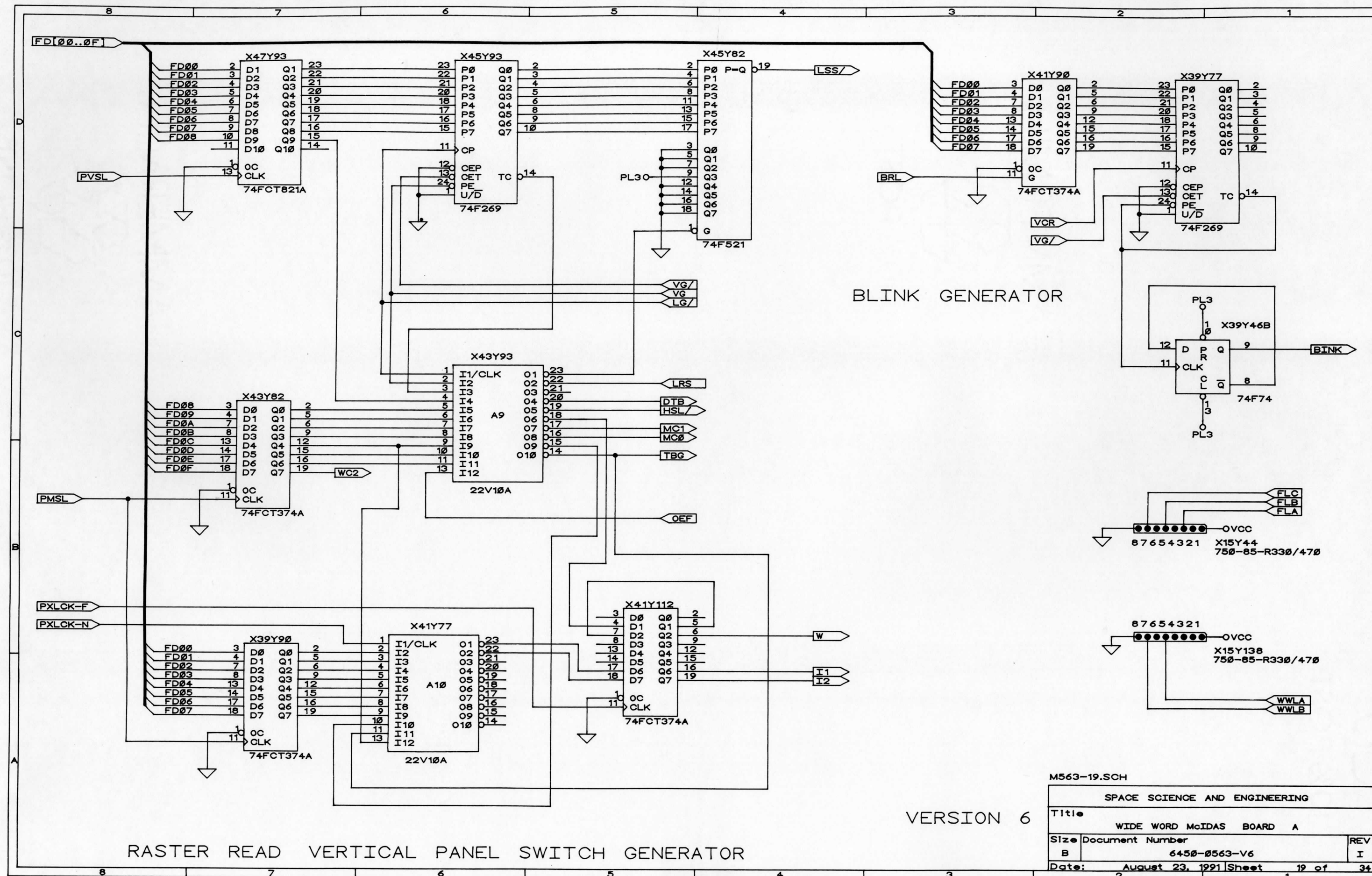




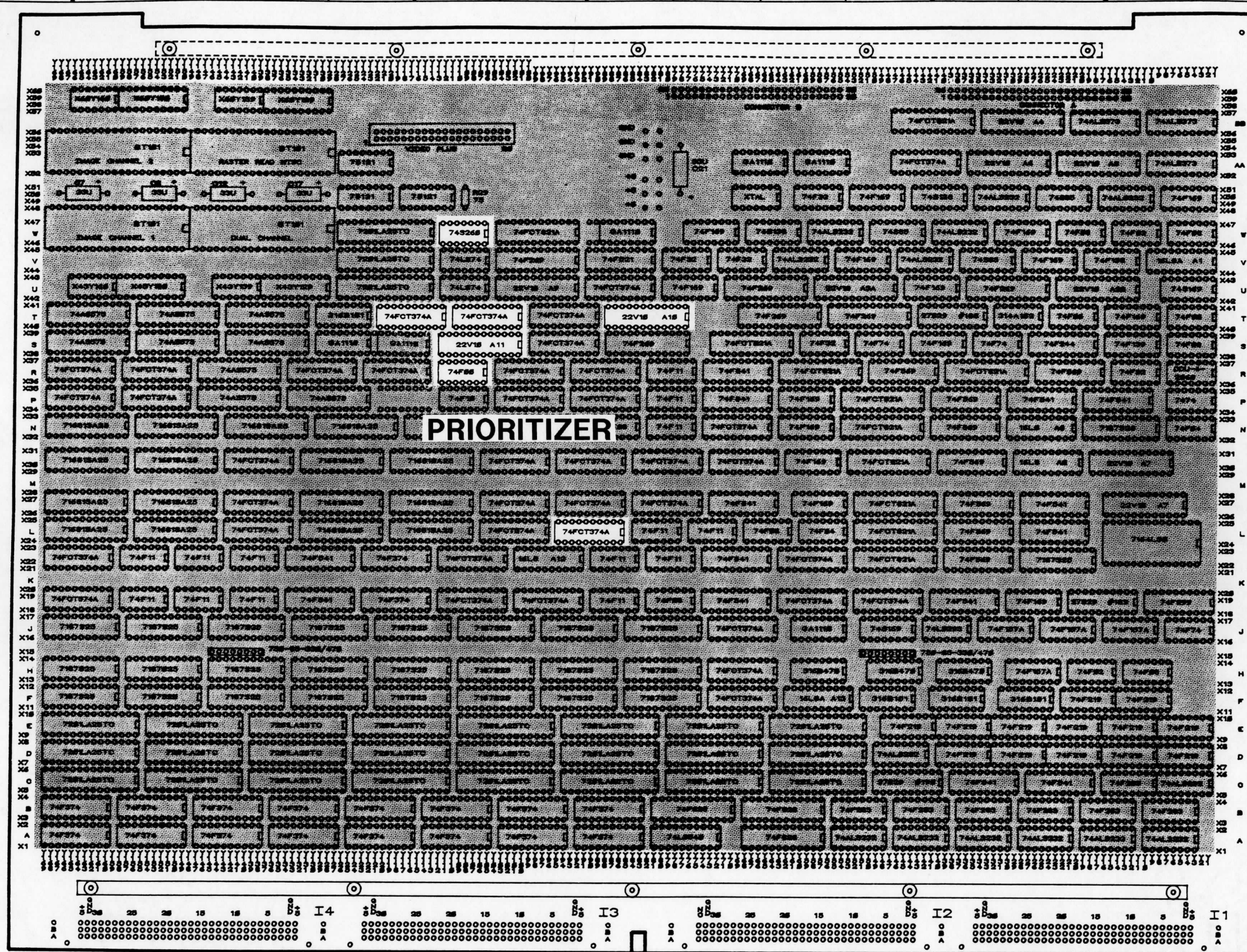
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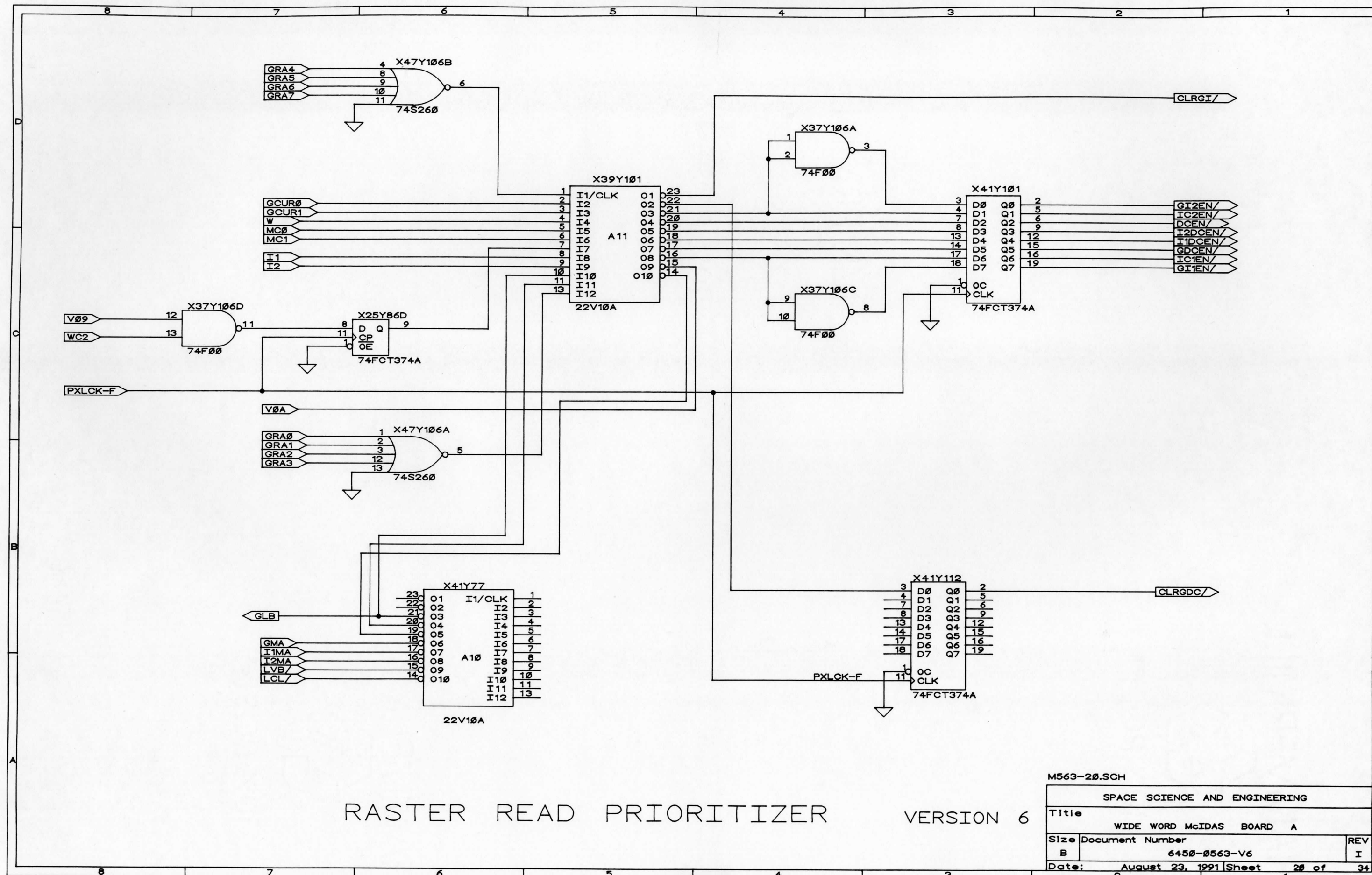
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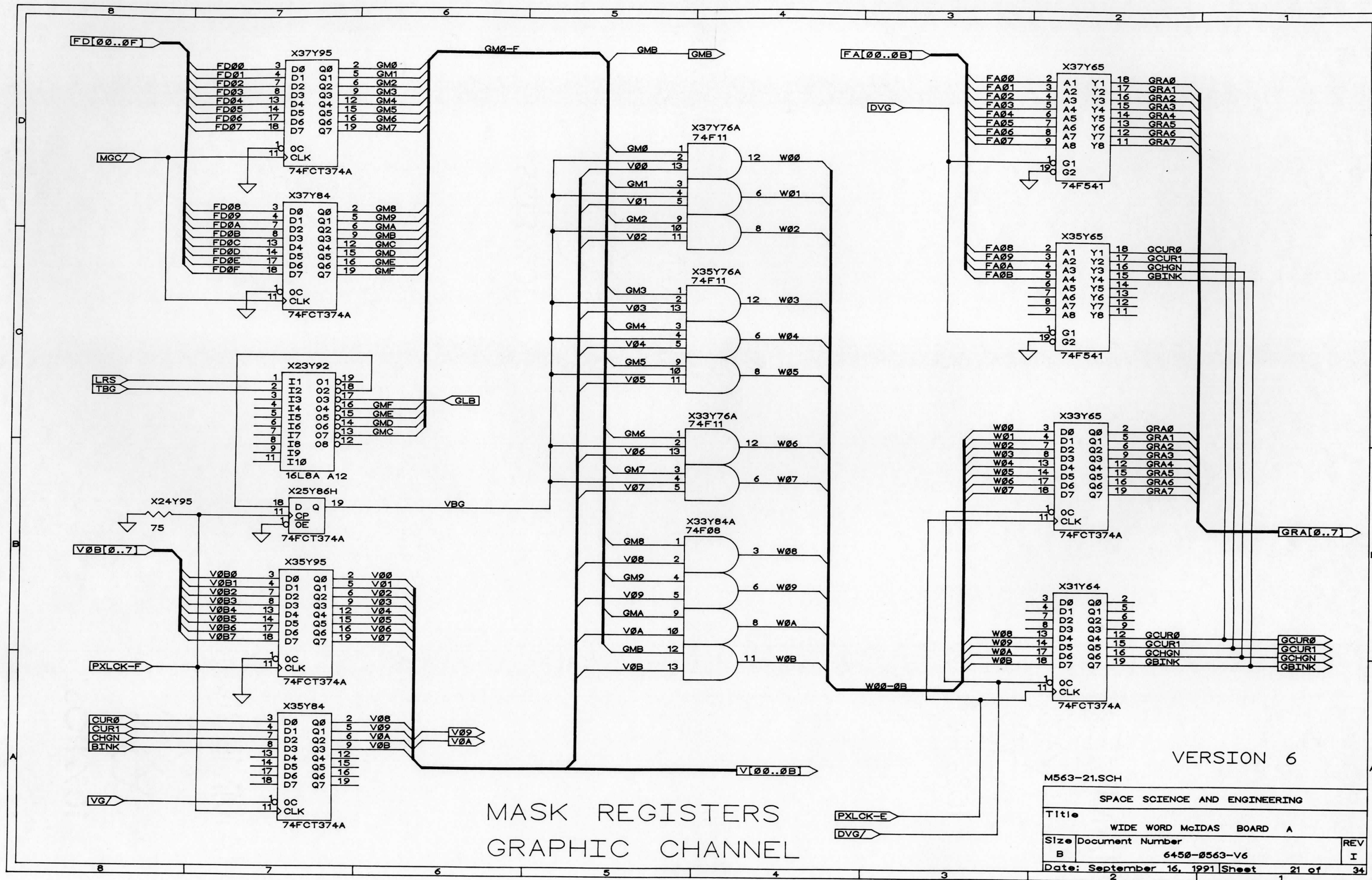
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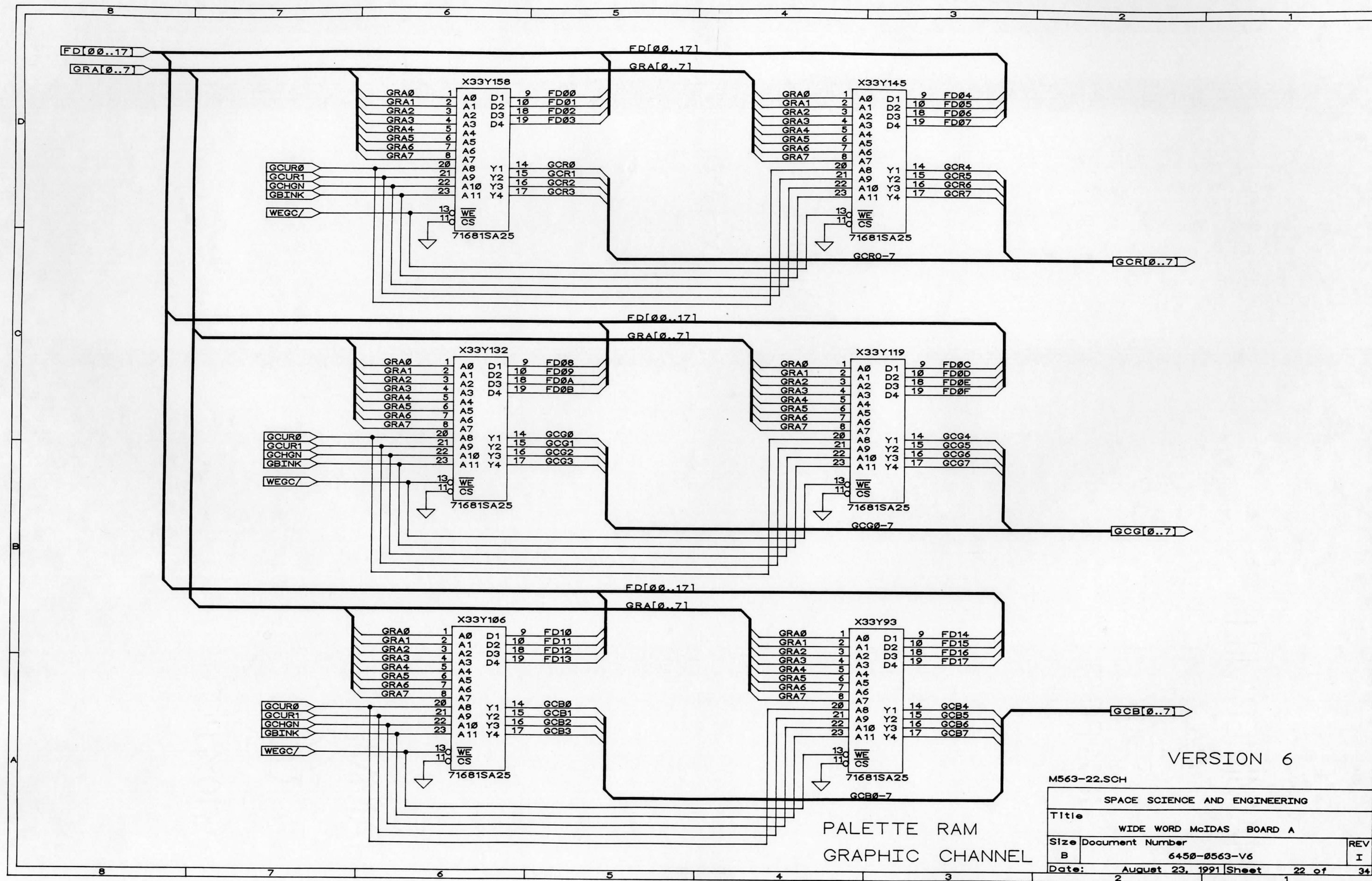
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RASTER READ PRIORITIZER VERSION 6

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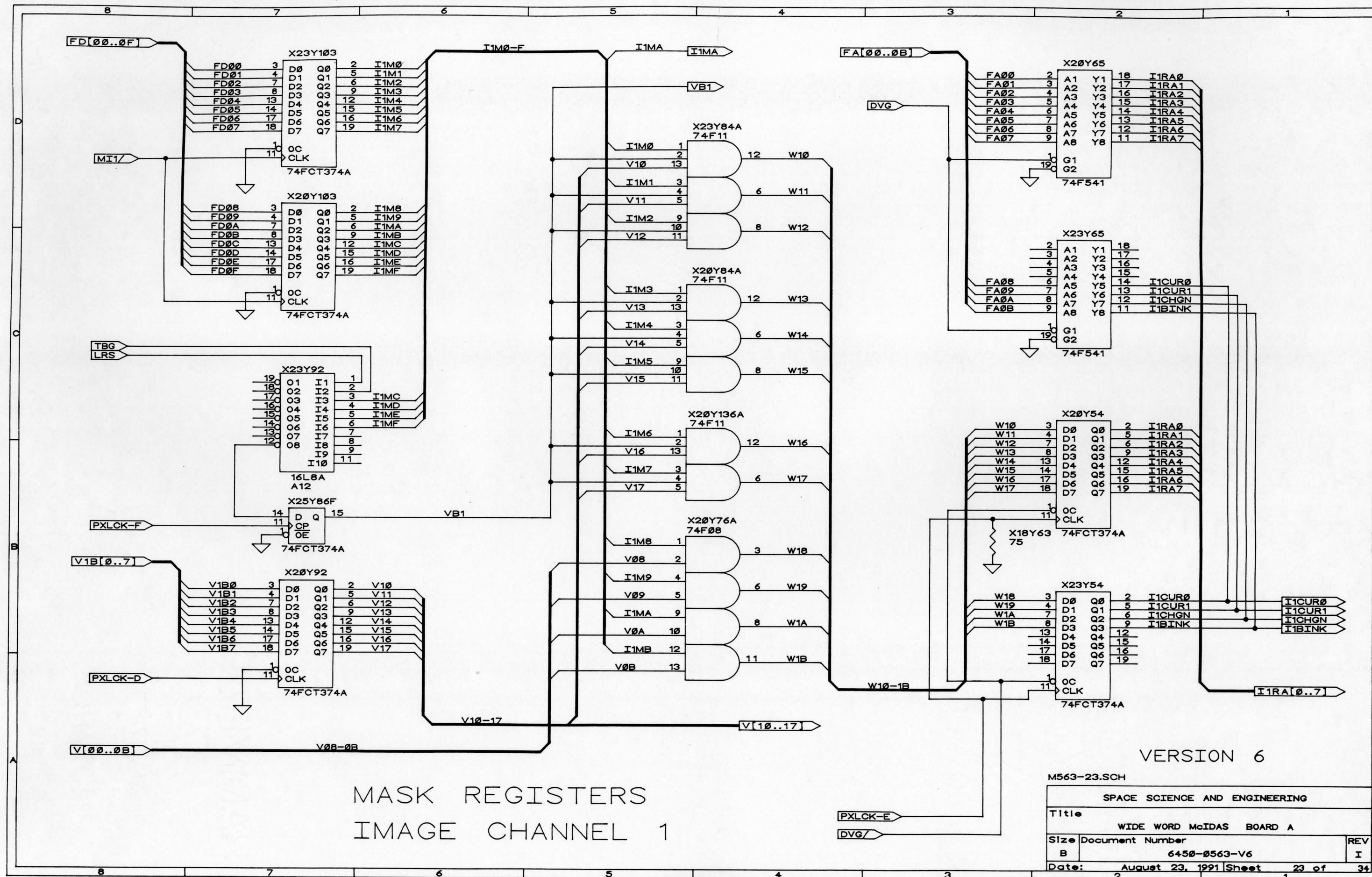


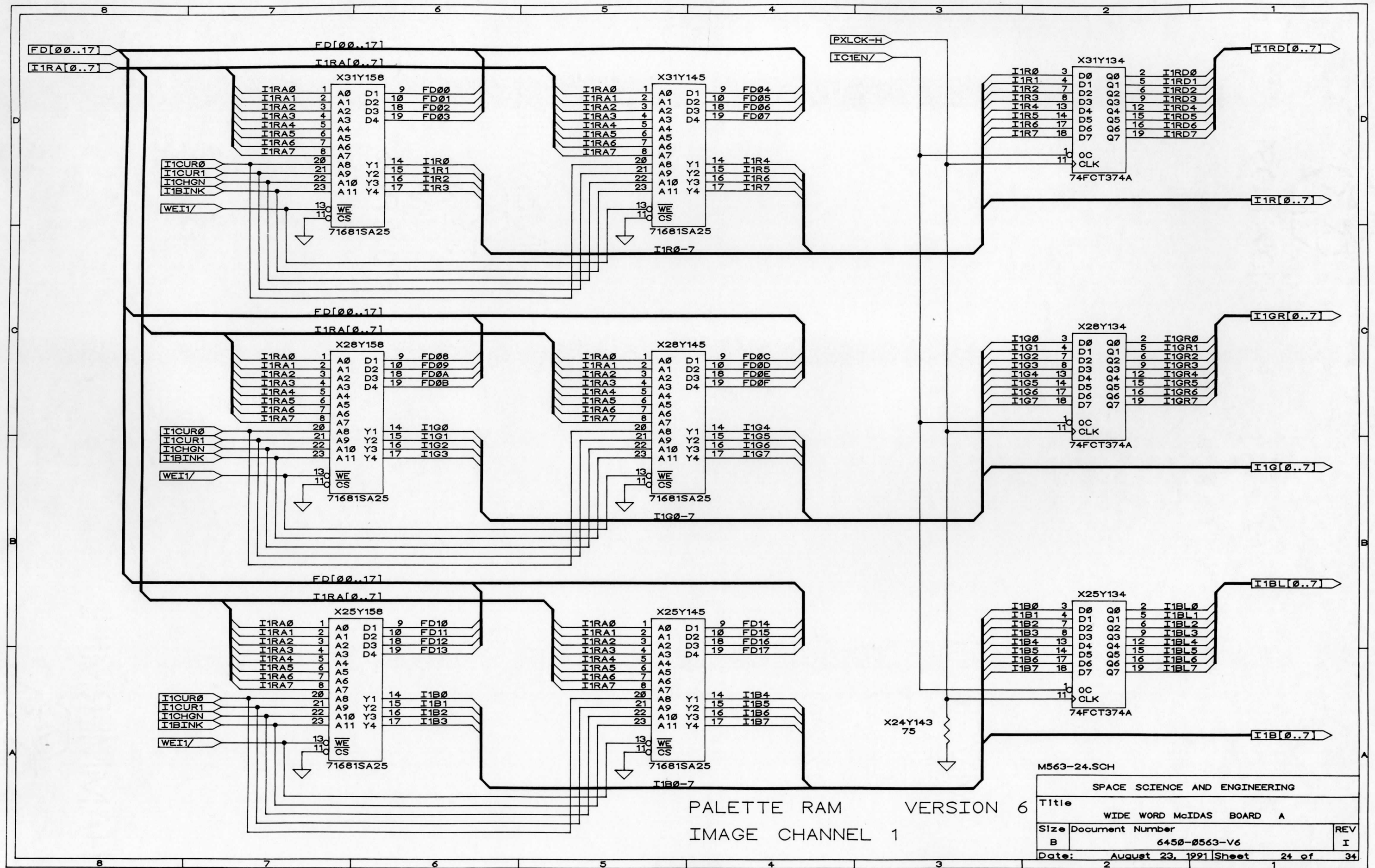
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GRAPHIC CHANNEL

VERSION 6

M563-22.SCH

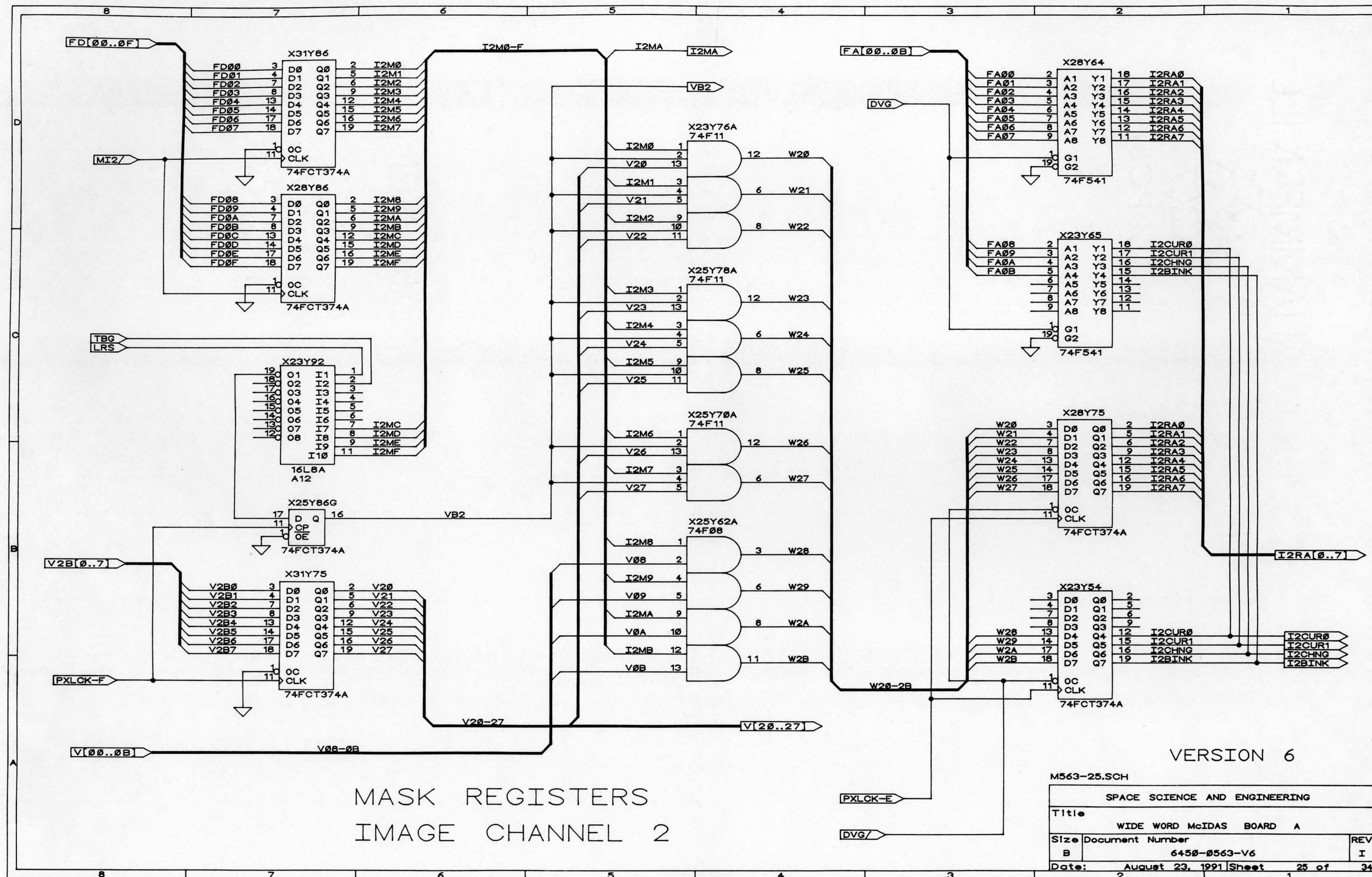
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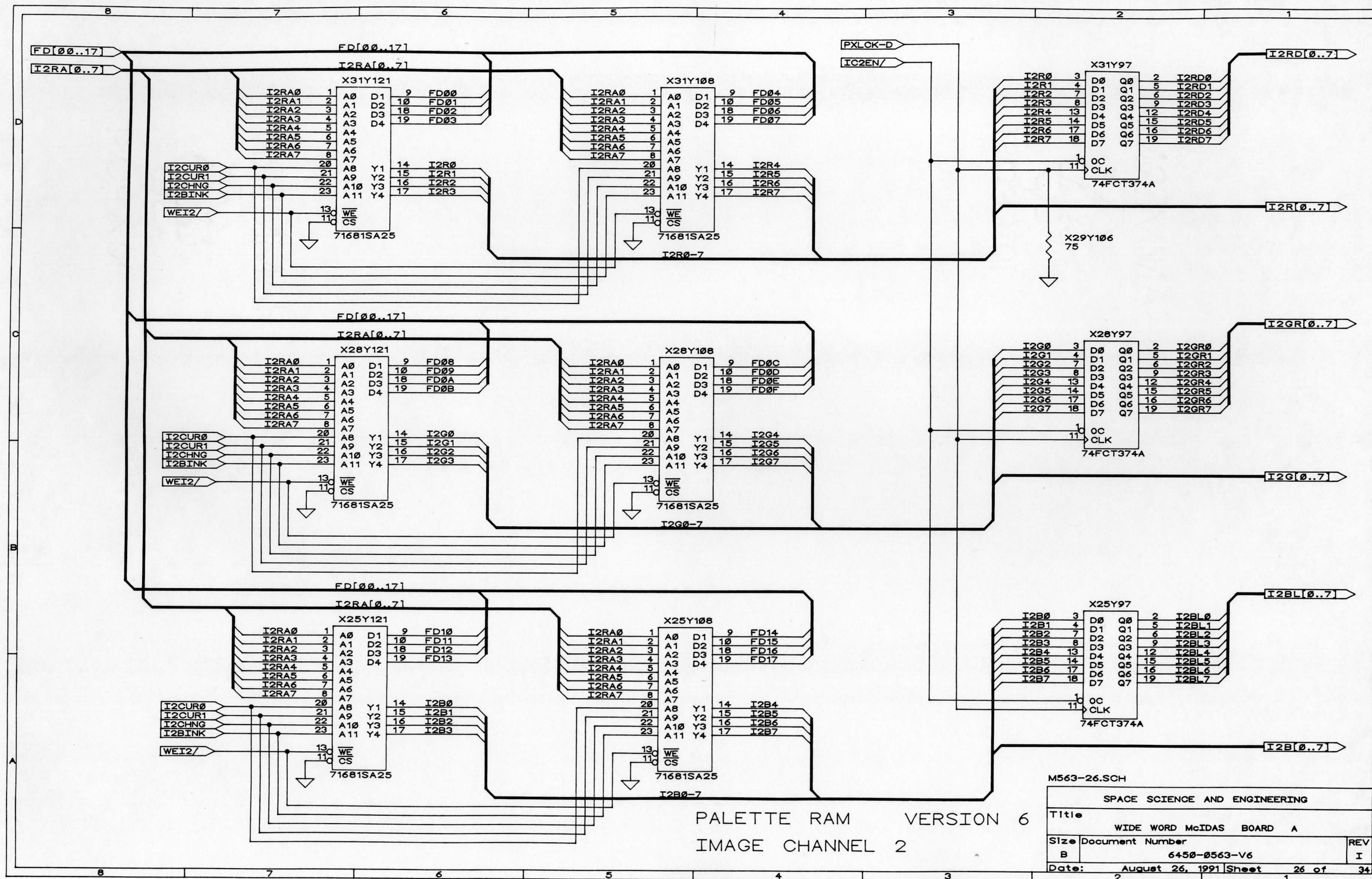




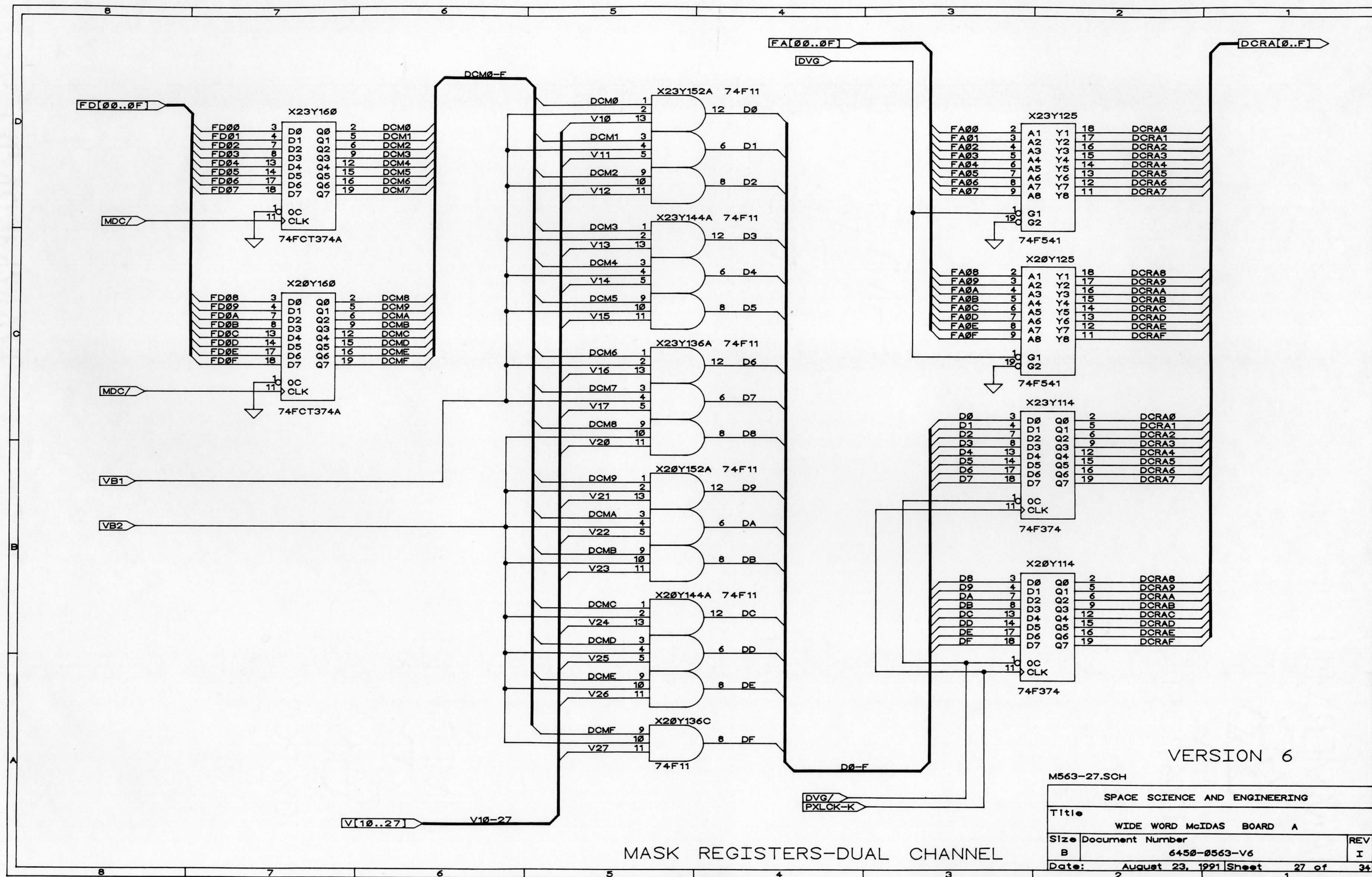
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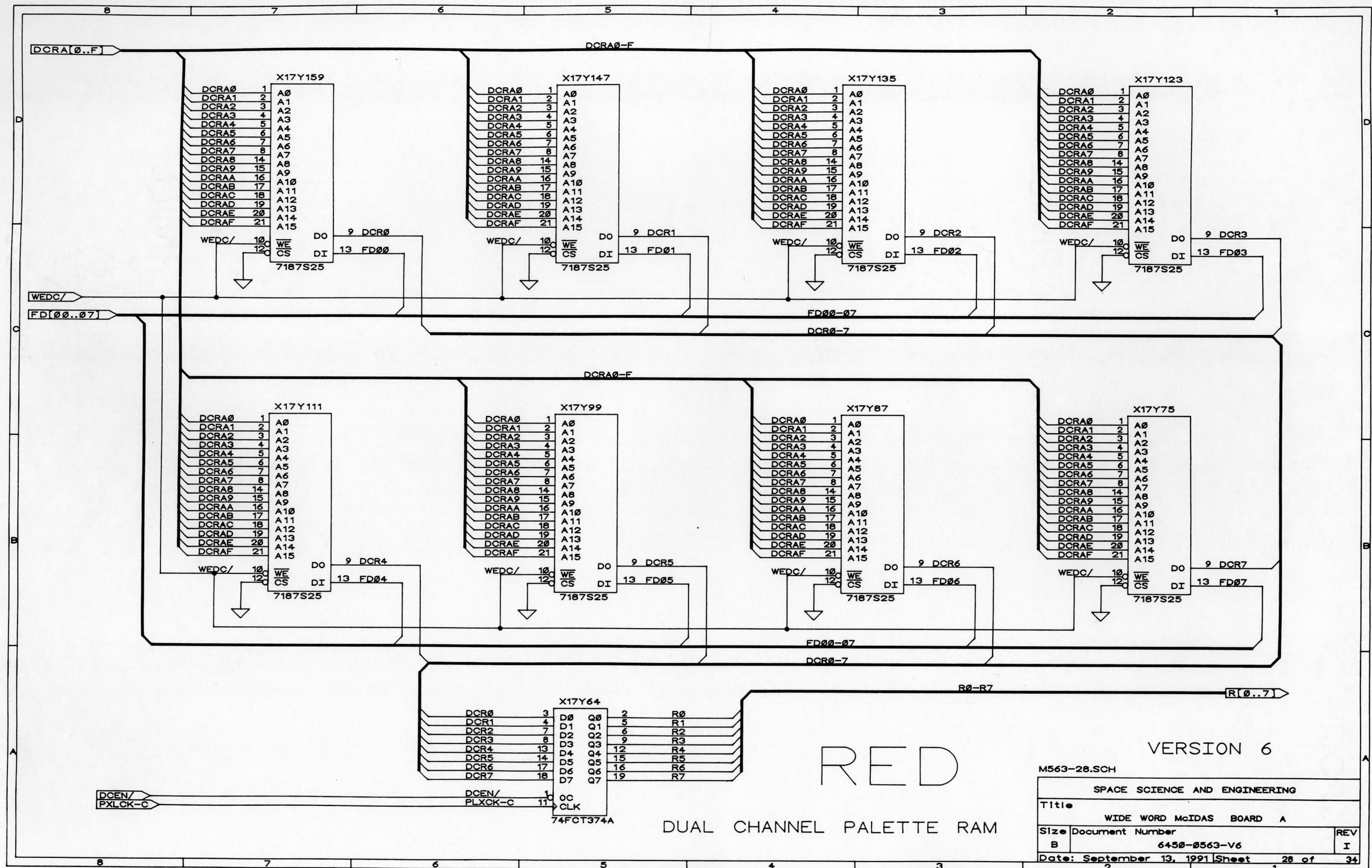
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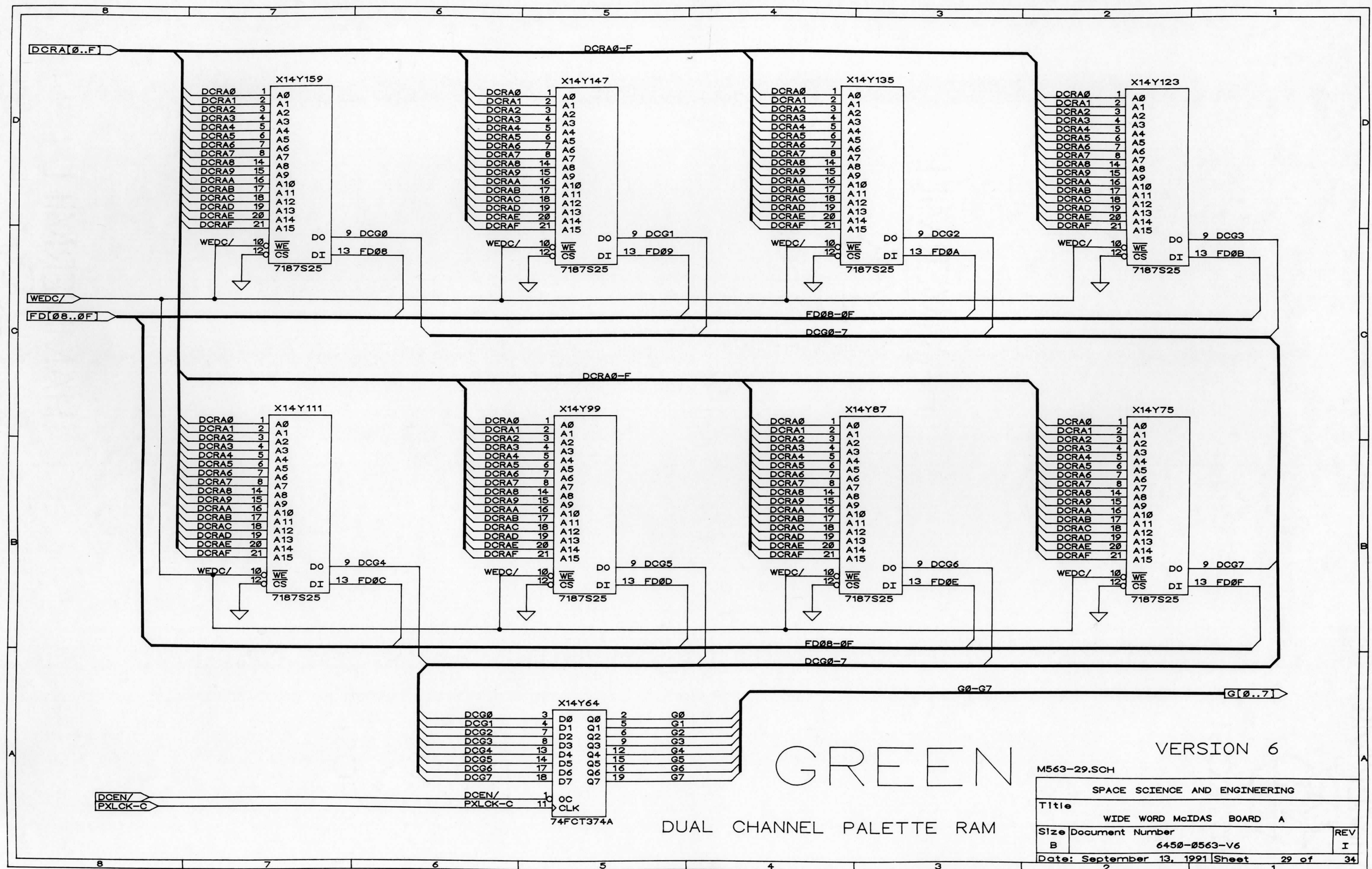


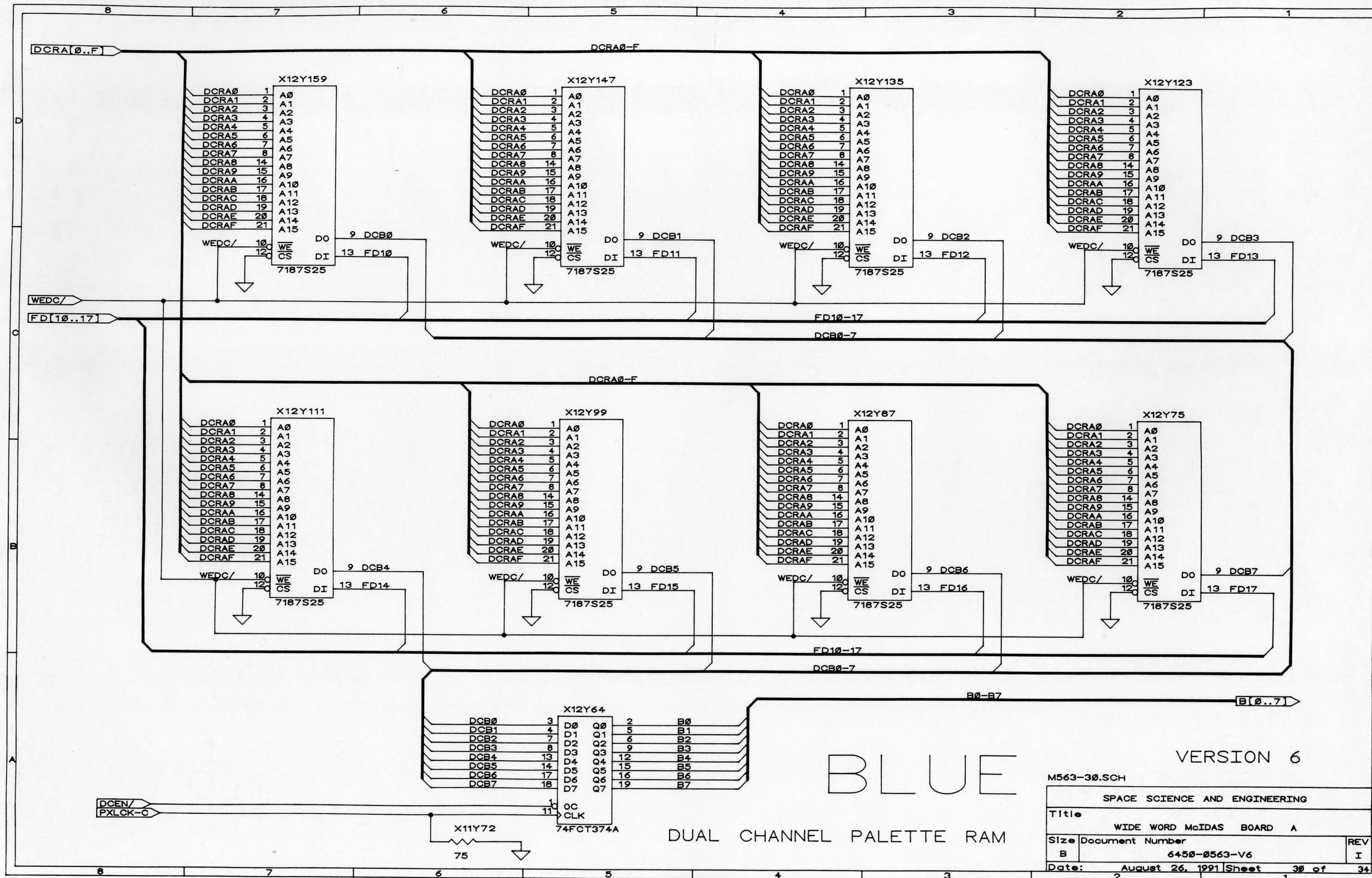


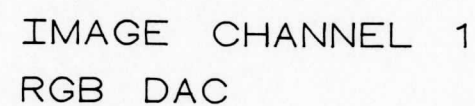
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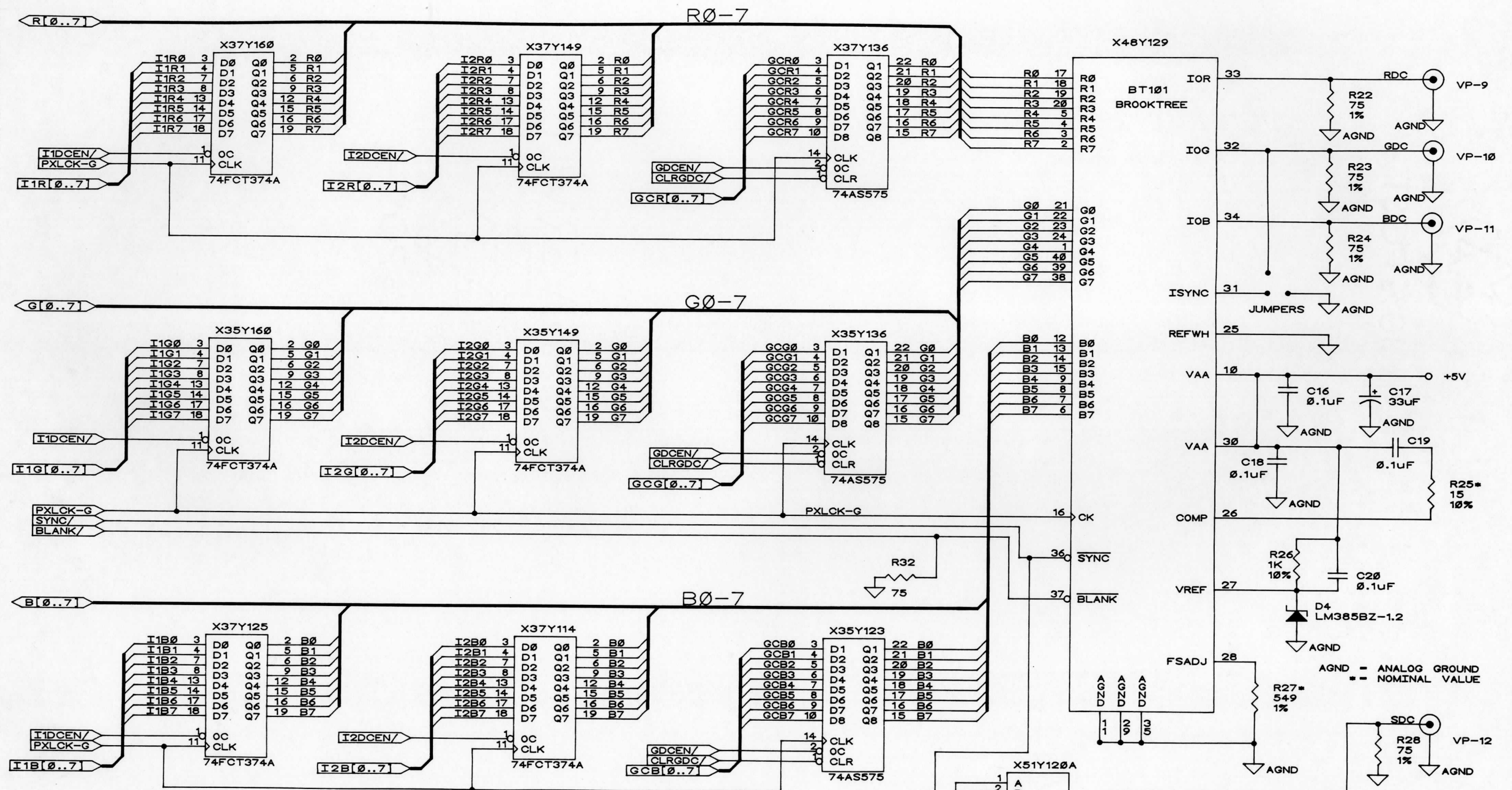








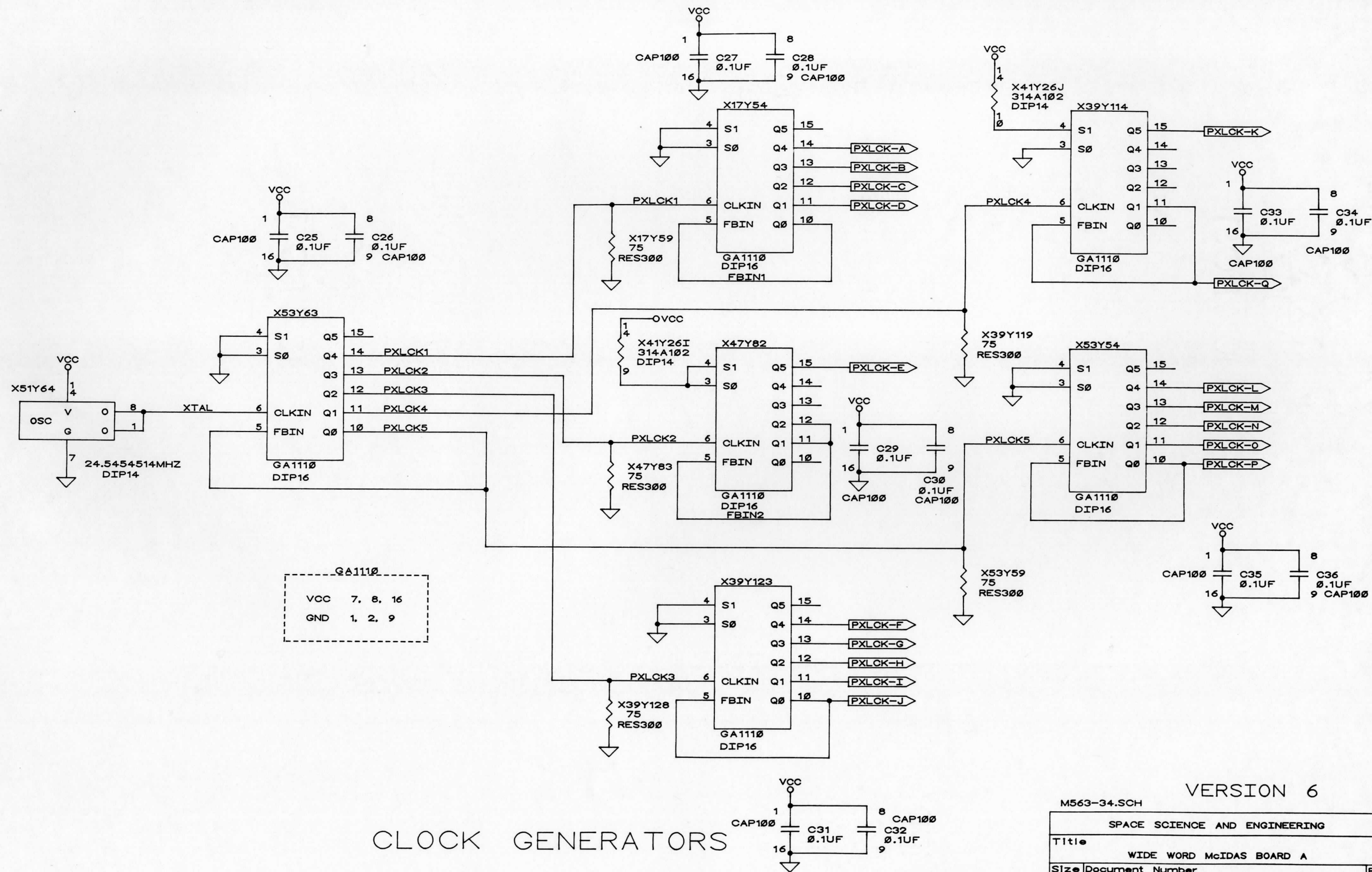
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DUAL CHANNEL - RGB DAC

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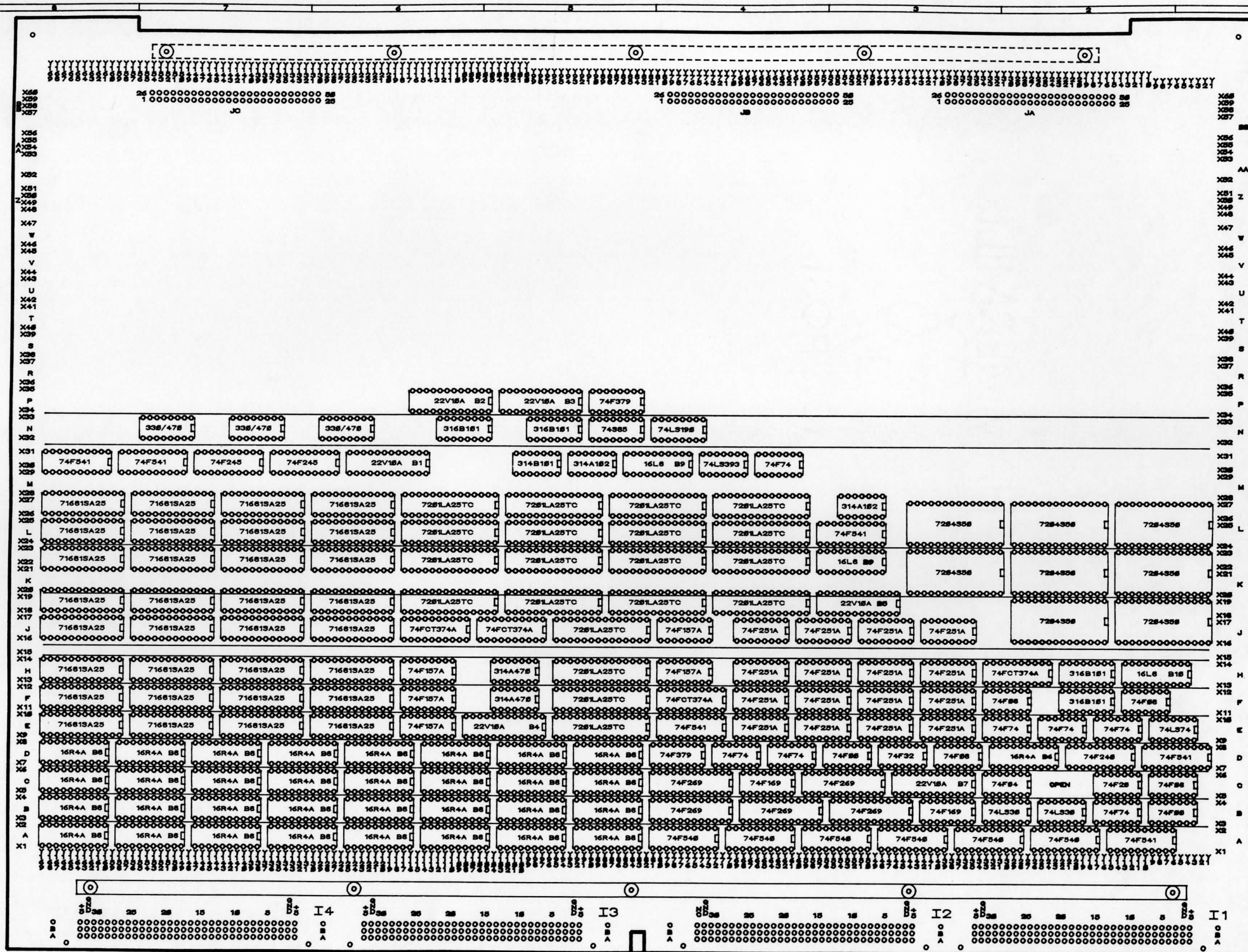
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BOARD B

VERSION 3.0

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UNIVERSITY OF WISCONSIN**

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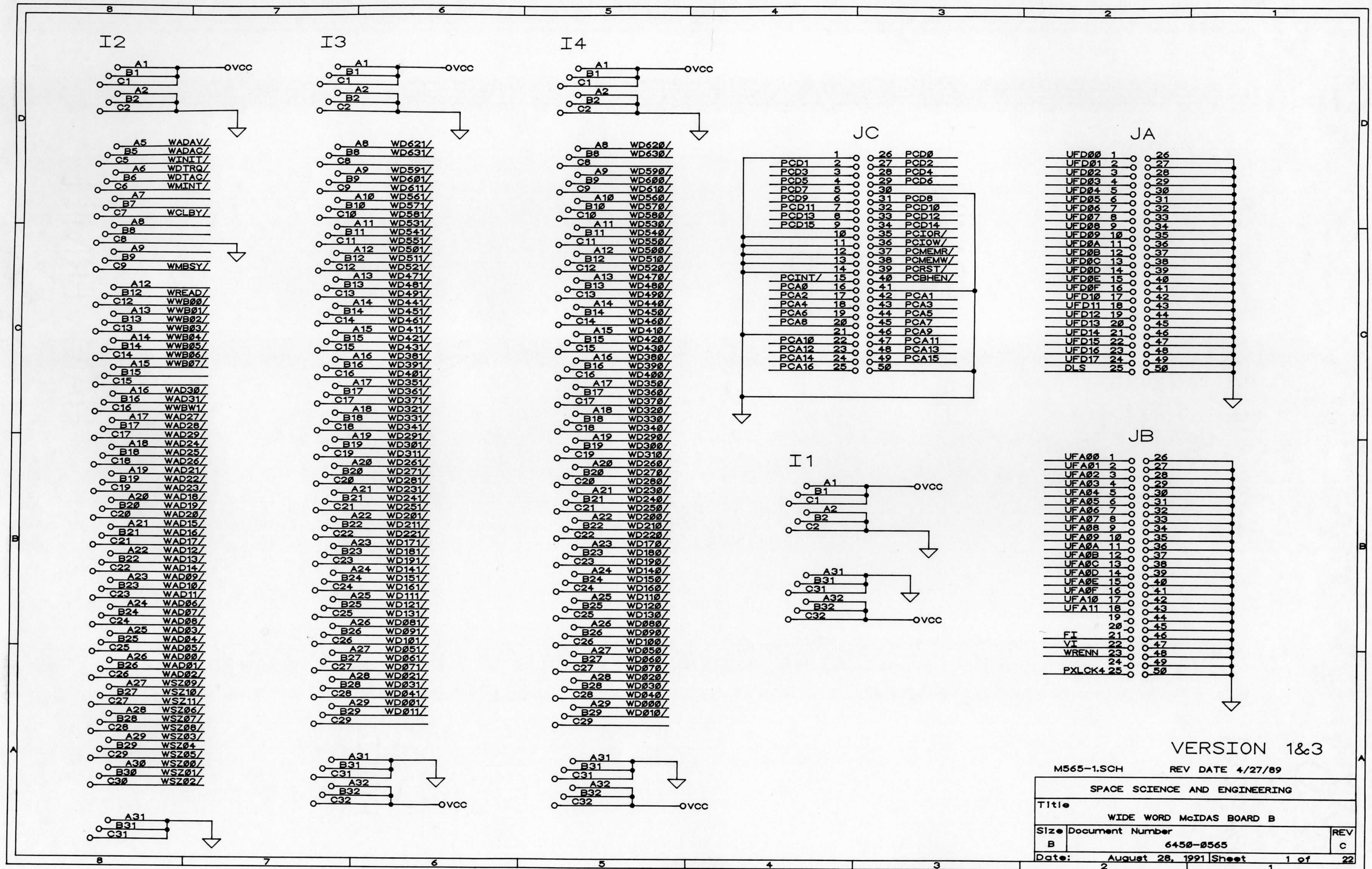


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M564AS5.5CH

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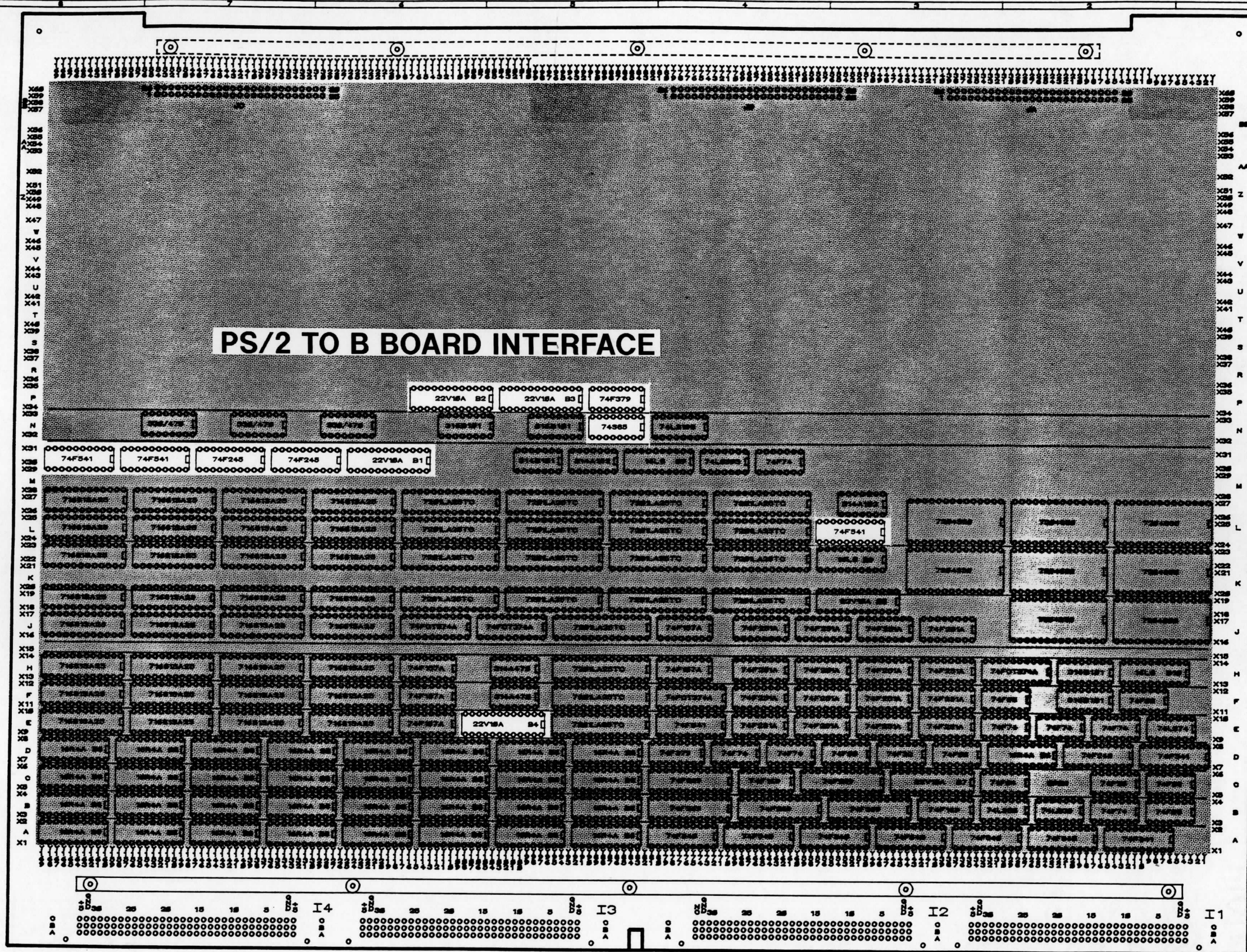
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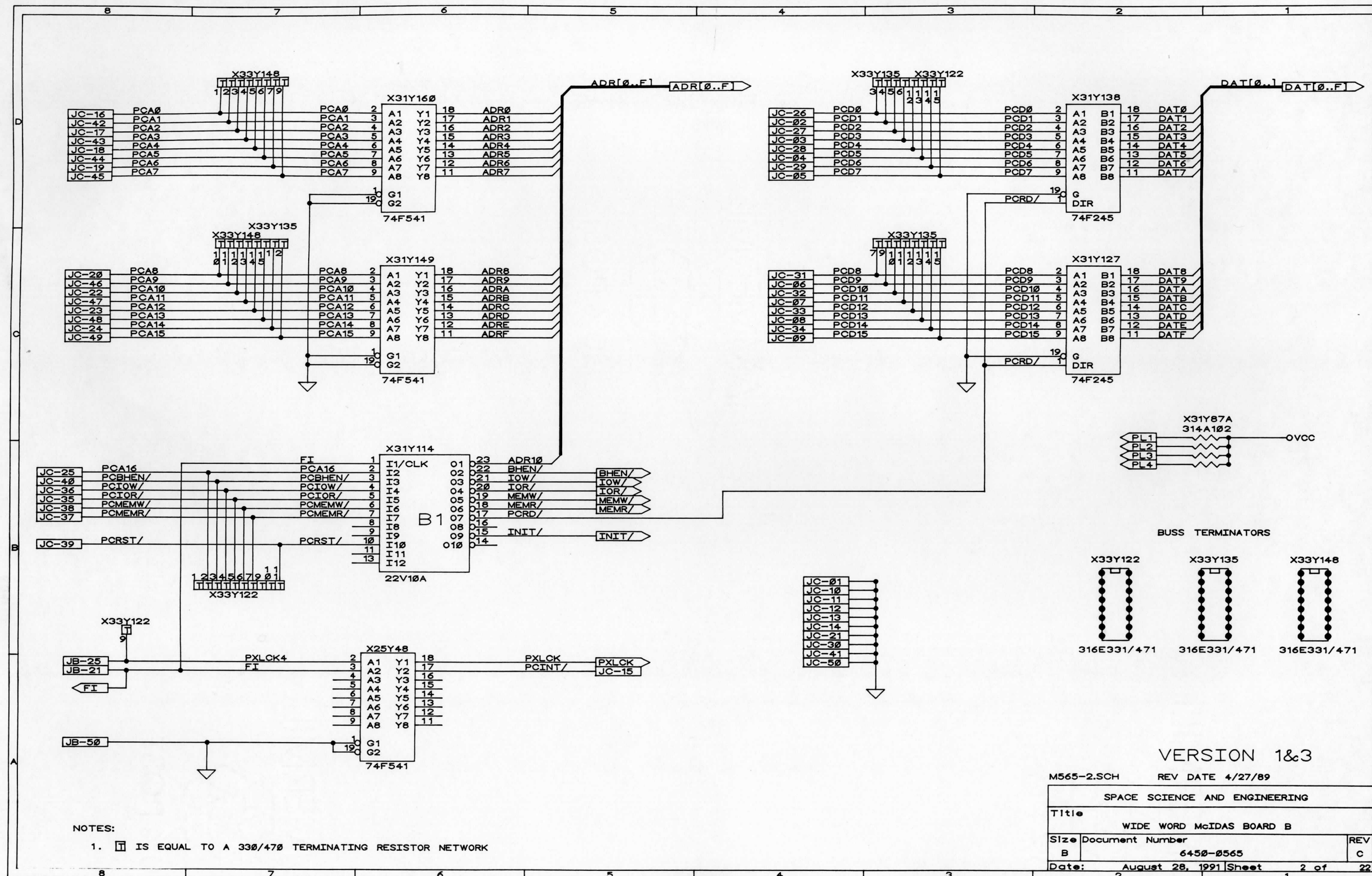


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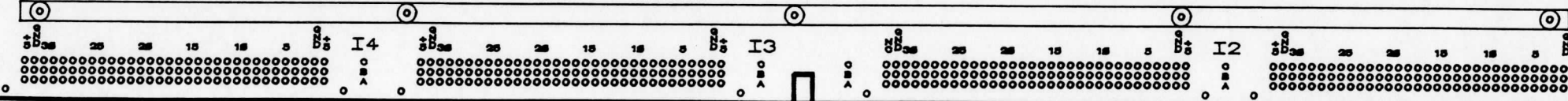
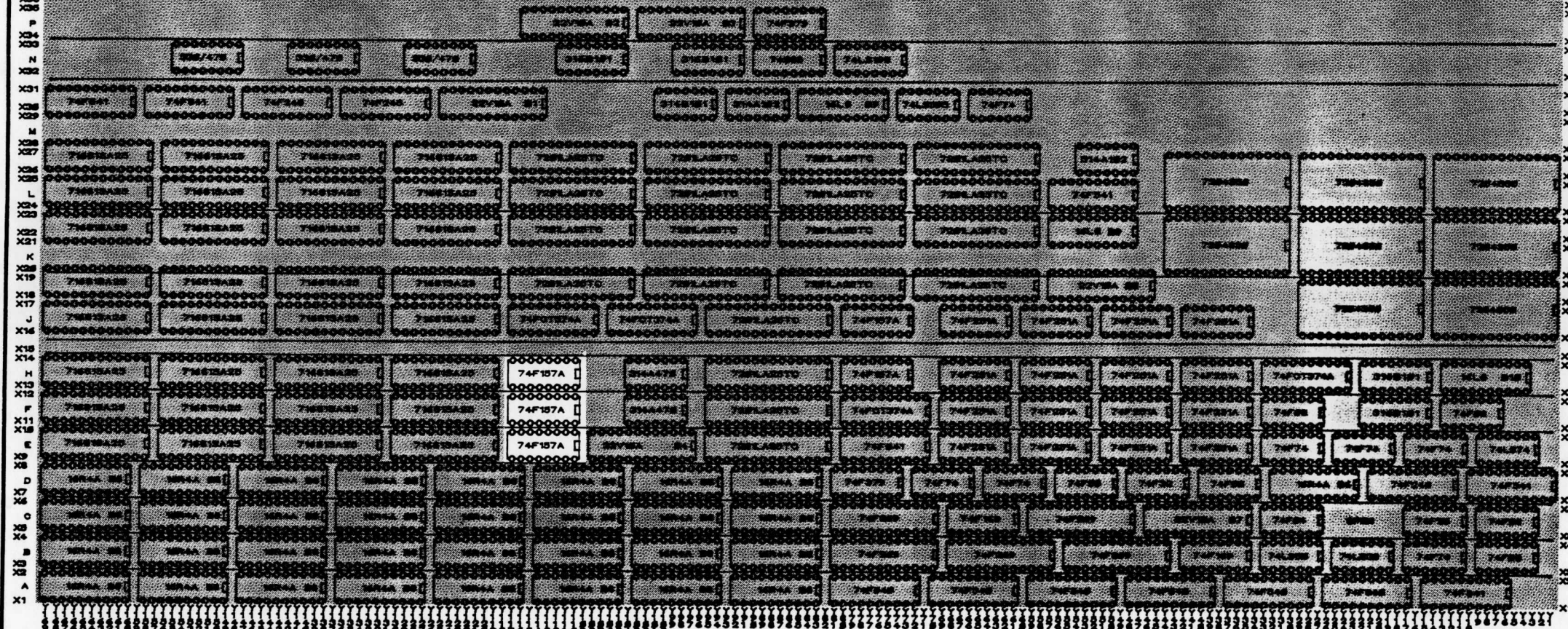
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B BOARD TO DATARAM MEMORY INTERFACE **[ADDRESS MUX(READBACK)]**

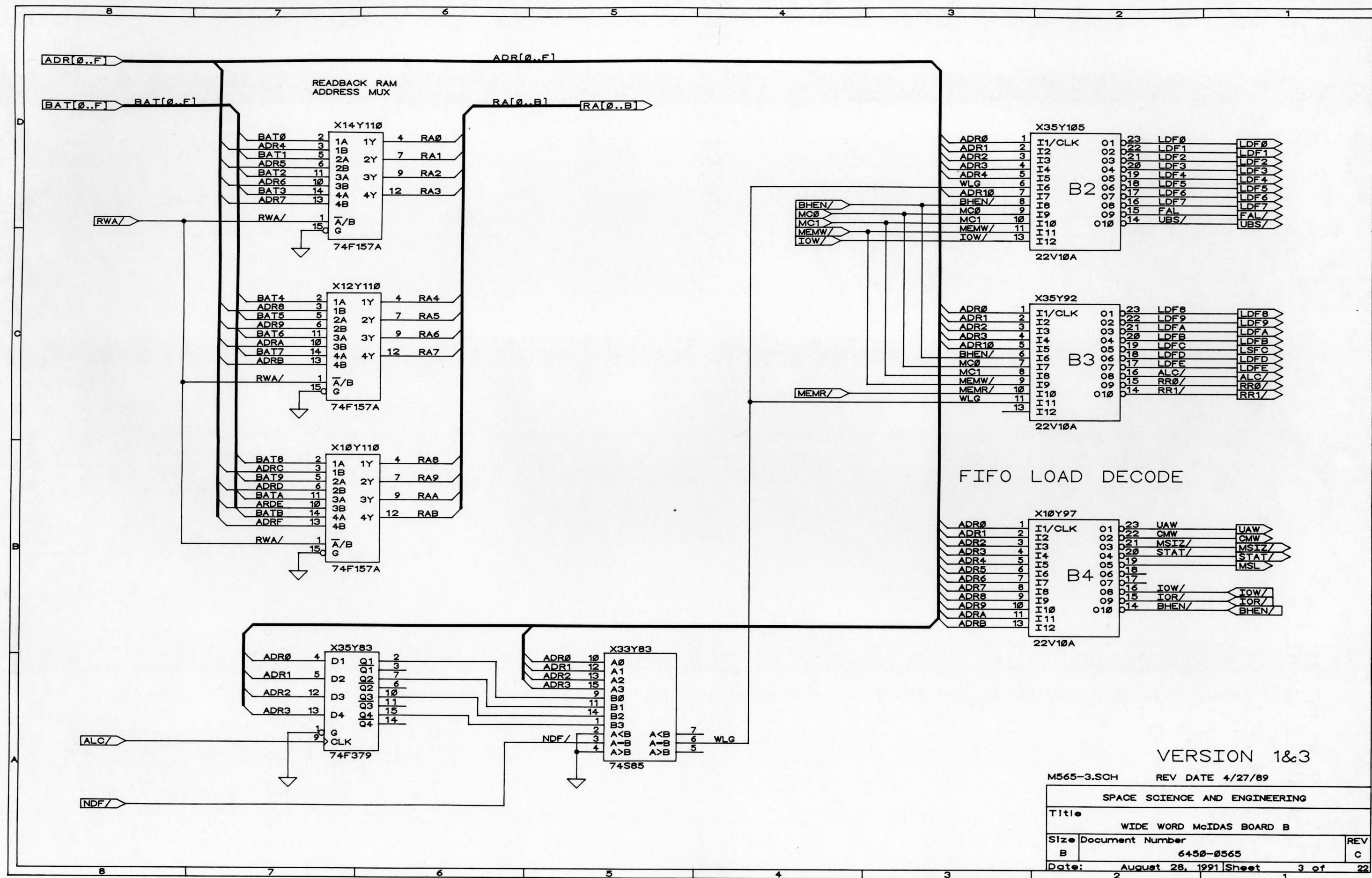


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VERSION 1&3

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SPACE SCIENCE AND ENGINEERING

Title

WIDE WORD McIDAS BOARD B

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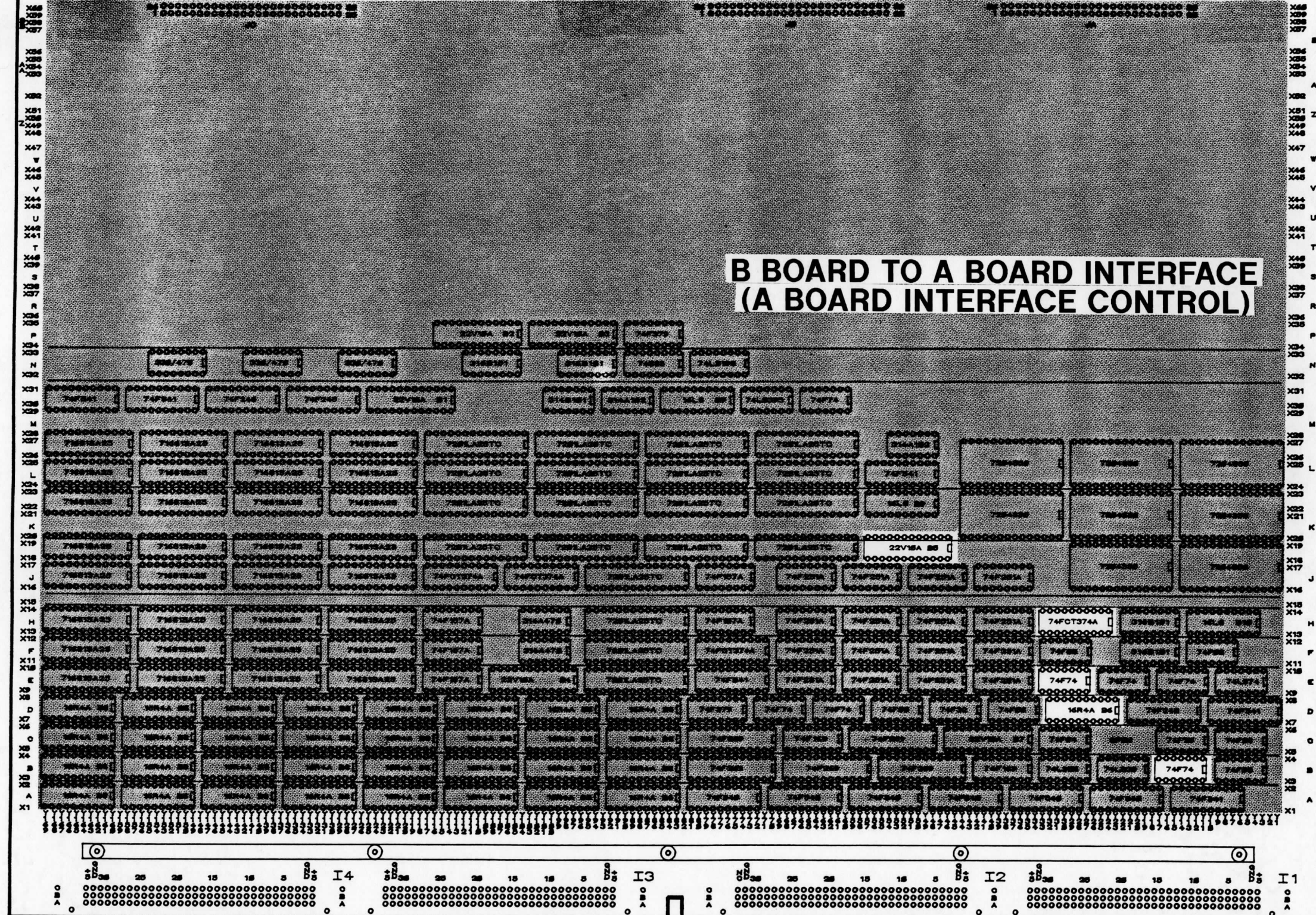
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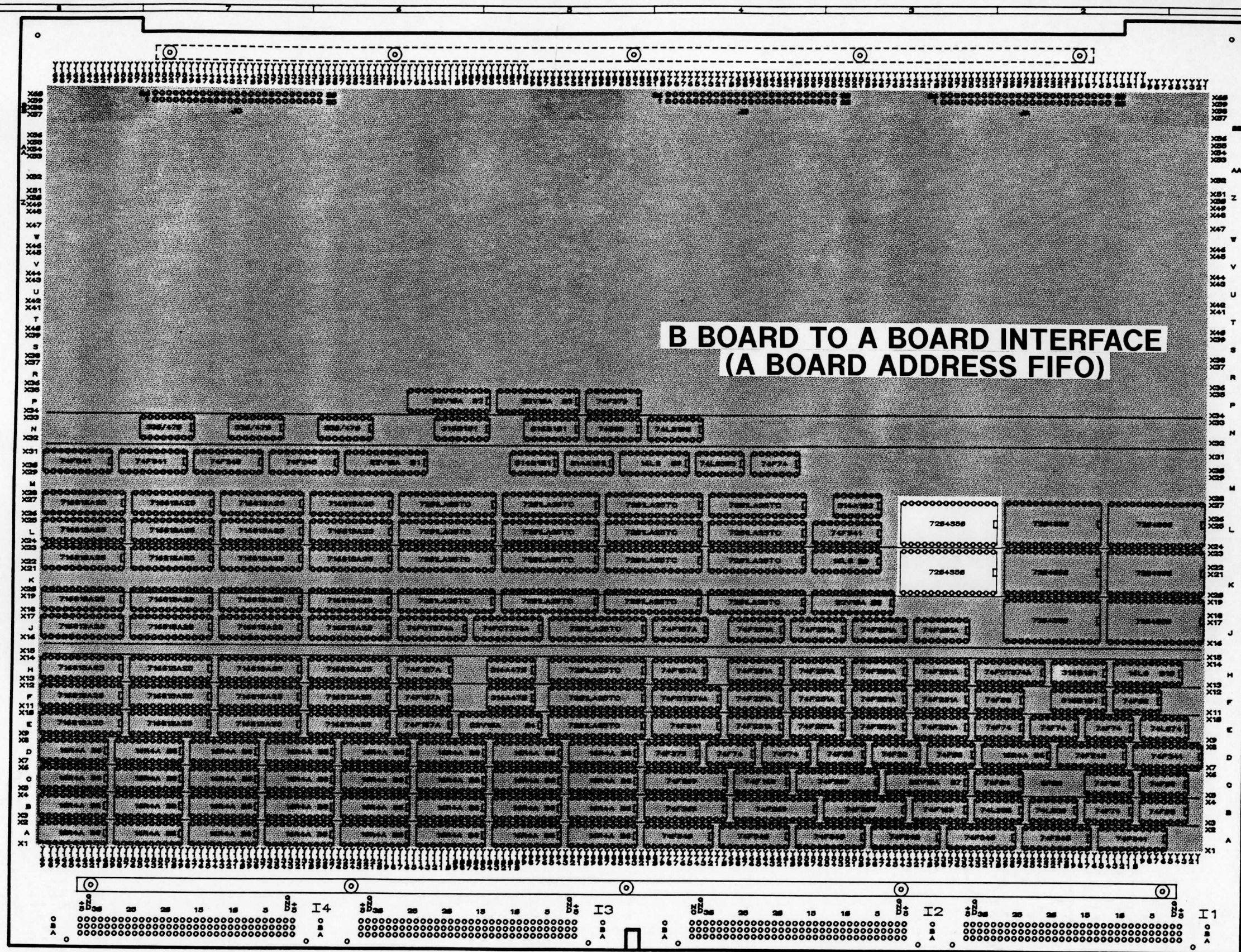


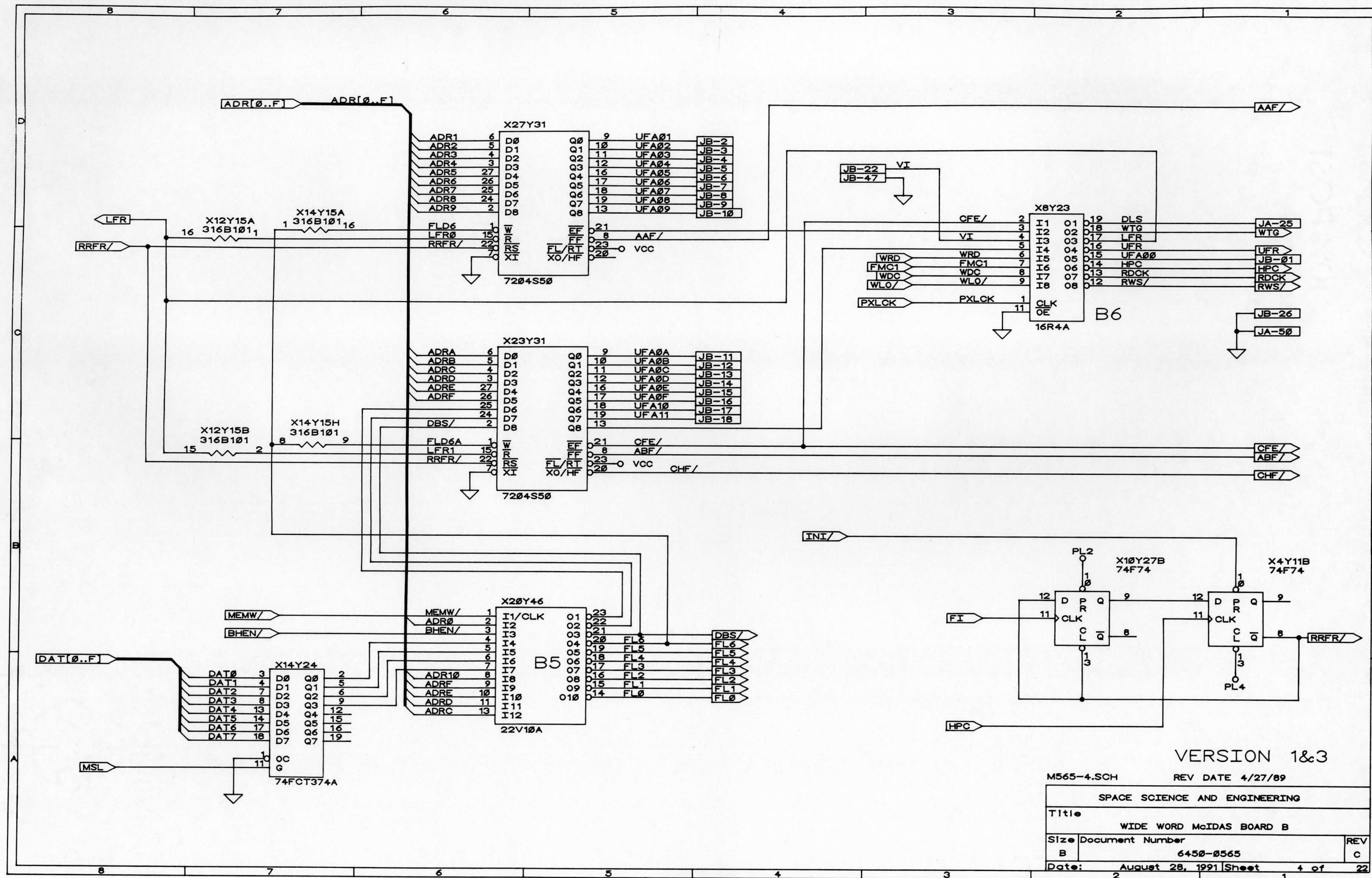
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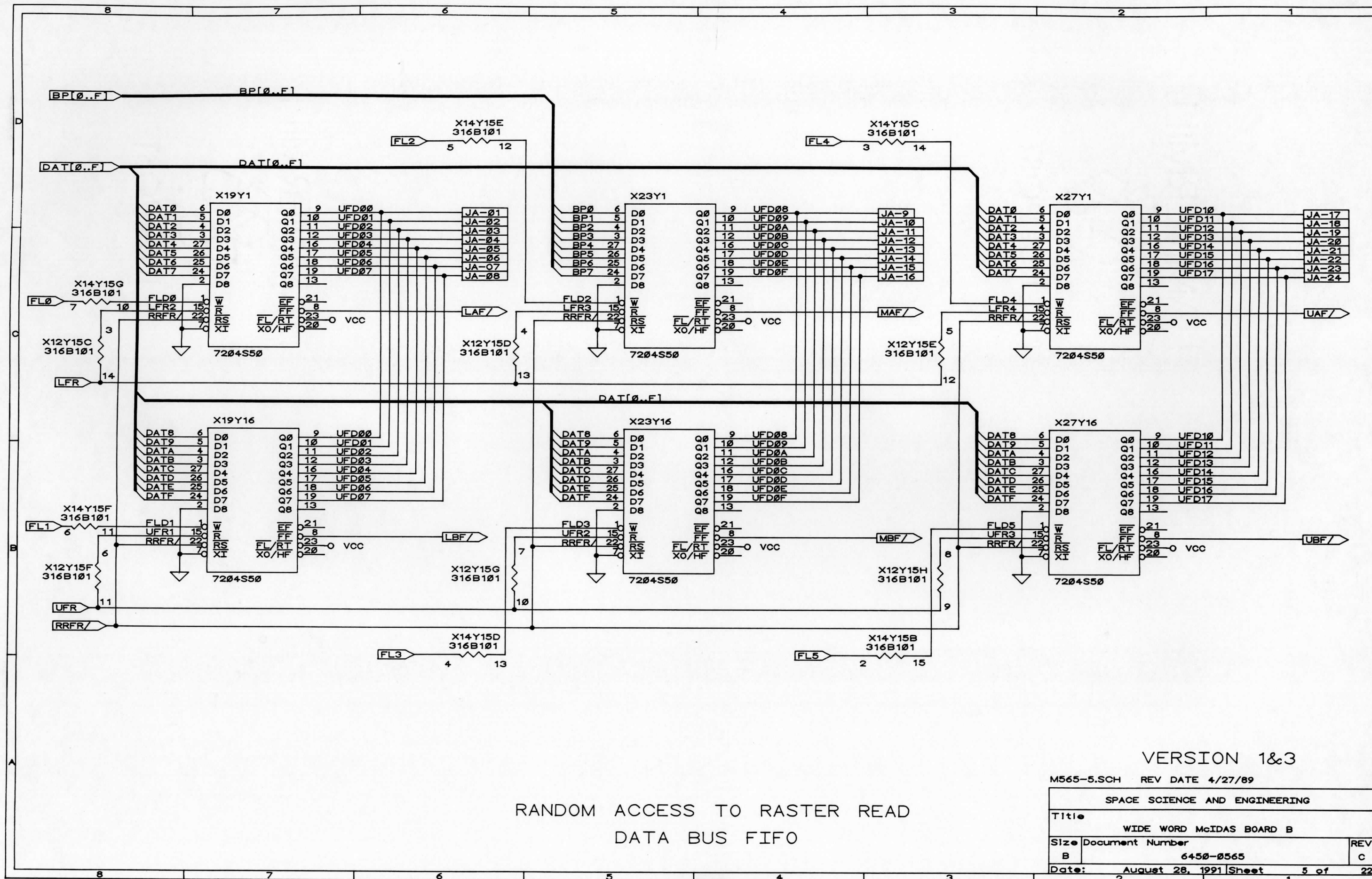
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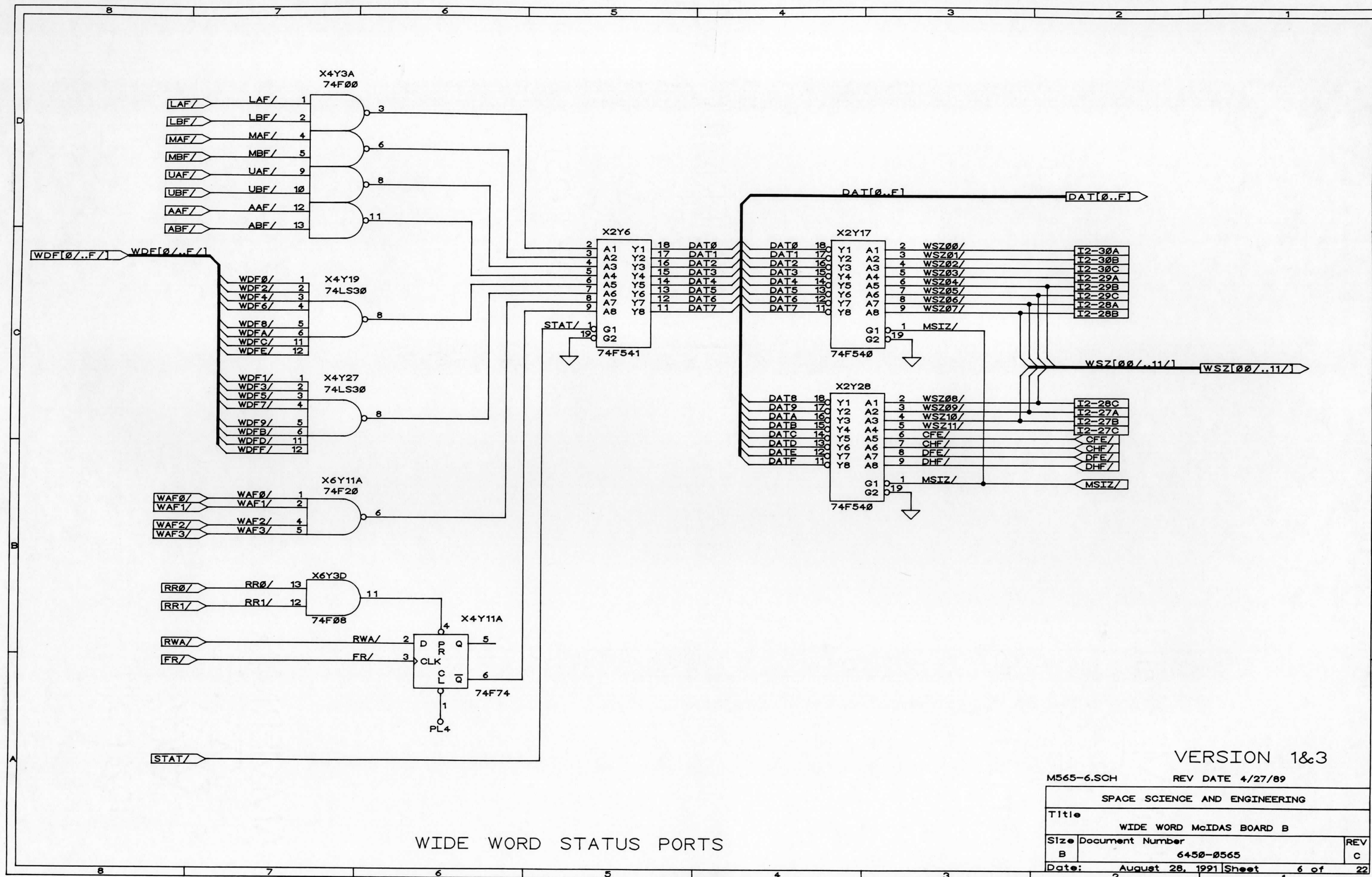
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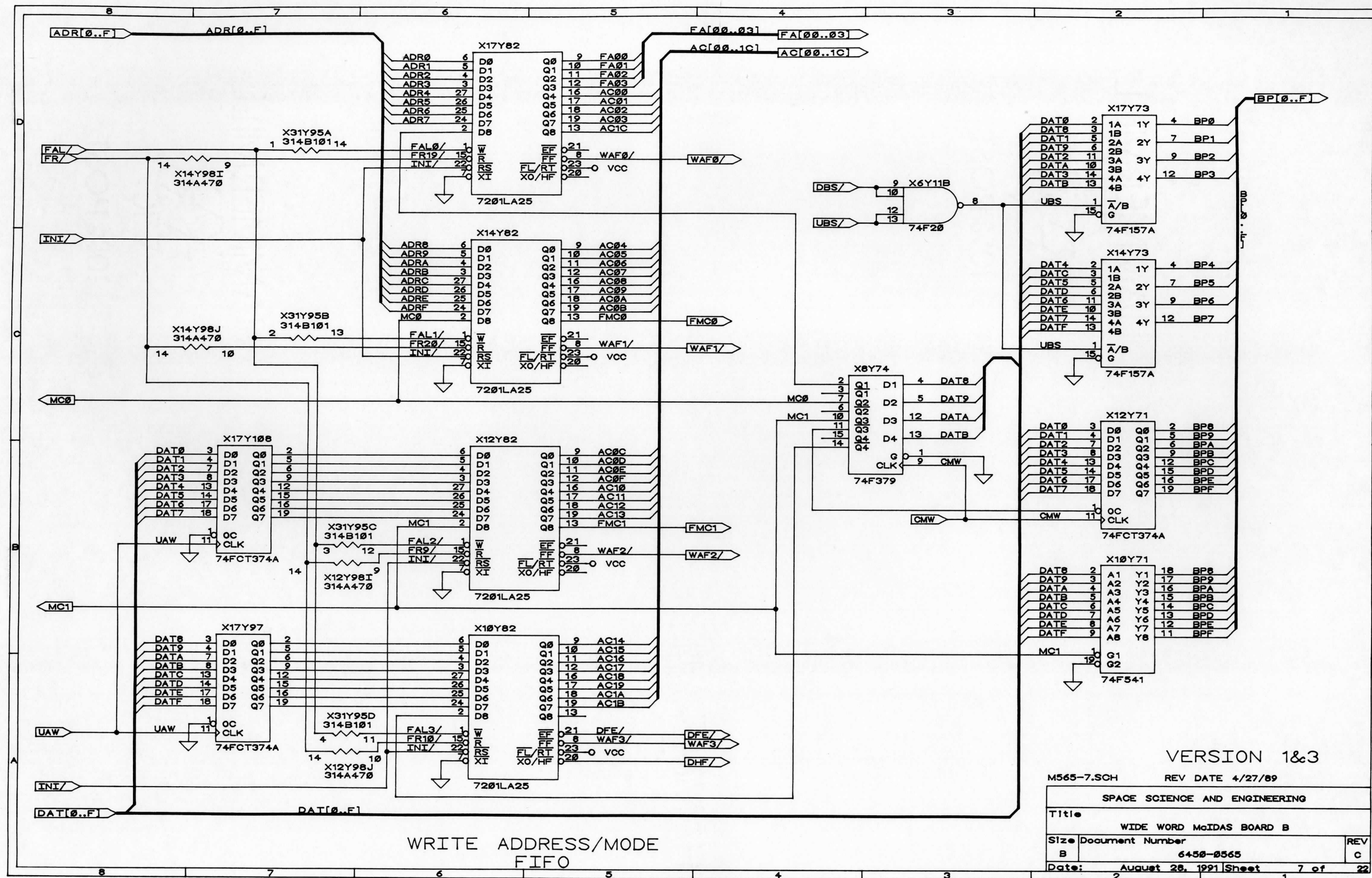
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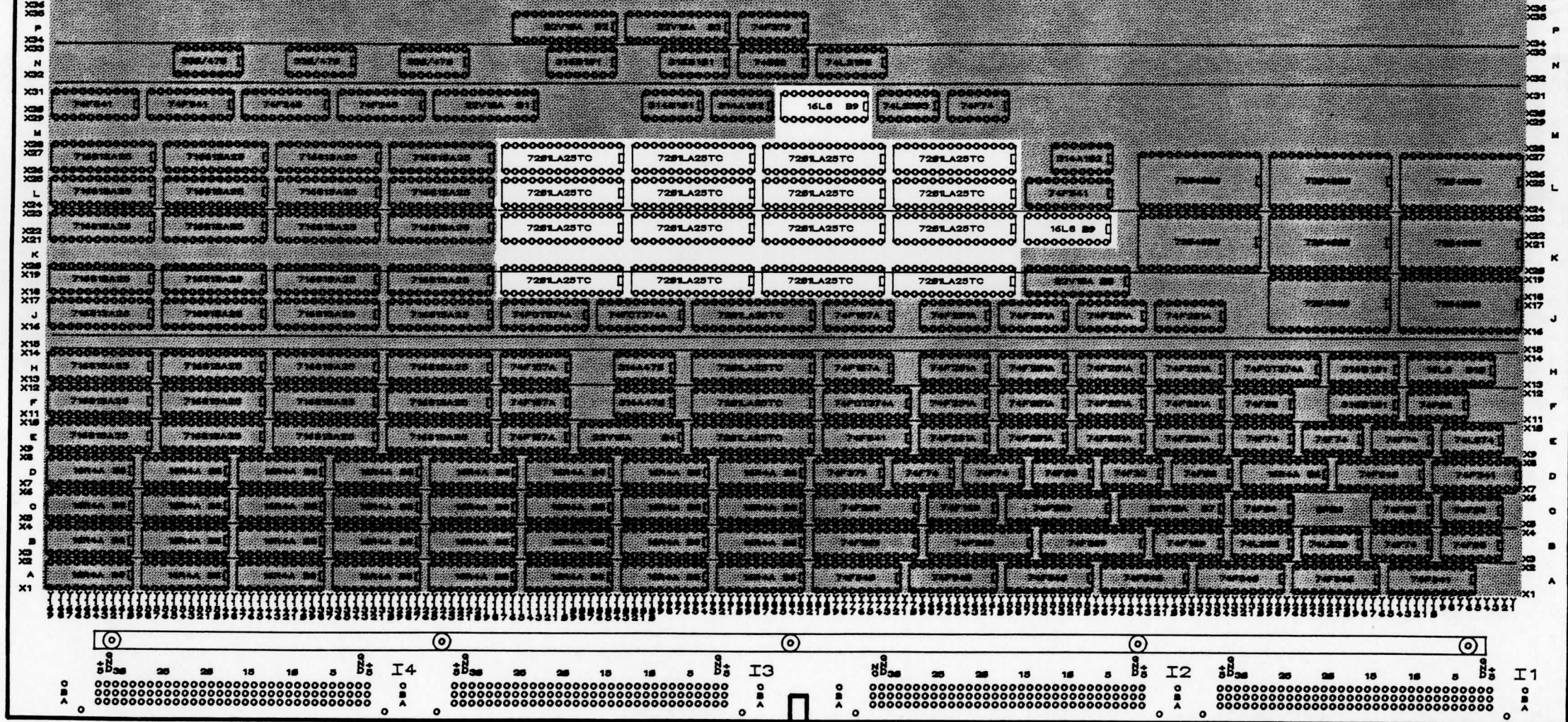
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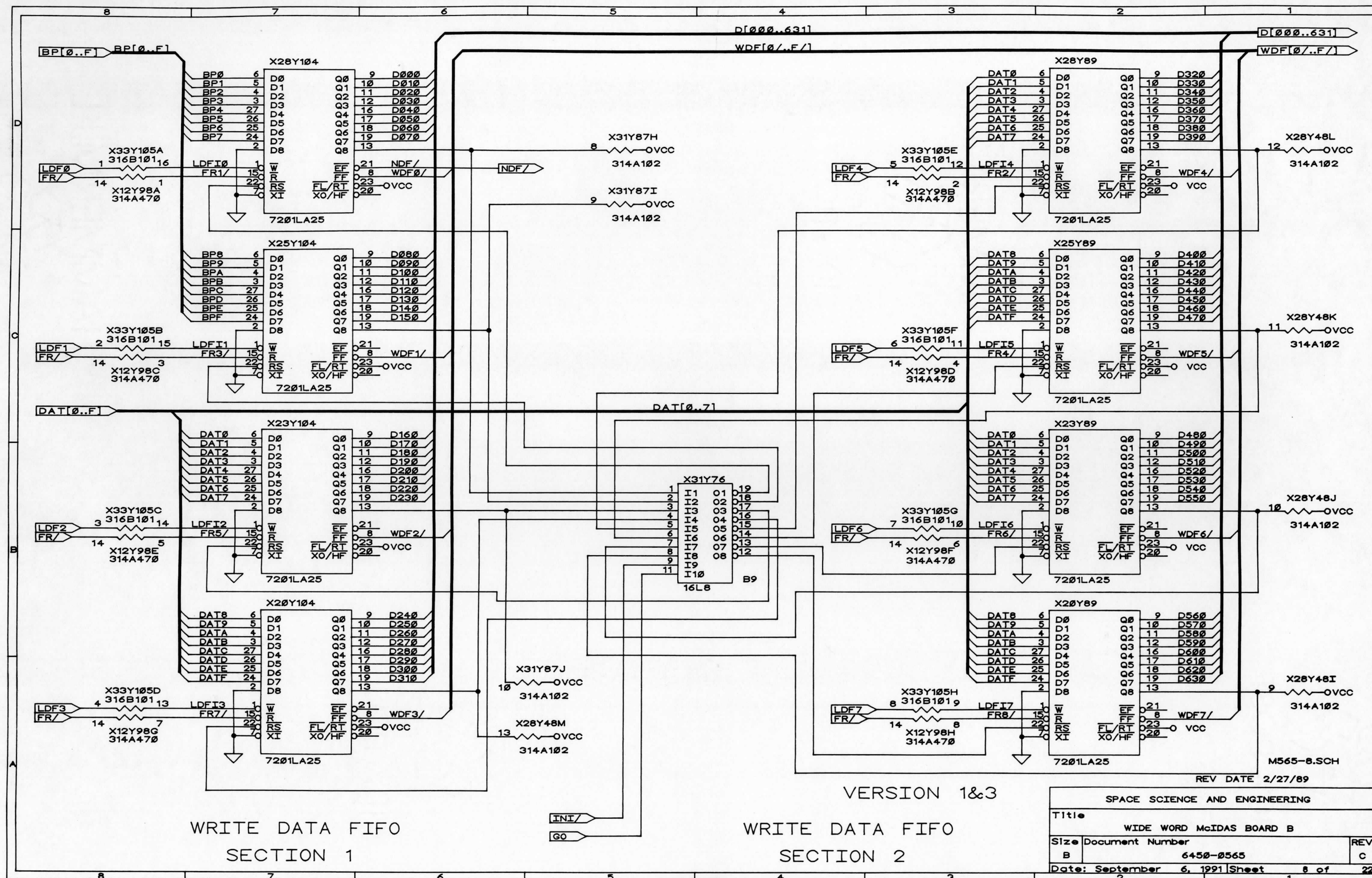


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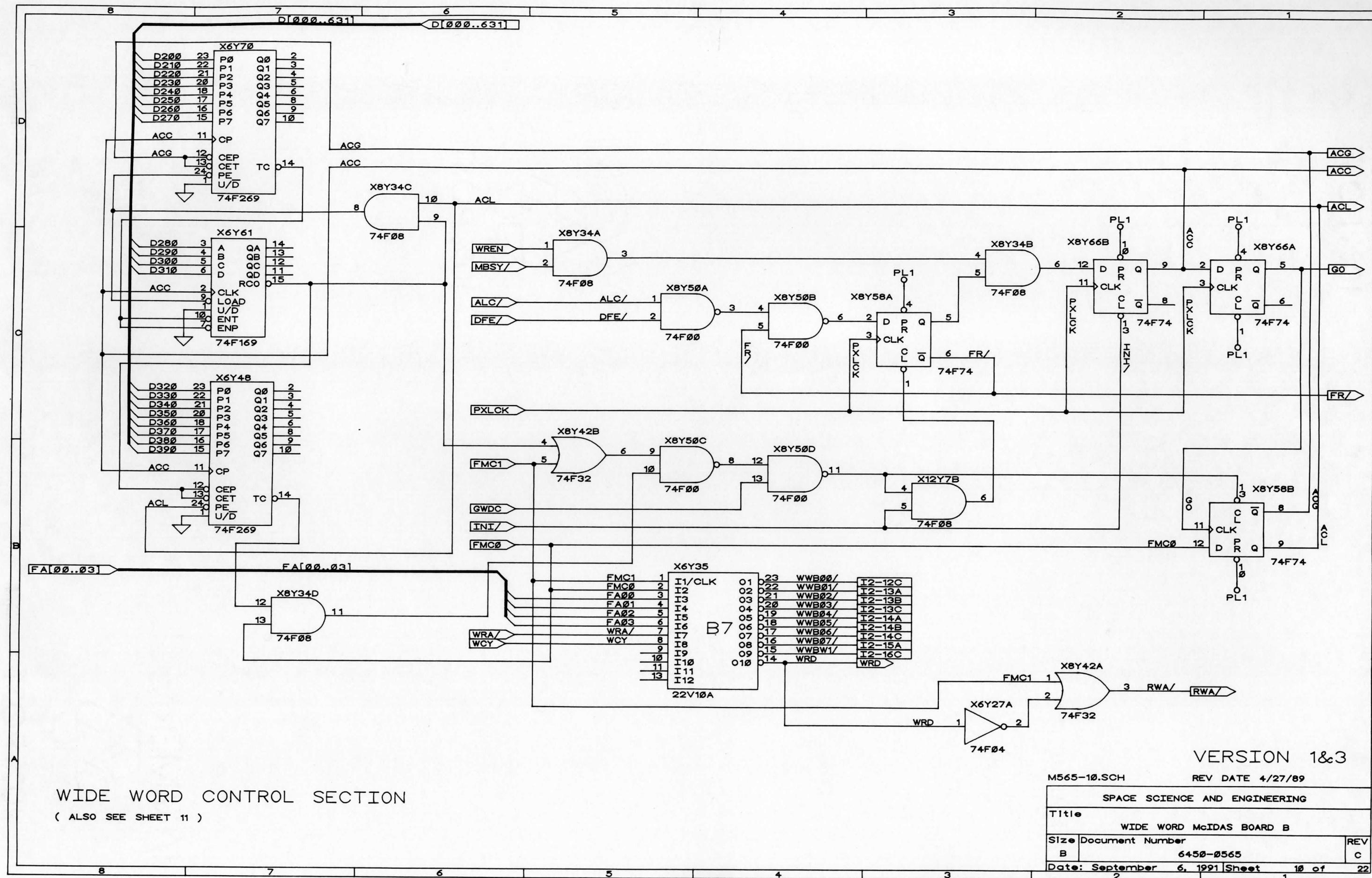
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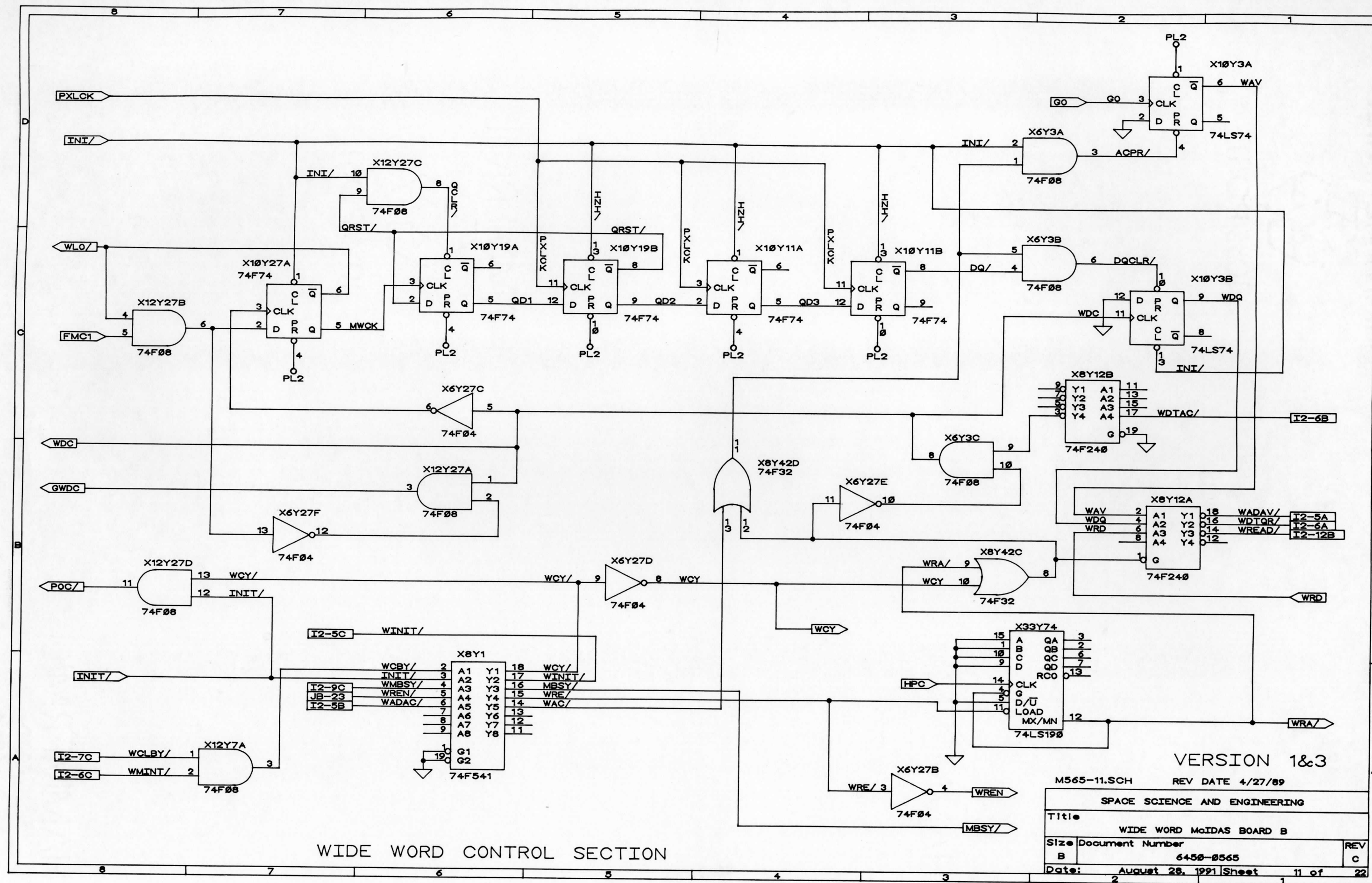
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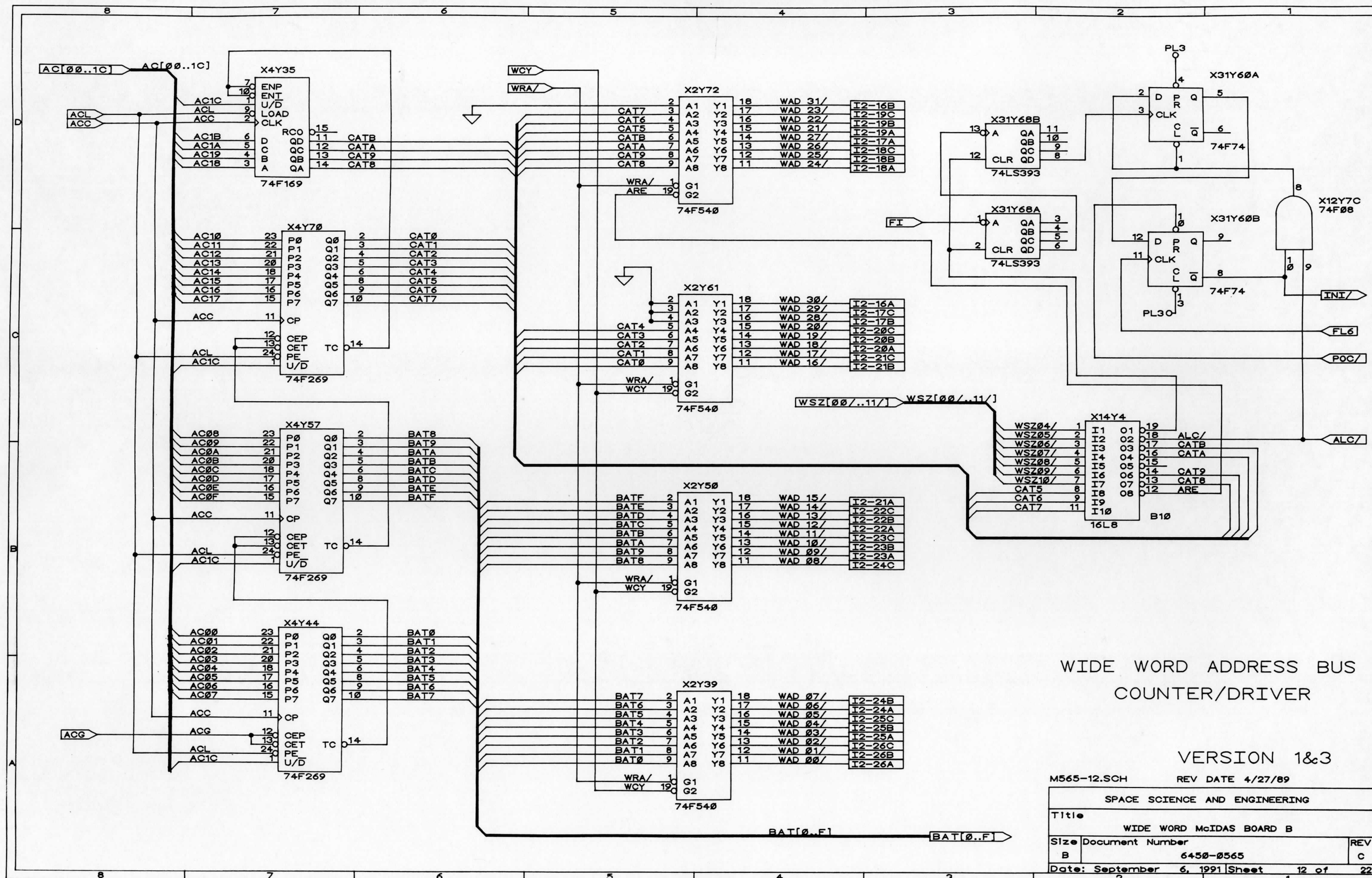
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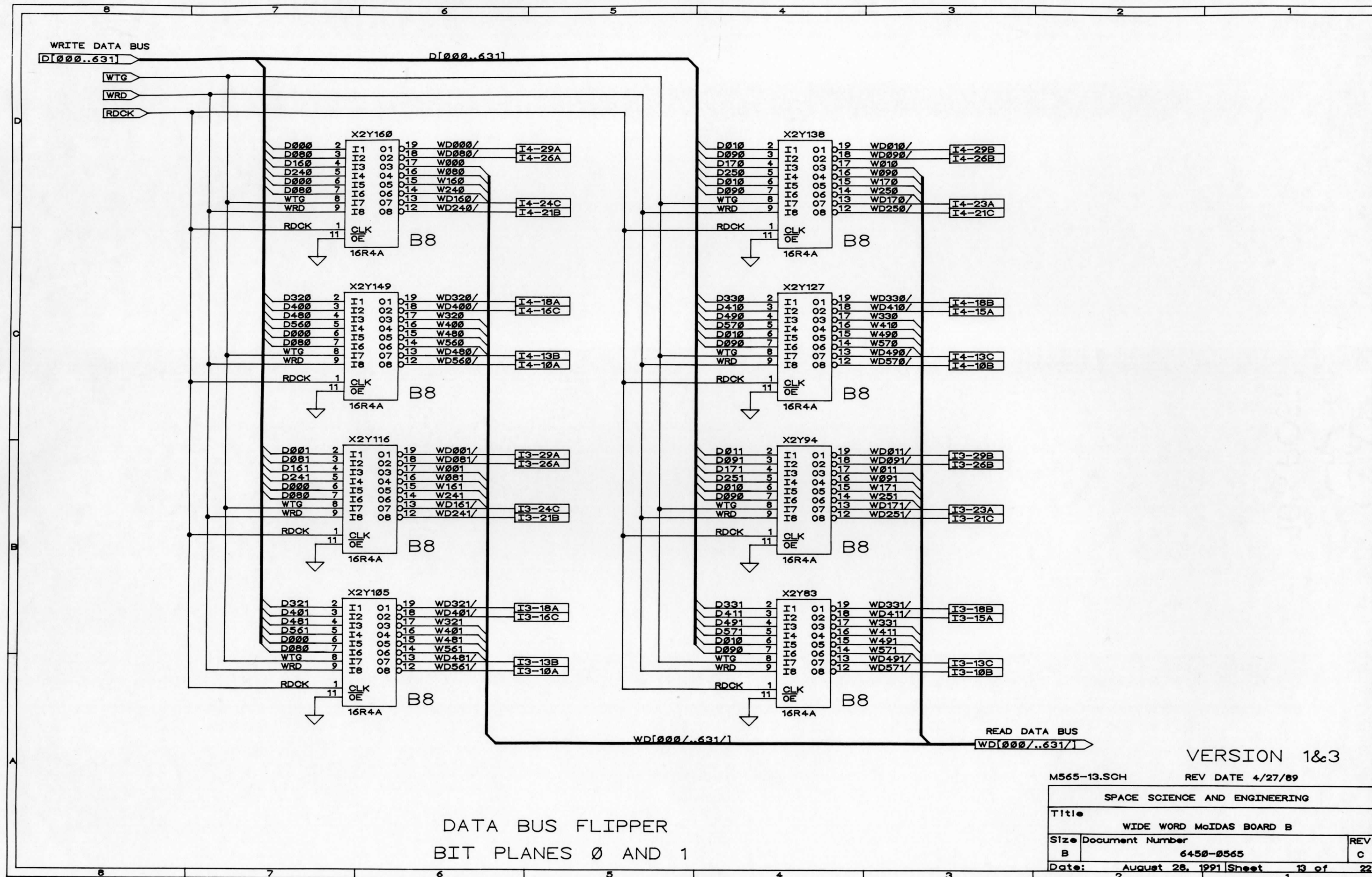


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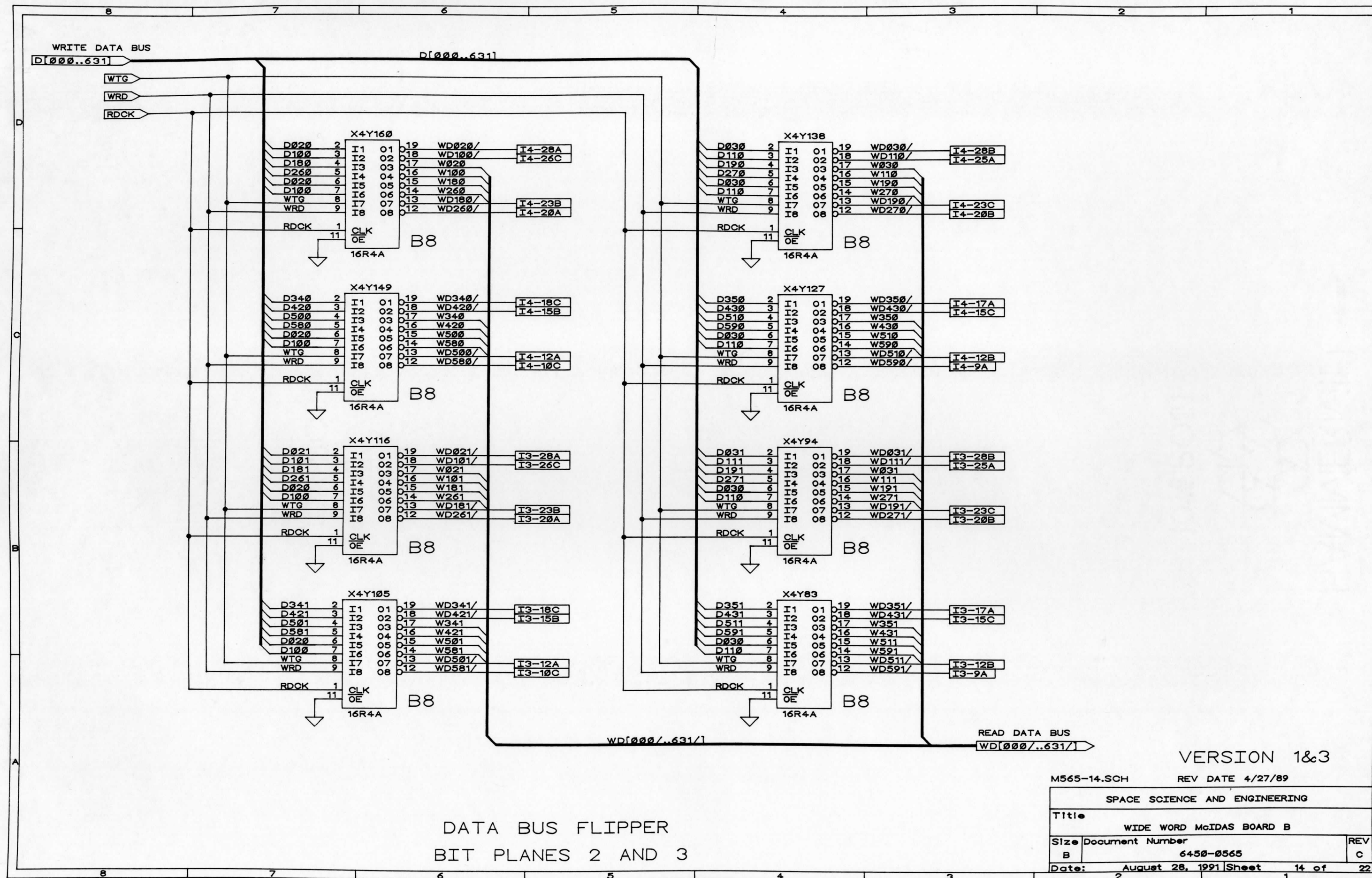
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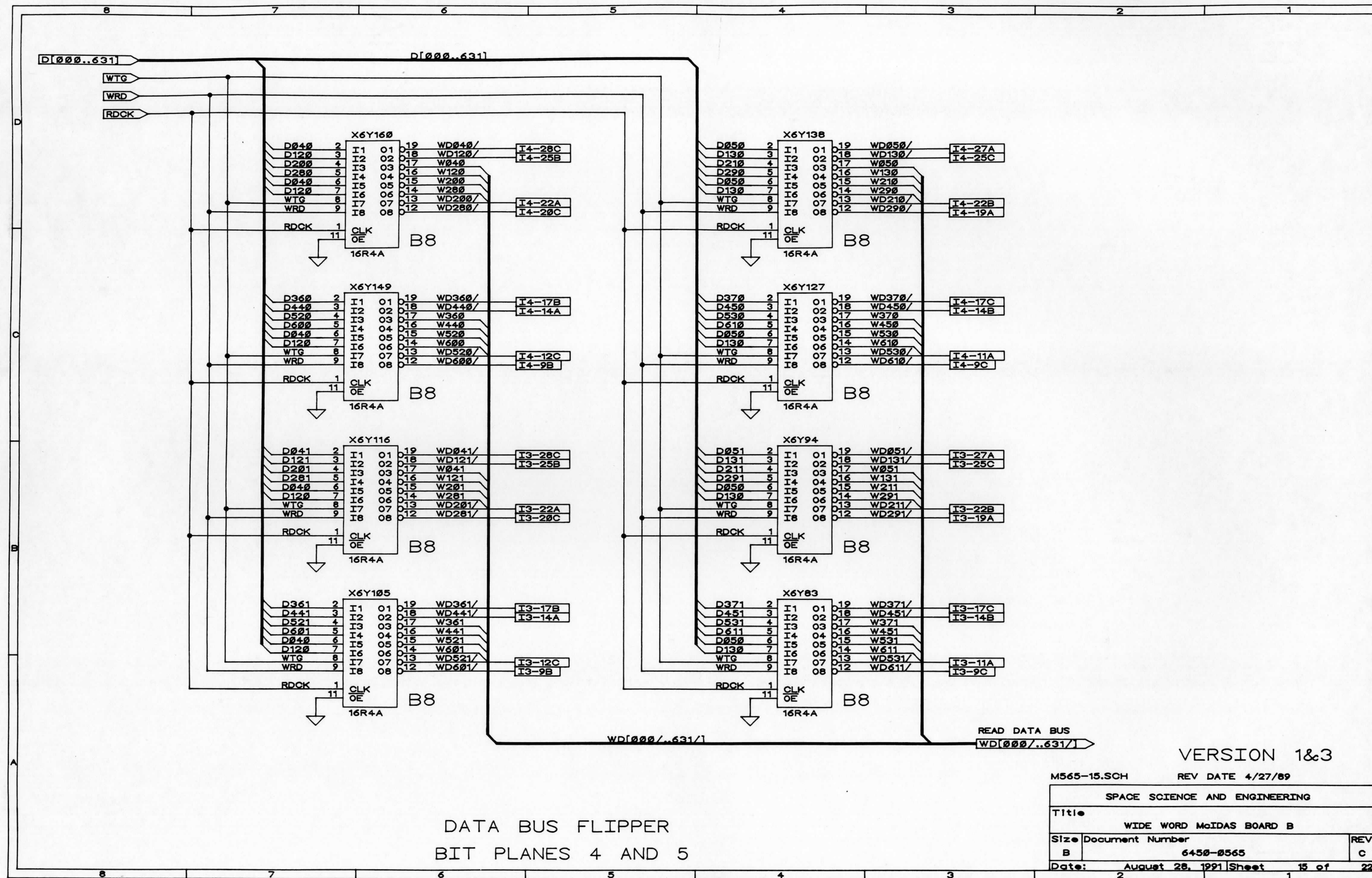
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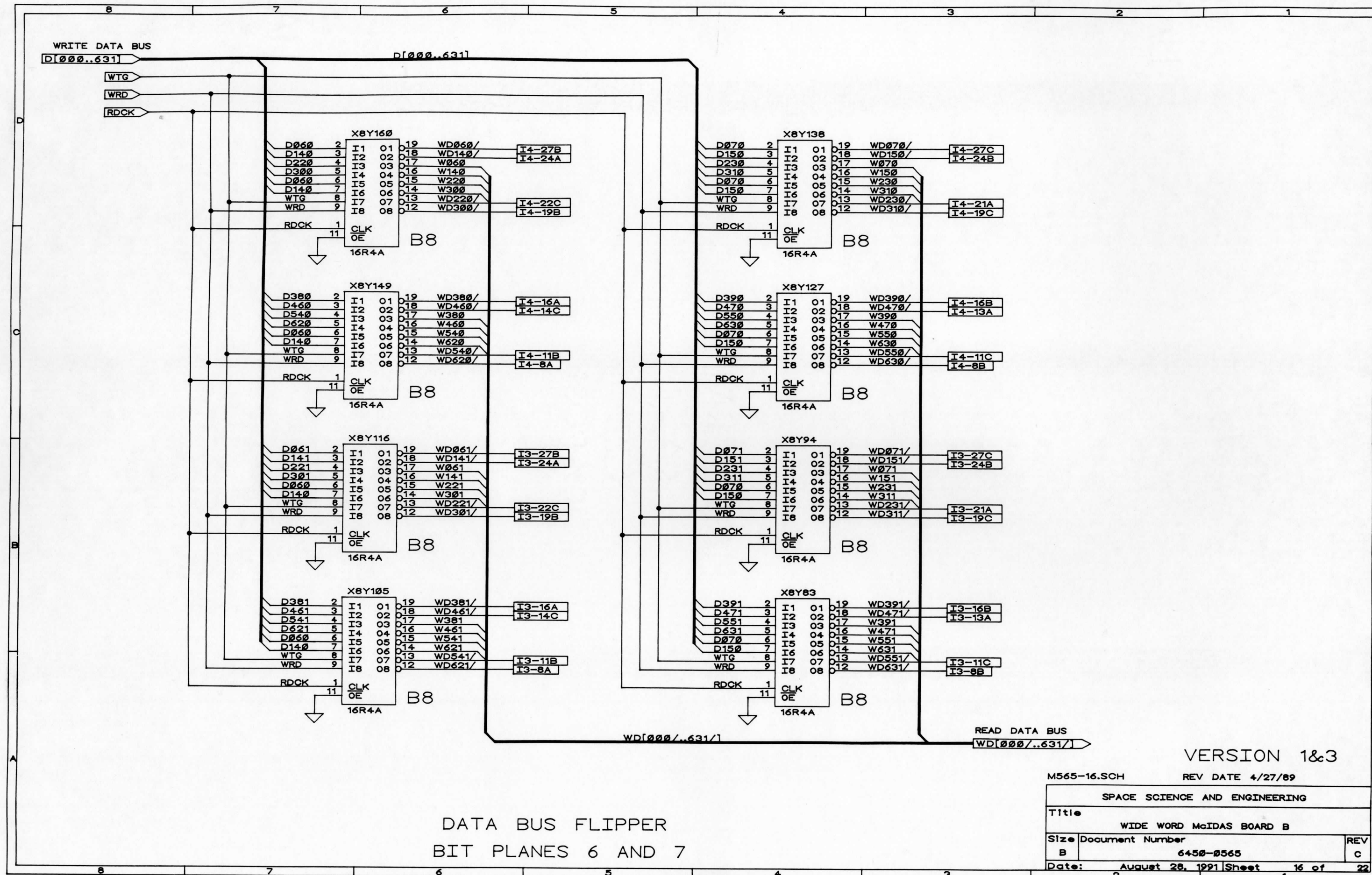
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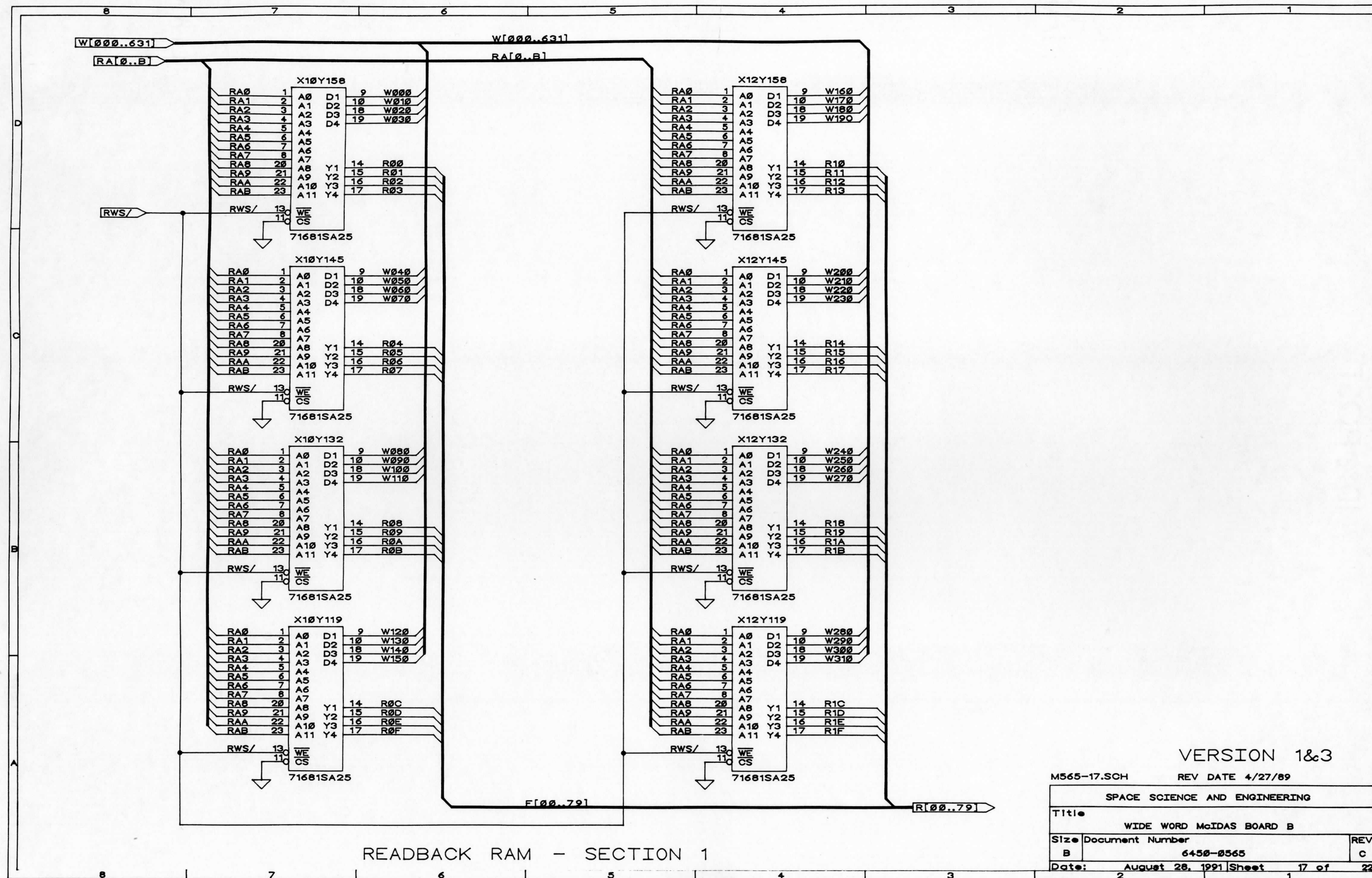
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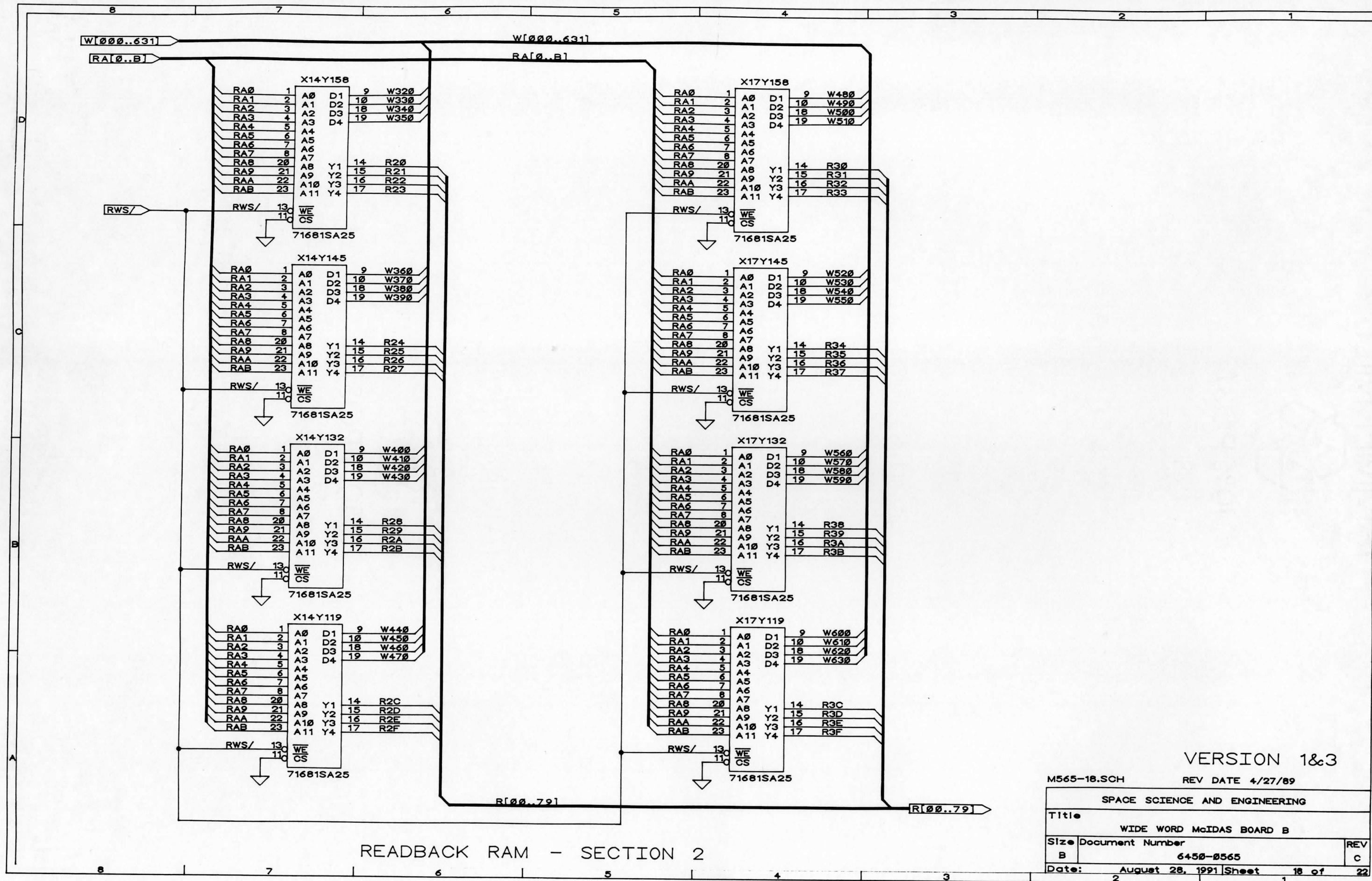
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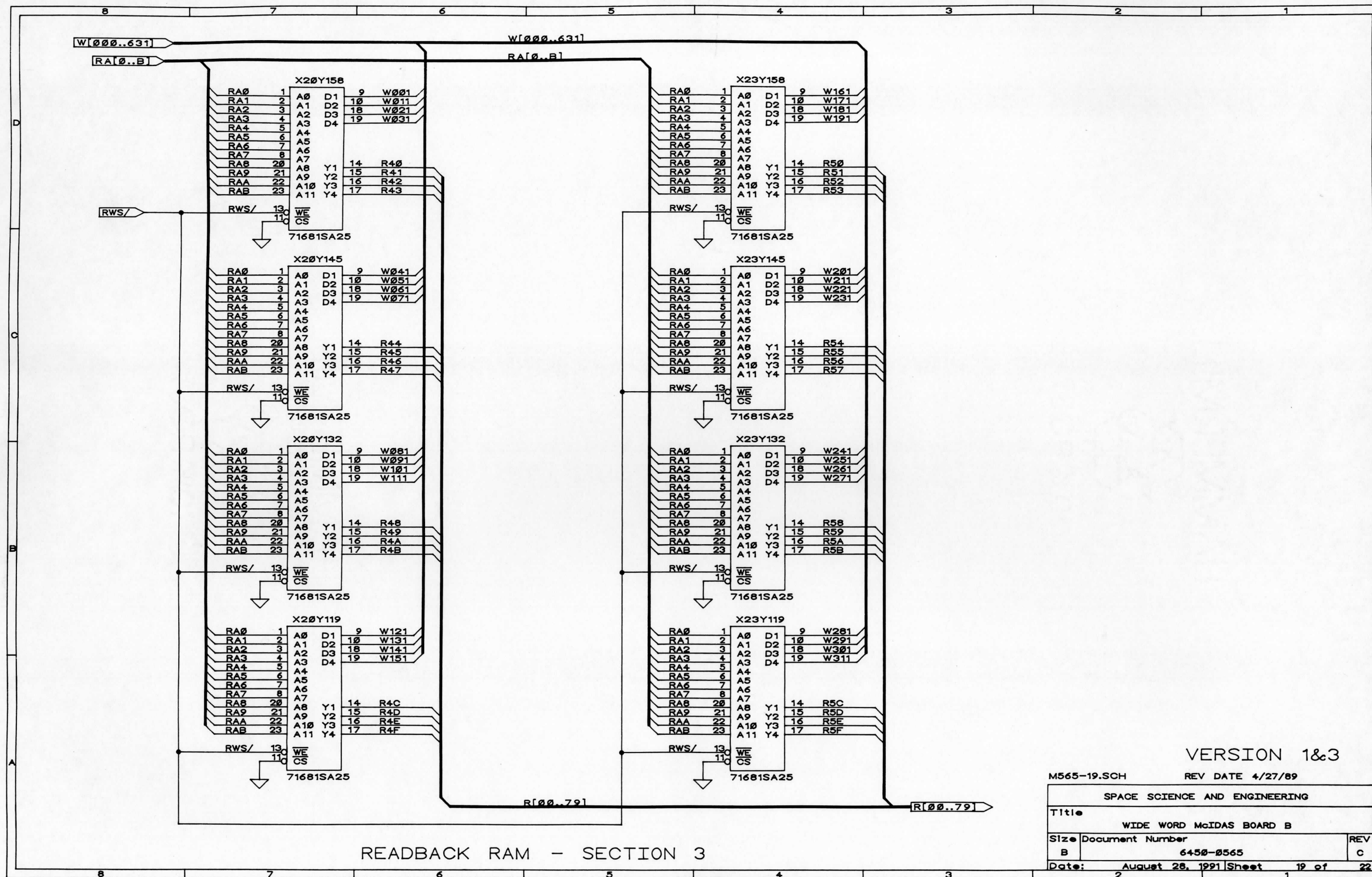
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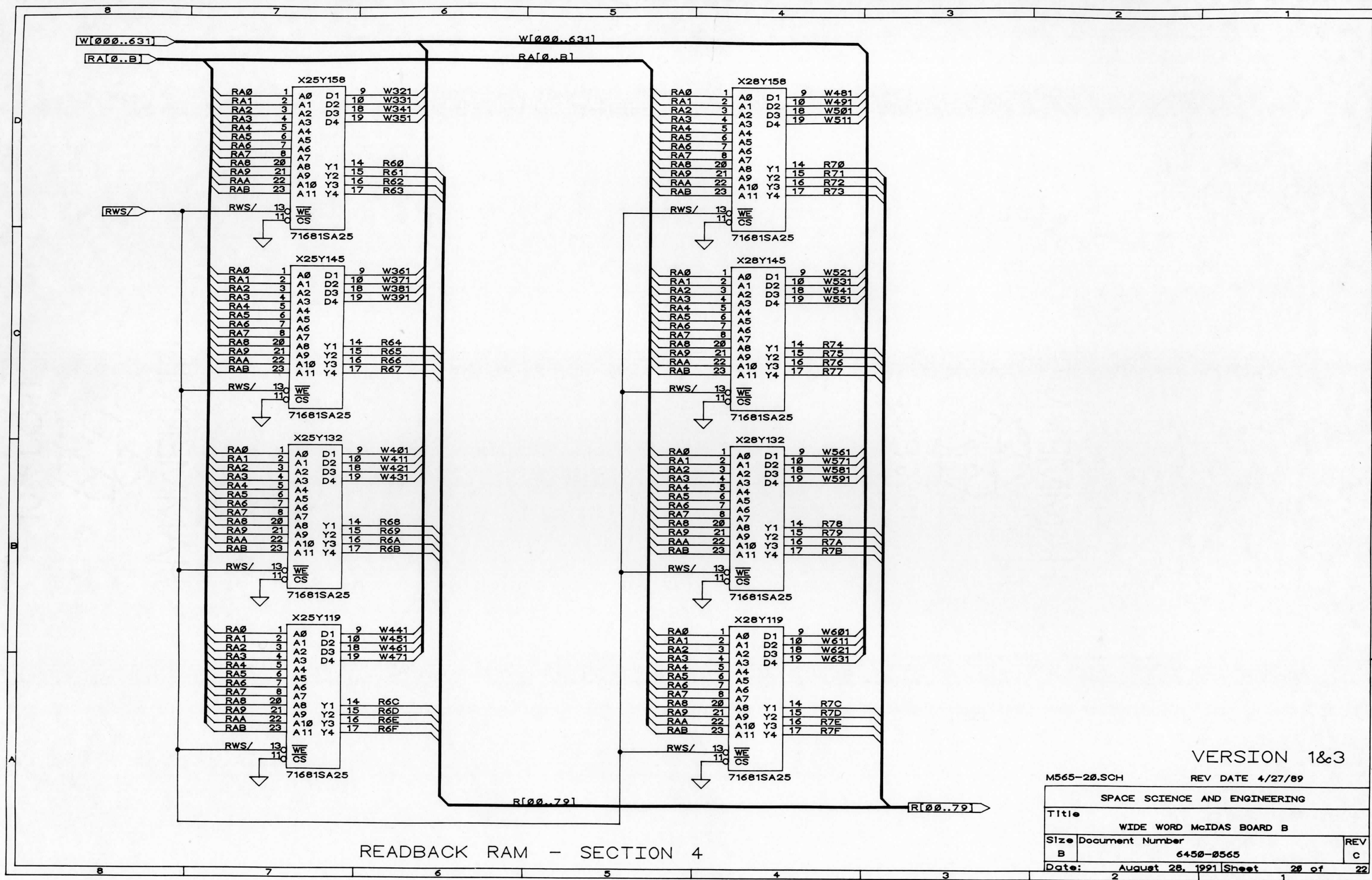
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READBACK RAM - SECTION 4

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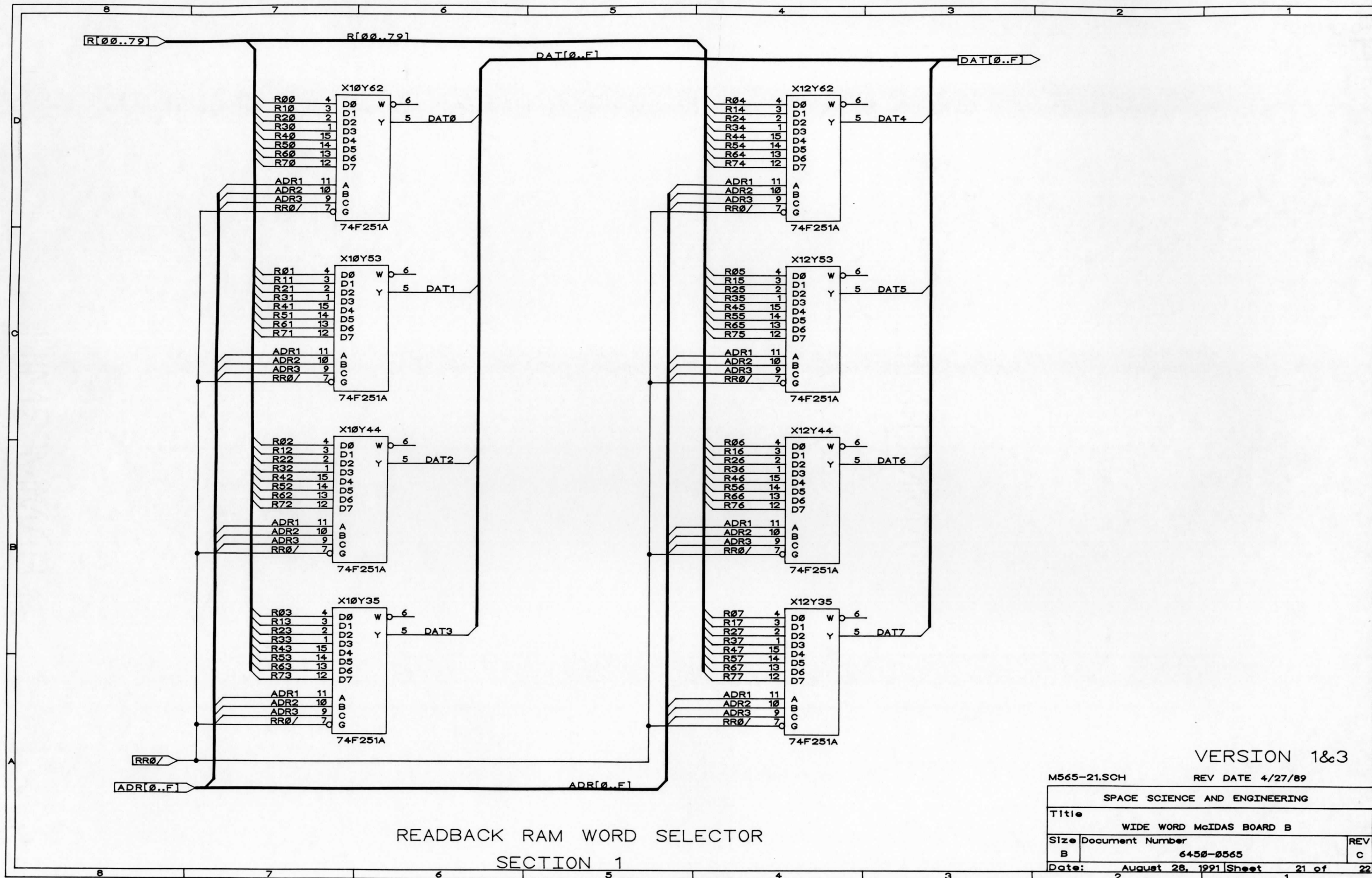
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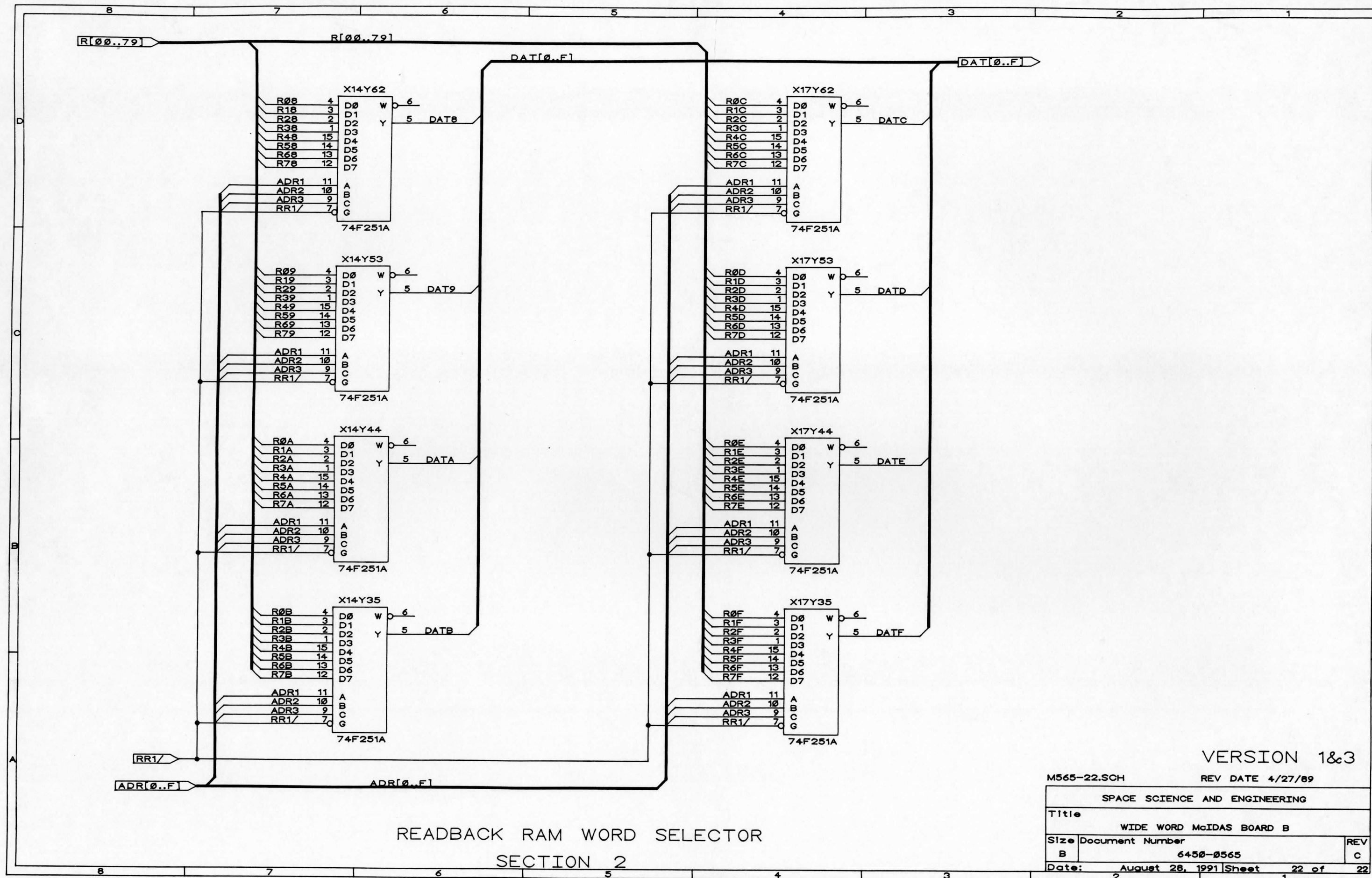
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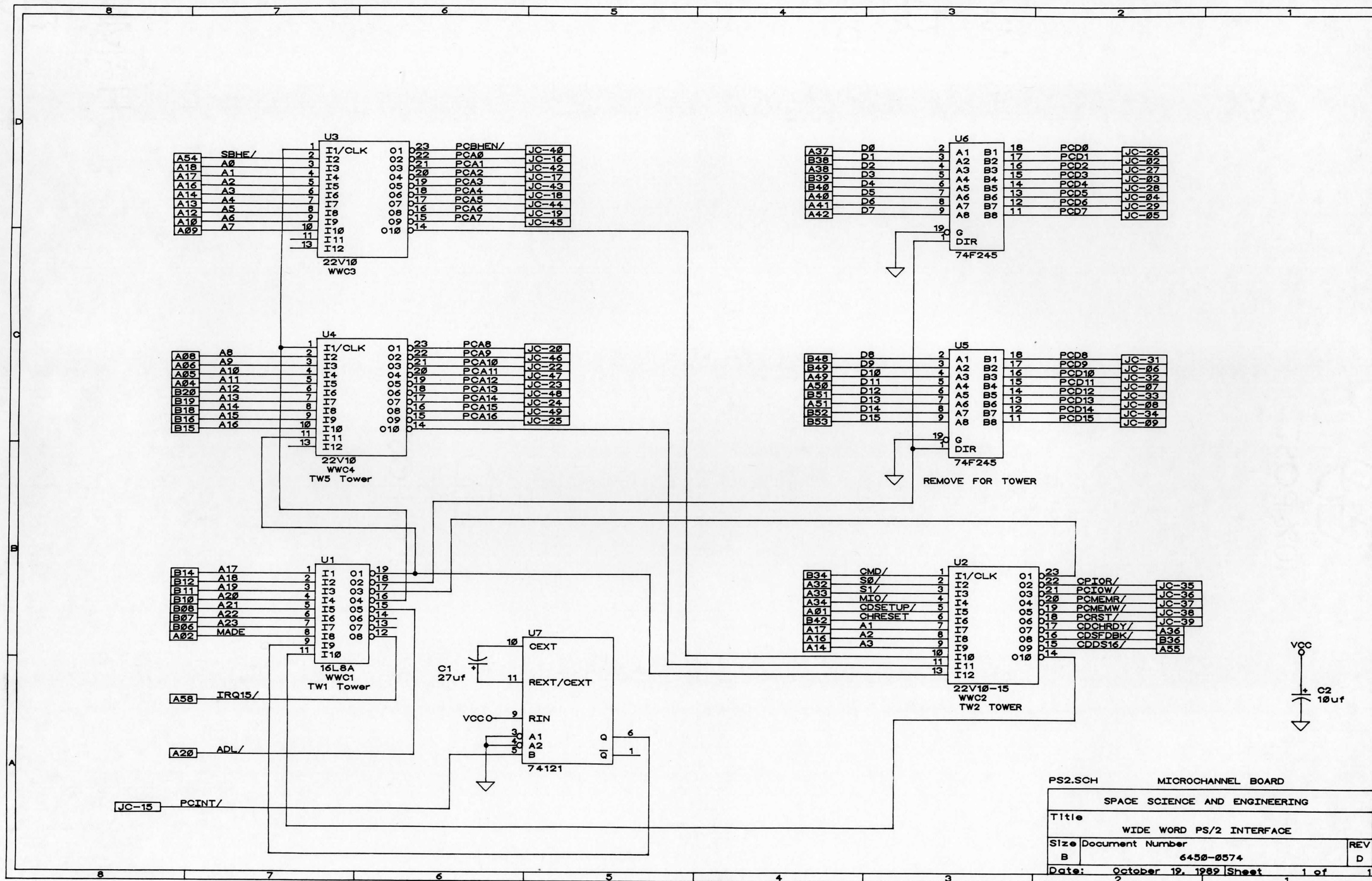
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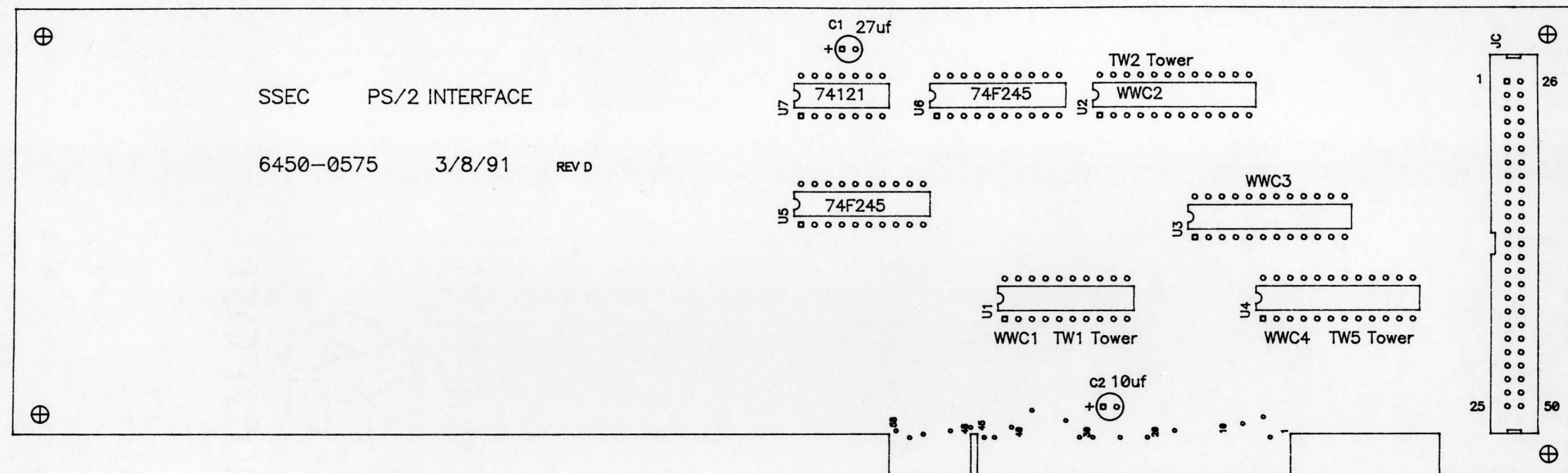
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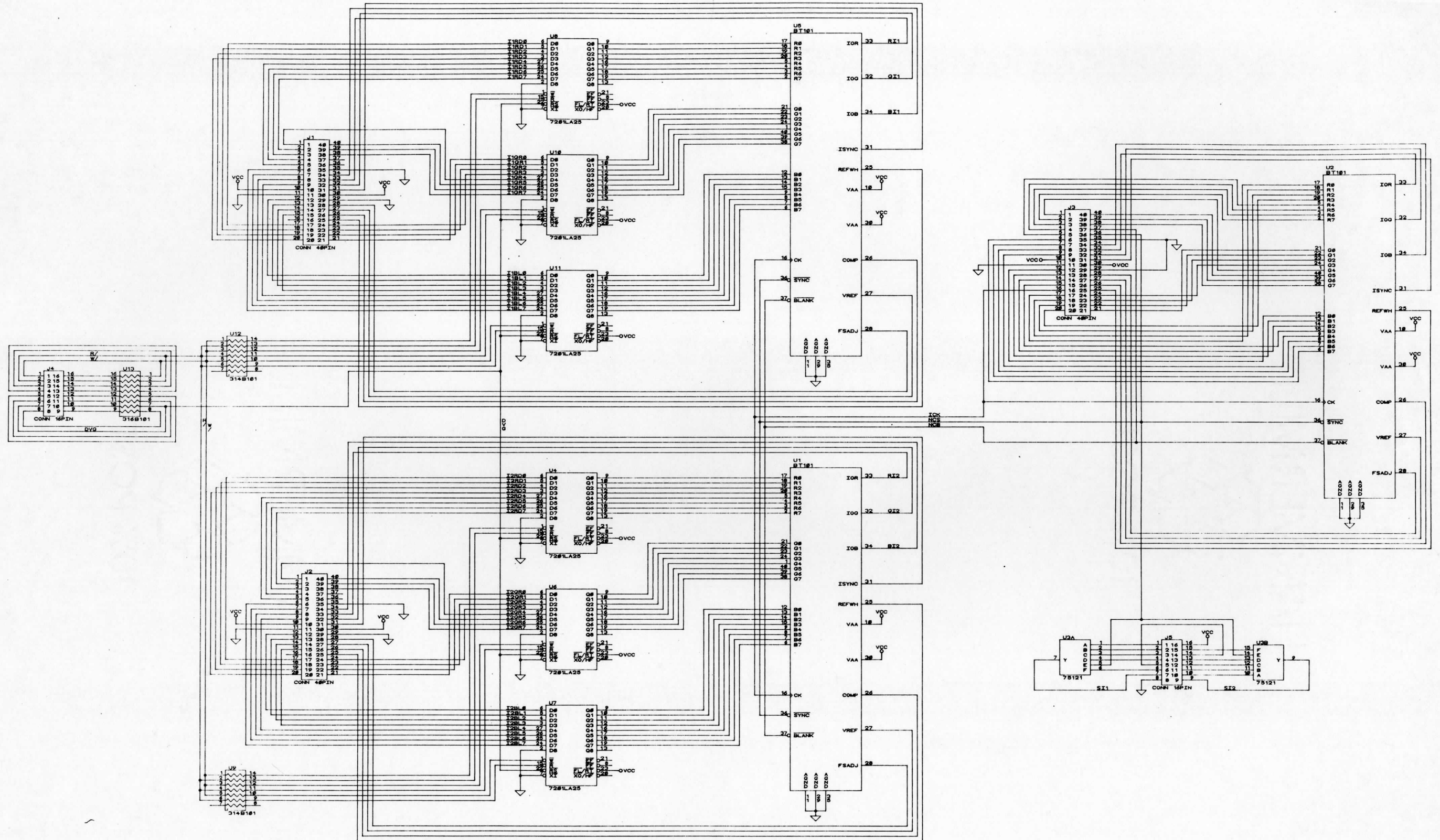
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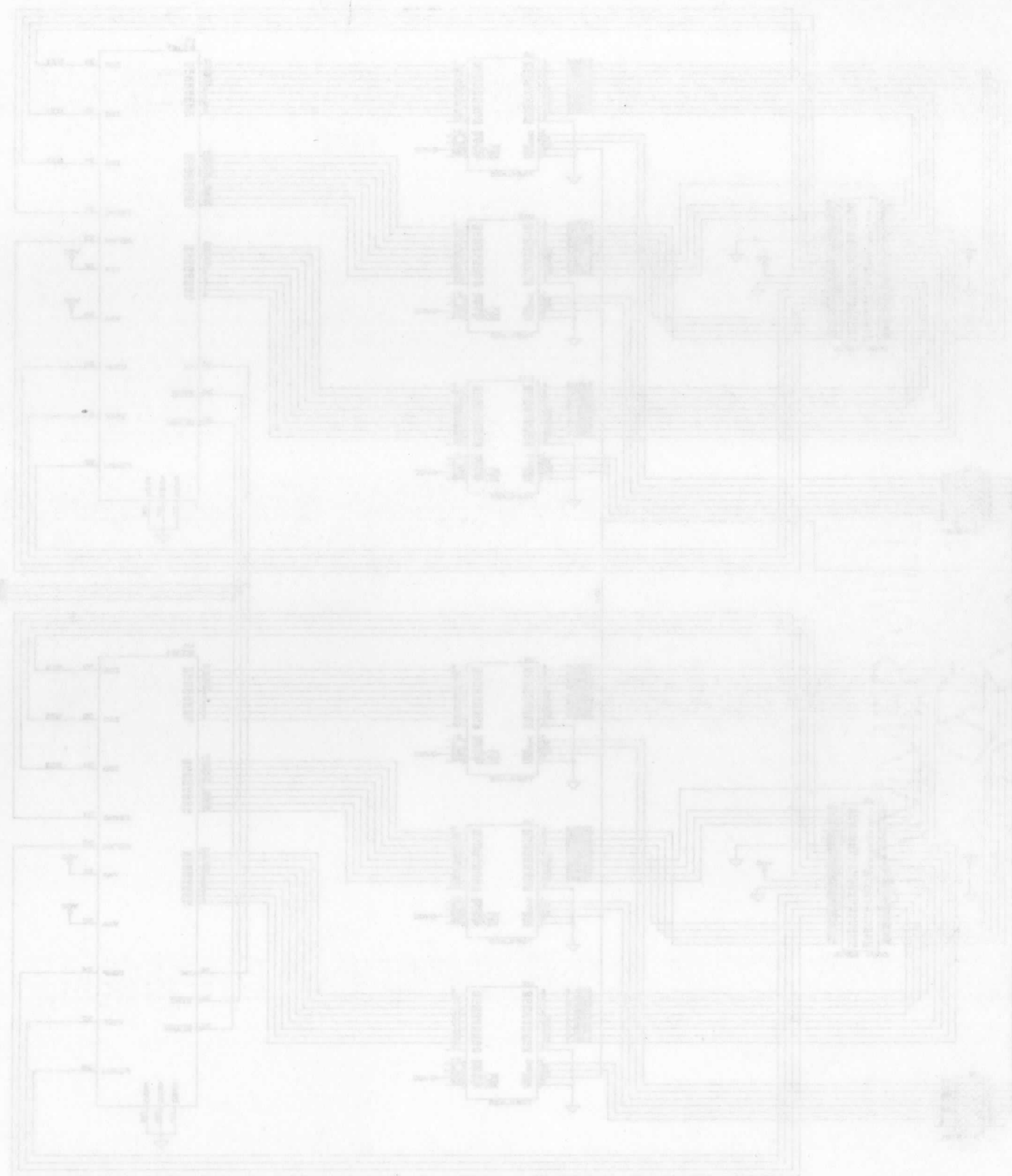


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